

# Power Analyser UMG 604

Modbus-address and  
Formulary



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# General

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You will find the addresses for the important device settings on page 105!



### Network Area Voltage Measurement

The UMG604 can conduct voltage measurements in three-phase 4-conductor systems (TT networks, TN networks) and three-phase 3-conductor systems (IT network).

### Three-Phase 4 Conductor Systems

All of the measured values listed in the table refer to a „three-phase 4 conductor system“ (TT network, TN network).

### Three-Phase 3 Conductor Systems

If a “three-phase 3 conductor system” is selected as the network form for the voltage measurement, all voltages, capacities and phase angles in the table are counted back to a star connection. The currents are not affected by this.



### Q Sign

Sign = 0 -> reactive power = 0  
Sign = +1 -> reactive power = inductive ( $Q > 0$ )  
Sign = -1 -> reactive power = capacitive ( $Q < 0$ )

# Modbus

## Modbus functions (master)

As a master, the UMG604 supports the following modbus functions;

### 01 Read Coil Status

Reads the ON/OFF status of discrete outputs (0X references, coils) in the slave. Broadcast is not supported.

### 02 Read Input Status

Reads the ON/OFF status of discrete inputs (0X references) in the slave. Broadcast is not supported.

### 03 Read Holding Registers

Reads the binary contents of holding registers (4X references) in the slave.

### 04 Read Input Registers

Reads the binary contents of input registers (3X references) in the slave.

### 05 Force Single Coil

Forces a single coil (0X references) to either ON or OFF. When broadcast, the function forces the same coil reference in all attached slaves.

### 06 Preset Single Register

Presets a value into a single holding register (4X reference). When broadcast, the function presets the same register reference in all attached slaves.

### 15 (0F Hex) Force Multiple Coils

Forces each coil (0X references) in a sequence of coils to either ON or OFF. When broadcast, the function forces the same coil reference in all attached slaves.

### 16 (10Hex) Preset Multiple Registers

Presets values into a sequence of holding registers (4X references). When broadcast, the function presets the same register references in all attached slaves.

### 23 (17Hex) Read/Write 4X Registers

Performs a combination of one read and one write operation in a single Modbus transaction. The function can write new contents to a group of 4XXXX registers, and then return the contents of another group of 4XXXX registers. Broadcast is not supported.

## Modbus Functions (Slave)

As a slave, the UMG604 supports the following modbus functions:

### 03 Read Holding Registers

Reads the binary contents of holding registers (4X references) in the slave.

### 04 Read Input Registers

Reads the binary contents of input registers (3X references) in the slave.

### 06 Preset Single Register

Presets a value into a single holding register (4X reference). When broadcast, the function presets the same register reference in all attached slaves.

### 16 (10Hex) Preset Multiple Registers

Presets values into a sequence of holding registers (4X references). When broadcast, the function presets the same register references in all attached slaves.

### 23 (17Hex) Read/Write 4X Registers

Performs a combination of one read and one write operation in a single Modbus transaction. The function can write new contents to a group of 4XXXX registers, and then return the contents of another group of 4XXXX registers. Broadcast is not supported.

## Transfer parameters

The UMG604 supports the following transfer parameters:

Baud rate	: 9.6kbps, 19.2kbps, 38.4kbps, 57.6kbps, 115.2 kbps and 921.6 kbps
Data bits	: 8
Parity	: none
Stop bits (UMG604)	: 2
Stop bits external	: 1 or 2

## Byte sequence

The data in the modbus address list can be called up in the

- Big-Endian (high-Byte before low-Byte) and in the
- Little-Endian (low-byte before high-byte)

format.

The addresses described in this address list supply the data in the „Big-Endian“ format.

If you require the data in the „Little-Endian“ format, you must add the value 32768 to the address.

## Update rate

The modbus register addresses are updated every 200ms.

## Number formats

Type	Size	Minimum	Maximum
char	8 bit	0	255
byte	8 bit	-128	127
short	16 bit	$-2^{15}$	$2^{15} - 1$
int	32 bit	$-2^{31}$	$2^{31} - 1$
uint	32 bit	0	$2^{32} - 1$
long64	64 bit	$-2^{63}$	$2^{63} - 1$
float	32 bit	IEEE 754	IEEE 754
double	64 bit	IEEE 754	IEEE 754

# Explanations of the measured values

## Measured value

- A measured value (in the UMG604) is a effective value which is formed over a period (measuring window) of 200ms.
- A measuring window is 10 periods in the 50Hz network and 12 periods in the 60Hz network.
- A measuring window has a start time and an end time.
- The resolution between the start time and end time is approximately 2ns.
- The accuracy of the start time and end time depends on the accuracy of the internal clock.  
(Typically +- 1 minute/month)
- In order to improve the accuracy of the internal clock, it is recommended that the clock in the device is compared with a time service and reset.

## Mean value of measured value

- For each measured value, a sliding mean value is calculated over the selected averaging time.
- The mean value is calculated every 200ms.
- You can take the possible averaging times from the table.

n	Mean time / seconds
0	5
1	10
2	15
3	30
4	60
5	300
6	480
7	600
8	900

## Max. value of measured value

- The *max. value of the measured value* is the largest measured value which has occurred since the last deletion.

## Min. value of measured value

- The *min. value of the measured value* is the lowest measured value which has occurred since the last deletion.

## Max. value of mean value

- The *max. value of the mean value* is the largest mean value which has occurred since the last deletion.

## Nominal current, voltage, frequency

- The limit values for events and transients are set by the nominal value in percentage.

## Nominal current $I_{\text{rated}}$

- The  $I_{\text{rated}}$  is the nominal current of the transformers and is required for calculation of the K-factor.

## Peak value negative

- Highest negative sampling value from the last 200ms measuring window

## Peak value positive

- Highest positive sampling value from the last 200ms measuring window.

## Crest factor

- The crest factor describes the relation between the peak value and effective value of a periodic quantity. It serves as a characteristic value for general description of the curve form of a periodic quantity. The distortion factor is another example of a quantity for characterization of the difference from the pure sinusoidal form.

- Example

*A sinusoidal change voltage with an effective value of 230 V has a peak value of approx. 325 V.  
The crest factor is then  $325 \text{ V} / 230 \text{ V} = 1.414$ .*

Effective value of the current for phase conductor p

$$I_p = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} i_{pk}^2}$$

Effective value of neutral conductor current

$$I_N = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} (i_{1k} + i_{2k} + i_{3k})^2}$$

Effective voltage L-N

$$U_{pN} = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} u_{pNk}^2}$$

Effective voltage L-L

$$U_{pg} = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} (u_{gNk} - u_{pNk})^2}$$

Star connection voltage (vectorial)

$$U_{\text{Sternpunktspannung}} = U_{1_{\text{rms}}} + U_{2_{\text{rms}}} + U_{3_{\text{rms}}}$$

Real power for phase conductor

$$P_p = \frac{1}{N} \cdot \sum_{k=0}^{N-1} (u_{pNk} \times i_{pk})$$

Apparent power for phase conductor

- Unsigned

$$S_p = U_{pN} \cdot I_p$$

Total apparent power (arithmetic)

- Unsigned

$$S_A = S_1 + S_2 + S_3$$

## Order number of harmonics

xxx[0] = mains frequency (50Hz/60Hz)  
 xxx[1] = 2nd harmonic (100Hz/120Hz)  
 xxx[2] = 3rd harmonic (150Hz/180Hz)  
 etc.

## THD

- THD (Total Harmonic Distortion) is the distortion factor and provides the relation of the harmonic parts of an oscillation to the mains frequency.

## Distortion factor for the voltage

- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)
- fund corresponds to n=1

$$THD_U = \frac{1}{|U_{fund}|} \sqrt{\sum_{n=2}^M |U_{n.Harm}|^2}$$

## Distortion factor for the current

- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)
- fund corresponds to n=1

$$THD_I = \frac{1}{|I_{fund}|} \sqrt{\sum_{n=2}^M |I_{n.Harm}|^2}$$

## THD

- THD for the interharmonics.
- Is calculated in the product series and UMG511 UMG605.

## Interharmonics

- Sinusoidal oscillations, which frequencies are not a multiple integer of the mains frequency.
- Is calculated in the product series and UMG511 UMG605.
- Calculation and measurement methods in accordance with the DIN EN 61000-4-30.
- The order number of interharmonics corresponds to the order number of the next smallest harmonic. For example, between the 3rd and 4th harmonic of the 3rd interharmonics.

## TDD (I)

- TDD Total demand distortion, harmonic current distortion in % of maximum demand load current
- IL = Maximum demand load current
- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)

$$TDD = \frac{1}{I_L} \sqrt{\sum_{n=2}^M I_n^2} \times 100\%$$

## Ripple control signal U (EN61000-4-30)

The ripple control signal U is a voltage (200ms measured value) which is measured at a carrier frequency specified by the user. Only frequencies beneath 3kHz are observed.

## Ripple control signal I

The ripple control signal I is a current (200ms measured value) which is measured at a carrier frequency specified by the user. Only frequencies beneath 3kHz are observed.



## Positive sequence-negative sequence-zero sequence

- The extent of a voltage or current imbalance in a three-phase system is identified using the positive sequence, negative sequence and zero sequence components.
- The balance of the rotation current system strived for in normal operation is disturbed by the unsymmetrical loads, errors and equipment.
- A three-phase system is called symmetric, when the three phase conductor voltages and currents are the same size and are displaced against each other by 120°. If one or both conditions are not fulfilled, the system is described as unsymmetrical. By calculating the symmetrical components consisting of the positive sequence, negative sequence and zero sequence, the simplified analysis of an imbalanced error is possible in a rotary current system..
- Imbalance is a feature of the network quality for the limits specified in international norms (EN 50160 for example).

### Positive sequence

$$U_{Mit} = \frac{1}{3} \left| U_{L1,fund} + U_{L2,fund} \cdot e^{j\frac{2\pi}{3}} + U_{L3,fund} \cdot e^{j\frac{4\pi}{3}} \right|$$

### Negative sequence

$$U_{Geg} = \frac{1}{3} \left| U_{L1,fund} + U_{L2,fund} \cdot e^{-j\frac{2\pi}{3}} + U_{L3,fund} \cdot e^{-j\frac{4\pi}{3}} \right|$$

### Zero sequence

$$U_{Nullsystem} = \frac{1}{3} \left| U_{L1,fund} + U_{L2,fund} + U_{L3,fund} \right|$$

A zero component can only occur if a sum current can flow back through the main conductor.

### Voltage imbalance

$$Unsymmetrie = \frac{U_{Geg}}{U_{Mit}}$$

### Under difference U (EN61000-4-30)

$$U_{unter} = \frac{U_{din} - \sqrt{\frac{\sum_{i=1}^n U_{rms-unter,i}^2}{n}}}{U_{din}} [\%]$$

### Under difference I

$$I_{unter} = \frac{I_{Nennstrom} - \sqrt{\frac{\sum_{i=1}^n I_{rms-unter,i}^2}{n}}}{I_{Nennstrom}} [\%]$$

### K-factor

- The K-factor describes the increase of the eddy current losses when loaded with harmonics. For a sinusoidal load on the transformer, the K-factor =1. The larger the K-factor, the heavier a transformer can be loaded with harmonics without overheating.

### Power Factor (vectorial) - Lambda

- The power factor is unsigned.

$$PF_A = \frac{|P|}{S_A}$$

### CosPhi - Fundamental Power Factor

- Only the mains frequency part is used for calculation of the cosphi.
- CosPhi sign:
  - = for the supply of real power
  - + = for obtaining real power

$$PF_1 = \cos(\varphi) = \frac{P_1}{S_1}$$

### CosPhi total

- CosPhi sign:
  - = for the supply of real power
  - + = for obtaining real power

$$\cos(\varphi)_{Sum_3} = \frac{P_{1_{fund}} + P_{2_{fund}} + P_{3_{fund}}}{\sqrt{(P_{1_{fund}} + P_{2_{fund}} + P_{3_{fund}})^2 + (Q_{1_{fund}} + Q_{2_{fund}} + Q_{3_{fund}})^2}}$$

$$\cos(\varphi)_{Sum_4} = \frac{P_{1_{fund}} + P_{2_{fund}} + P_{3_{fund}} + P_{4_{fund}}}{\sqrt{(P_{1_{fund}} + P_{2_{fund}} + P_{3_{fund}} + P_{4_{fund}})^2 + (Q_{1_{fund}} + Q_{2_{fund}} + Q_{3_{fund}} + Q_{4_{fund}})^2}}$$

### Phase Angle Phi

- The phase angle between current and voltage of the external conductor p is calculated according to DIN EN 61557-12 and displayed.
- The sign of the phase angle corresponding to the sign of the reactive power.

## Mains frequency power factor

The mains frequency power factor is the power factor of the mains frequency and is calculated using the fourier analysis (FFT). The voltage and current must not be sinusoidal. All in the device calculated reactive power are resulting of fundamental reactive power.

### Power factor sign

- Sign  $Q = +1$  for  $\varphi_p$  in the range  $0^\circ \dots 180^\circ$  (inductive)
- Sign  $Q = -1$  for  $\varphi_p$  in the range  $180^\circ \dots 360^\circ$  (capacitive)

$$\text{Vorzeichen } Q(\varphi_p) = +1 \text{ falls } \varphi_p \in [0^\circ - 180^\circ]$$

$$\text{Vorzeichen } Q(\varphi_p) = -1 \text{ falls } \varphi_p \in [180^\circ - 360^\circ]$$

### Reactive power for phase conductor p

- Reactive power of the mains frequency.

$$Q_{fundp} = \text{Vorzeichen } Q(\varphi_p) \cdot \sqrt{S_{fundp}^2 - P_{fundp}^2}$$

### Total reactive power

- Reactive power of the mains frequency.

$$Q_V = Q_1 + Q_2 + Q_3$$

### Distortion power factor

- The distortion power factor is the power factor of all mains frequencies and is calculated using the fourier analysis (FFT).
- The apparent power „S” contains all fundamental harmonics and all harmonic rates up to the M-th harmonic.
- The effective power „P” contains all fundamental harmonics and all harmonic rates up to the M-th harmonic.
- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)

$$D = \sqrt{S^2 - P^2 - Q_{fund}^2}$$

### Reactive energy per phase

$$E_{r_{L1}} = \int Q_{L1}(t) \cdot \Delta t$$

### Reactive energy per phase, inductive

$$E_{r(ind)_{L1}} = \int Q_{L1}(t) \cdot \Delta t \quad \text{für } Q_{L1}(t) > 0$$

### Reactive energy per phase, capacitive

$$E_{r(cap)_{L1}} = \int Q_{L1}(t) \cdot \Delta t \quad \text{für } Q_{L1}(t) < 0$$

### Reactive energy, sum L1-L3

$$E_{r_{L1,L2,L3}} = \int (Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) \cdot \Delta t$$

### Reactive energy, sum L1-L3, inductive

$$E_{r(ind)_{L1,L2,L3}} = \int (Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) \cdot \Delta t$$

für  $(Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) > 0$

### Reactive energy, sum L1-L3, capacitive

$$E_{r(cap)_{L1,L2,L3}} = \int (Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) \cdot \Delta t$$

für  $(Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) < 0$

# Address List

## Frequently required readings

Address	Format	Designation	Unit	Remarks
19000	float	_G_ULN[0]	V	Voltage L1-N
19002	float	_G_ULN[1]	V	Voltage L2-N
19004	float	_G_ULN[2]	V	Voltage L3-N
19006	float	_G_ULL[0]	V	Voltage L1-L2
19008	float	_G_ULL[1]	V	Voltage L2-L3
19010	float	_G_ULL[2]	V	Voltage L3-L1
19012	float	_G_ILN[0]	A	Apparent current, L1-N
19014	float	_G_ILN[1]	A	Apparent current, L2-N
19016	float	_G_ILN[2]	A	Apparent current, L3-N
19018	float	_G_I_SUM3	A	Vector sum; $I_N=I_1+I_2+I_3$
19020	float	_G_PLN[0]	W	Real power L1-N
19022	float	_G_PLN[1]	W	Real power L2-N
19024	float	_G_PLN[2]	W	Real power L3-N
19026	float	_G_P_SUM3	W	$P_{sum3}=P_1+P_2+P_3$
19028	float	_G_SLN[0]	VA	Apparent power L1-N
19030	float	_G_SLN[1]	VA	Apparent power L2-N
19032	float	_G_SLN[2]	VA	Apparent power L3-N
19034	float	_G_S_SUM3	VA	Sum; $S_{sum3}=S_1+S_2+S_3$
19036	float	_G_QLN[0]	var	Reactive power L1 (fundamental comp.)
19038	float	_G_QLN[1]	var	Reactive power L2 (fundamental comp.)
19040	float	_G_QLN[2]	var	Reactive power L3 (fundamental comp.)
19042	float	_G_Q_SUM3	var	$Q_{sum3}=Q_1+Q_2+Q_3$ (fundamental comp.)
19044	float	_G_COS_PHI[0]	-	CosPhi; UL1 IL1 (fundamental comp.)
19046	float	_G_COS_PHI[1]	-	CosPhi; UL2 IL2 (fundamental comp.)
19048	float	_G_COS_PHI[2]	-	CosPhi; UL3 IL3 (fundamental comp.)
19050	float	_G_FREQ	Hz	Measured frequency
19052	float	_G_PHASE_SEQ	-	Rotation field; 1=right, 0=none, -1=left
19054	float	_G_WH[0]	Wh	Real energy L1
19056	float	_G_WH[1]	Wh	Real energy L2
19058	float	_G_WH[2]	Wh	Real energy L3
19060	float	_G_WH_SUML13	Wh	Real energy L1..L3
19062	float	_G_WH_V[0]	Wh	Real energy L1, consumed
19064	float	_G_WH_V[1]	Wh	Real energy L2, consumed
19066	float	_G_WH_V[2]	Wh	Real energy L3, consumed
19068	float	_G_WH_V_HT_SUML13	Wh	Real energy L1..L3, consumed, rate 1
19070	float	_G_WH_Z[0]	Wh	Real energy L1, delivered
19072	float	_G_WH_Z[1]	Wh	Real energy L2, delivered
19074	float	_G_WH_Z[2]	Wh	Real energy L3, delivered
19076	float	_G_WH_Z_SUML13	Wh	Real energy L1..L3, delivered
19078	float	_G_WH_S[0]	VAh	Apparent energy L1
19080	float	_G_WH_S[1]	VAh	Apparent energy L2
19082	float	_G_WH_S[2]	VAh	Apparent energy L3
19084	float	_G_WH_S_SUML13	VAh	Apparent energy L1..L3
19086	float	_G_QH[0]	varh	Reactive energy L1 (fundamental comp.)
19088	float	_G_QH[1]	varh	Reactive energy L2 (fundamental comp.)
19090	float	_G_QH[2]	varh	Reactive energy L3 (fundamental comp.)
19092	float	_G_QH_SUML13	varh	Reactive energy L1..L3 (fundamental comp.)
19094	float	_G_IQH[0]	varh	Reactive energy, inductive, L1 (fundamental comp.)
19096	float	_G_IQH[1]	varh	Reactive energy, inductive, L2 (fundamental comp.)
19098	float	_G_IQH[2]	varh	Reactive energy, inductive, L3 (fundamental comp.)
19100	float	_G_IQH_SUML13	varh	Reactive energy L1..L3, ind. (fundamental comp.)
19102	float	_G_CQH[0]	varh	Reactive energy, capacitive, L1 (fundamental comp.)
19104	float	_G_CQH[1]	varh	Reactive energy, capacitive, L2 (fundamental comp.)
19106	float	_G_CQH[2]	varh	Reactive energy, capacitive, L3 (fundamental comp.)
19108	float	_G_CQH_SUML13	varh	Reactive energy L1..L3, cap. (fundamental comp.)

Address	Format	Designation	Unit	Remarks
19110	float	_G_THD_ULN[0]	%	Harmonic, THD, U L1-N
19112	float	_G_THD_ULN[1]	%	Harmonic, THD, U L2-N
19114	float	_G_THD_ULN[2]	%	Harmonic, THD, U L3-N
19116	float	_G_THD_ILN[0]	%	Harmonic, THD, I L1
19118	float	_G_THD_ILN[1]	%	Harmonic, THD, I L2
19120	float	_G_THD_ILN[2]	%	Harmonic, THD, I L3

Address	Format	Designation	Unit	Remarks
0	long64	_REALTIME	2 ns	Time (UTC)
4	int	_SYSTIME	sec	Time (UTC)
6	short	_DAY		Day (1 .. 31)
7	short	_MONTH		Month (0=Jan, .. 11=Dec)
8	short	_YEAR		Year
9	short	_HOUR	h	Hour (1 .. 24)
10	short	_MIN	min	Minute (1 .. 59)
11	short	_SEC	s	Second (1 .. 59)
12	short	_WEEKDAY		Weekday , (0=Sun .. 6=Sat)
13	float	_FFT_UL1[0]	V	1. Harmonic, UL1-N
15	float	_FFT_UL1[1]	V	2. Harmonic, UL1-N
17	float	_FFT_UL1[2]	V	3. Harmonic, UL1-N
19	float	_FFT_UL1[3]	V	4. Harmonic, UL1-N
21	float	_FFT_UL1[4]	V	5. Harmonic, UL1-N
23	float	_FFT_UL1[5]	V	6. Harmonic, UL1-N
25	float	_FFT_UL1[6]	V	7. Harmonic, UL1-N
27	float	_FFT_UL1[7]	V	8. Harmonic, UL1-N
29	float	_FFT_UL1[8]	V	9. Harmonic, UL1-N
31	float	_FFT_UL1[9]	V	10. Harmonic, UL1-N
33	float	_FFT_UL1[10]	V	11. Harmonic, UL1-N
35	float	_FFT_UL1[11]	V	12. Harmonic, UL1-N
37	float	_FFT_UL1[12]	V	13. Harmonic, UL1-N
39	float	_FFT_UL1[13]	V	14. Harmonic, UL1-N
41	float	_FFT_UL1[14]	V	15. Harmonic, UL1-N
43	float	_FFT_UL1[15]	V	16. Harmonic, UL1-N
45	float	_FFT_UL1[16]	V	17. Harmonic, UL1-N
47	float	_FFT_UL1[17]	V	18. Harmonic, UL1-N
49	float	_FFT_UL1[18]	V	19. Harmonic, UL1-N
51	float	_FFT_UL1[19]	V	20. Harmonic, UL1-N
53	float	_FFT_UL1[20]	V	21. Harmonic, UL1-N
55	float	_FFT_UL1[21]	V	22. Harmonic, UL1-N
57	float	_FFT_UL1[22]	V	23. Harmonic, UL1-N
59	float	_FFT_UL1[23]	V	24. Harmonic, UL1-N
61	float	_FFT_UL1[24]	V	25. Harmonic, UL1-N
63	float	_FFT_UL1[25]	V	26. Harmonic, UL1-N
65	float	_FFT_UL1[26]	V	27. Harmonic, UL1-N
67	float	_FFT_UL1[27]	V	28. Harmonic, UL1-N
69	float	_FFT_UL1[28]	V	29. Harmonic, UL1-N
71	float	_FFT_UL1[29]	V	30. Harmonic, UL1-N
73	float	_FFT_UL1[30]	V	31. Harmonic, UL1-N
75	float	_FFT_UL1[31]	V	32. Harmonic, UL1-N
77	float	_FFT_UL1[32]	V	33. Harmonic, UL1-N
79	float	_FFT_UL1[33]	V	34. Harmonic, UL1-N
81	float	_FFT_UL1[34]	V	35. Harmonic, UL1-N
83	float	_FFT_UL1[35]	V	36. Harmonic, UL1-N
85	float	_FFT_UL1[36]	V	37. Harmonic, UL1-N
87	float	_FFT_UL1[37]	V	38. Harmonic, UL1-N
89	float	_FFT_UL1[38]	V	39. Harmonic, UL1-N
91	float	_FFT_UL1[39]	V	40. Harmonic, UL1-N
93	float	_FFT_UL2[0]	V	
95	float	_FFT_UL2[1]	V	
97	float	_FFT_UL2[2]	V	
99	float	_FFT_UL2[3]	V	
101	float	_FFT_UL2[4]	V	
103	float	_FFT_UL2[5]	V	
105	float	_FFT_UL2[6]	V	
107	float	_FFT_UL2[7]	V	
109	float	_FFT_UL2[8]	V	
111	float	_FFT_UL2[9]	V	

Address	Format	Designation	Unit	Remarks
113	float	_FFT_UL2[10]	V	
115	float	_FFT_UL2[11]	V	
117	float	_FFT_UL2[12]	V	
119	float	_FFT_UL2[13]	V	
121	float	_FFT_UL2[14]	V	
123	float	_FFT_UL2[15]	V	
125	float	_FFT_UL2[16]	V	
127	float	_FFT_UL2[17]	V	
129	float	_FFT_UL2[18]	V	
131	float	_FFT_UL2[19]	V	
133	float	_FFT_UL2[20]	V	
135	float	_FFT_UL2[21]	V	
137	float	_FFT_UL2[22]	V	
139	float	_FFT_UL2[23]	V	
141	float	_FFT_UL2[24]	V	
143	float	_FFT_UL2[25]	V	
145	float	_FFT_UL2[26]	V	
147	float	_FFT_UL2[27]	V	
149	float	_FFT_UL2[28]	V	
151	float	_FFT_UL2[29]	V	
153	float	_FFT_UL2[30]	V	
155	float	_FFT_UL2[31]	V	
157	float	_FFT_UL2[32]	V	
159	float	_FFT_UL2[33]	V	
161	float	_FFT_UL2[34]	V	
163	float	_FFT_UL2[35]	V	
165	float	_FFT_UL2[36]	V	
167	float	_FFT_UL2[37]	V	
169	float	_FFT_UL2[38]	V	
171	float	_FFT_UL2[39]	V	
173	float	_FFT_UL3[0]	V	
175	float	_FFT_UL3[1]	V	
177	float	_FFT_UL3[2]	V	
179	float	_FFT_UL3[3]	V	
181	float	_FFT_UL3[4]	V	
183	float	_FFT_UL3[5]	V	
185	float	_FFT_UL3[6]	V	
187	float	_FFT_UL3[7]	V	
189	float	_FFT_UL3[8]	V	
191	float	_FFT_UL3[9]	V	
193	float	_FFT_UL3[10]	V	
195	float	_FFT_UL3[11]	V	
197	float	_FFT_UL3[12]	V	
199	float	_FFT_UL3[13]	V	
201	float	_FFT_UL3[14]	V	
203	float	_FFT_UL3[15]	V	
205	float	_FFT_UL3[16]	V	
207	float	_FFT_UL3[17]	V	
209	float	_FFT_UL3[18]	V	
211	float	_FFT_UL3[19]	V	
213	float	_FFT_UL3[20]	V	
215	float	_FFT_UL3[21]	V	
217	float	_FFT_UL3[22]	V	
219	float	_FFT_UL3[23]	V	
221	float	_FFT_UL3[24]	V	
223	float	_FFT_UL3[25]	V	
225	float	_FFT_UL3[26]	V	
227	float	_FFT_UL3[27]	V	
229	float	_FFT_UL3[28]	V	

Address	Format	Designation	Unit	Remarks
231	float	_FFT_UL3[29]	V	
233	float	_FFT_UL3[30]	V	
235	float	_FFT_UL3[31]	V	
237	float	_FFT_UL3[32]	V	
239	float	_FFT_UL3[33]	V	
241	float	_FFT_UL3[34]	V	
243	float	_FFT_UL3[35]	V	
245	float	_FFT_UL3[36]	V	
247	float	_FFT_UL3[37]	V	
249	float	_FFT_UL3[38]	V	
251	float	_FFT_UL3[39]	V	
253	float	_FFT_UL4[0]	V	
255	float	_FFT_UL4[1]	V	
257	float	_FFT_UL4[2]	V	
259	float	_FFT_UL4[3]	V	
261	float	_FFT_UL4[4]	V	
263	float	_FFT_UL4[5]	V	
265	float	_FFT_UL4[6]	V	
267	float	_FFT_UL4[7]	V	
269	float	_FFT_UL4[8]	V	
271	float	_FFT_UL4[9]	V	
273	float	_FFT_UL4[10]	V	
275	float	_FFT_UL4[11]	V	
277	float	_FFT_UL4[12]	V	
279	float	_FFT_UL4[13]	V	
281	float	_FFT_UL4[14]	V	
283	float	_FFT_UL4[15]	V	
285	float	_FFT_UL4[16]	V	
287	float	_FFT_UL4[17]	V	
289	float	_FFT_UL4[18]	V	
291	float	_FFT_UL4[19]	V	
293	float	_FFT_UL4[20]	V	
295	float	_FFT_UL4[21]	V	
297	float	_FFT_UL4[22]	V	
299	float	_FFT_UL4[23]	V	
301	float	_FFT_UL4[24]	V	
303	float	_FFT_UL4[25]	V	
305	float	_FFT_UL4[26]	V	
307	float	_FFT_UL4[27]	V	
309	float	_FFT_UL4[28]	V	
311	float	_FFT_UL4[29]	V	
313	float	_FFT_UL4[30]	V	
315	float	_FFT_UL4[31]	V	
317	float	_FFT_UL4[32]	V	
319	float	_FFT_UL4[33]	V	
321	float	_FFT_UL4[34]	V	
323	float	_FFT_UL4[35]	V	
325	float	_FFT_UL4[36]	V	
327	float	_FFT_UL4[37]	V	
329	float	_FFT_UL4[38]	V	
331	float	_FFT_UL4[39]	V	
333	float	_FFT_IL1[0]	A	
335	float	_FFT_IL1[1]	A	
337	float	_FFT_IL1[2]	A	
339	float	_FFT_IL1[3]	A	
341	float	_FFT_IL1[4]	A	
343	float	_FFT_IL1[5]	A	
345	float	_FFT_IL1[6]	A	
347	float	_FFT_IL1[7]	A	



Address	Format	Designation	Unit	Remarks
349	float	_FFT_IL1[8]	A	
351	float	_FFT_IL1[9]	A	
353	float	_FFT_IL1[10]	A	
355	float	_FFT_IL1[11]	A	
357	float	_FFT_IL1[12]	A	
359	float	_FFT_IL1[13]	A	
361	float	_FFT_IL1[14]	A	
363	float	_FFT_IL1[15]	A	
365	float	_FFT_IL1[16]	A	
367	float	_FFT_IL1[17]	A	
369	float	_FFT_IL1[18]	A	
371	float	_FFT_IL1[19]	A	
373	float	_FFT_IL1[20]	A	
375	float	_FFT_IL1[21]	A	
377	float	_FFT_IL1[22]	A	
379	float	_FFT_IL1[23]	A	
381	float	_FFT_IL1[24]	A	
383	float	_FFT_IL1[25]	A	
385	float	_FFT_IL1[26]	A	
387	float	_FFT_IL1[27]	A	
389	float	_FFT_IL1[28]	A	
391	float	_FFT_IL1[29]	A	
393	float	_FFT_IL1[30]	A	
395	float	_FFT_IL1[31]	A	
397	float	_FFT_IL1[32]	A	
399	float	_FFT_IL1[33]	A	
401	float	_FFT_IL1[34]	A	
403	float	_FFT_IL1[35]	A	
405	float	_FFT_IL1[36]	A	
407	float	_FFT_IL1[37]	A	
409	float	_FFT_IL1[38]	A	
411	float	_FFT_IL1[39]	A	
413	float	_FFT_IL2[0]	A	
415	float	_FFT_IL2[1]	A	
417	float	_FFT_IL2[2]	A	
419	float	_FFT_IL2[3]	A	
421	float	_FFT_IL2[4]	A	
423	float	_FFT_IL2[5]	A	
425	float	_FFT_IL2[6]	A	
427	float	_FFT_IL2[7]	A	
429	float	_FFT_IL2[8]	A	
431	float	_FFT_IL2[9]	A	
433	float	_FFT_IL2[10]	A	
435	float	_FFT_IL2[11]	A	
437	float	_FFT_IL2[12]	A	
439	float	_FFT_IL2[13]	A	
441	float	_FFT_IL2[14]	A	
443	float	_FFT_IL2[15]	A	
445	float	_FFT_IL2[16]	A	
447	float	_FFT_IL2[17]	A	
449	float	_FFT_IL2[18]	A	
451	float	_FFT_IL2[19]	A	
453	float	_FFT_IL2[20]	A	
455	float	_FFT_IL2[21]	A	
457	float	_FFT_IL2[22]	A	
459	float	_FFT_IL2[23]	A	
461	float	_FFT_IL2[24]	A	
463	float	_FFT_IL2[25]	A	
465	float	_FFT_IL2[26]	A	

Address	Format	Designation	Unit	Remarks
467	float	_FFT_IL2[27]	A	
469	float	_FFT_IL2[28]	A	
471	float	_FFT_IL2[29]	A	
473	float	_FFT_IL2[30]	A	
475	float	_FFT_IL2[31]	A	
477	float	_FFT_IL2[32]	A	
479	float	_FFT_IL2[33]	A	
481	float	_FFT_IL2[34]	A	
483	float	_FFT_IL2[35]	A	
485	float	_FFT_IL2[36]	A	
487	float	_FFT_IL2[37]	A	
489	float	_FFT_IL2[38]	A	
491	float	_FFT_IL2[39]	A	
493	float	_FFT_IL3[0]	A	
495	float	_FFT_IL3[1]	A	
497	float	_FFT_IL3[2]	A	
499	float	_FFT_IL3[3]	A	
501	float	_FFT_IL3[4]	A	
503	float	_FFT_IL3[5]	A	
505	float	_FFT_IL3[6]	A	
507	float	_FFT_IL3[7]	A	
509	float	_FFT_IL3[8]	A	
511	float	_FFT_IL3[9]	A	
513	float	_FFT_IL3[10]	A	
515	float	_FFT_IL3[11]	A	
517	float	_FFT_IL3[12]	A	
519	float	_FFT_IL3[13]	A	
521	float	_FFT_IL3[14]	A	
523	float	_FFT_IL3[15]	A	
525	float	_FFT_IL3[16]	A	
527	float	_FFT_IL3[17]	A	
529	float	_FFT_IL3[18]	A	
531	float	_FFT_IL3[19]	A	
533	float	_FFT_IL3[20]	A	
535	float	_FFT_IL3[21]	A	
537	float	_FFT_IL3[22]	A	
539	float	_FFT_IL3[23]	A	
541	float	_FFT_IL3[24]	A	
543	float	_FFT_IL3[25]	A	
545	float	_FFT_IL3[26]	A	
547	float	_FFT_IL3[27]	A	
549	float	_FFT_IL3[28]	A	
551	float	_FFT_IL3[29]	A	
553	float	_FFT_IL3[30]	A	
555	float	_FFT_IL3[31]	A	
557	float	_FFT_IL3[32]	A	
559	float	_FFT_IL3[33]	A	
561	float	_FFT_IL3[34]	A	
563	float	_FFT_IL3[35]	A	
565	float	_FFT_IL3[36]	A	
567	float	_FFT_IL3[37]	A	
569	float	_FFT_IL3[38]	A	
571	float	_FFT_IL3[39]	A	
573	float	_FFT_IL4[0]	A	
575	float	_FFT_IL4[1]	A	
577	float	_FFT_IL4[2]	A	
579	float	_FFT_IL4[3]	A	
581	float	_FFT_IL4[4]	A	
583	float	_FFT_IL4[5]	A	

Address	Format	Designation	Unit	Remarks
585	float	_FFT_IL4[6]	A	
587	float	_FFT_IL4[7]	A	
589	float	_FFT_IL4[8]	A	
591	float	_FFT_IL4[9]	A	
593	float	_FFT_IL4[10]	A	
595	float	_FFT_IL4[11]	A	
597	float	_FFT_IL4[12]	A	
599	float	_FFT_IL4[13]	A	
601	float	_FFT_IL4[14]	A	
603	float	_FFT_IL4[15]	A	
605	float	_FFT_IL4[16]	A	
607	float	_FFT_IL4[17]	A	
609	float	_FFT_IL4[18]	A	
611	float	_FFT_IL4[19]	A	
613	float	_FFT_IL4[20]	A	
615	float	_FFT_IL4[21]	A	
617	float	_FFT_IL4[22]	A	
619	float	_FFT_IL4[23]	A	
621	float	_FFT_IL4[24]	A	
623	float	_FFT_IL4[25]	A	
625	float	_FFT_IL4[26]	A	
627	float	_FFT_IL4[27]	A	
629	float	_FFT_IL4[28]	A	
631	float	_FFT_IL4[29]	A	
633	float	_FFT_IL4[30]	A	
635	float	_FFT_IL4[31]	A	
637	float	_FFT_IL4[32]	A	
639	float	_FFT_IL4[33]	A	
641	float	_FFT_IL4[34]	A	
643	float	_FFT_IL4[35]	A	
645	float	_FFT_IL4[36]	A	
647	float	_FFT_IL4[37]	A	
649	float	_FFT_IL4[38]	A	
651	float	_FFT_IL4[39]	A	
653	float	_FFT_PL1[0]	W	
655	float	_FFT_PL1[1]	W	
657	float	_FFT_PL1[2]	W	
659	float	_FFT_PL1[3]	W	
661	float	_FFT_PL1[4]	W	
663	float	_FFT_PL1[5]	W	
665	float	_FFT_PL1[6]	W	
667	float	_FFT_PL1[7]	W	
669	float	_FFT_PL1[8]	W	
671	float	_FFT_PL1[9]	W	
673	float	_FFT_PL1[10]	W	
675	float	_FFT_PL1[11]	W	
677	float	_FFT_PL1[12]	W	
679	float	_FFT_PL1[13]	W	
681	float	_FFT_PL1[14]	W	
683	float	_FFT_PL1[15]	W	
685	float	_FFT_PL1[16]	W	
687	float	_FFT_PL1[17]	W	
689	float	_FFT_PL1[18]	W	
691	float	_FFT_PL1[19]	W	
693	float	_FFT_PL1[20]	W	
695	float	_FFT_PL1[21]	W	
697	float	_FFT_PL1[22]	W	
699	float	_FFT_PL1[23]	W	
701	float	_FFT_PL1[24]	W	

Address	Format	Designation	Unit	Remarks
703	float	_FFT_PL1[25]	W	
705	float	_FFT_PL1[26]	W	
707	float	_FFT_PL1[27]	W	
709	float	_FFT_PL1[28]	W	
711	float	_FFT_PL1[29]	W	
713	float	_FFT_PL1[30]	W	
715	float	_FFT_PL1[31]	W	
717	float	_FFT_PL1[32]	W	
719	float	_FFT_PL1[33]	W	
721	float	_FFT_PL1[34]	W	
723	float	_FFT_PL1[35]	W	
725	float	_FFT_PL1[36]	W	
727	float	_FFT_PL1[37]	W	
729	float	_FFT_PL1[38]	W	
731	float	_FFT_PL1[39]	W	
733	float	_FFT_PL2[0]	W	
735	float	_FFT_PL2[1]	W	
737	float	_FFT_PL2[2]	W	
739	float	_FFT_PL2[3]	W	
741	float	_FFT_PL2[4]	W	
743	float	_FFT_PL2[5]	W	
745	float	_FFT_PL2[6]	W	
747	float	_FFT_PL2[7]	W	
749	float	_FFT_PL2[8]	W	
751	float	_FFT_PL2[9]	W	
753	float	_FFT_PL2[10]	W	
755	float	_FFT_PL2[11]	W	
757	float	_FFT_PL2[12]	W	
759	float	_FFT_PL2[13]	W	
761	float	_FFT_PL2[14]	W	
763	float	_FFT_PL2[15]	W	
765	float	_FFT_PL2[16]	W	
767	float	_FFT_PL2[17]	W	
769	float	_FFT_PL2[18]	W	
771	float	_FFT_PL2[19]	W	
773	float	_FFT_PL2[20]	W	
775	float	_FFT_PL2[21]	W	
777	float	_FFT_PL2[22]	W	
779	float	_FFT_PL2[23]	W	
781	float	_FFT_PL2[24]	W	
783	float	_FFT_PL2[25]	W	
785	float	_FFT_PL2[26]	W	
787	float	_FFT_PL2[27]	W	
789	float	_FFT_PL2[28]	W	
791	float	_FFT_PL2[29]	W	
793	float	_FFT_PL2[30]	W	
795	float	_FFT_PL2[31]	W	
797	float	_FFT_PL2[32]	W	
799	float	_FFT_PL2[33]	W	
801	float	_FFT_PL2[34]	W	
803	float	_FFT_PL2[35]	W	
805	float	_FFT_PL2[36]	W	
807	float	_FFT_PL2[37]	W	
809	float	_FFT_PL2[38]	W	
811	float	_FFT_PL2[39]	W	
813	float	_FFT_PL3[0]	W	
815	float	_FFT_PL3[1]	W	
817	float	_FFT_PL3[2]	W	
819	float	_FFT_PL3[3]	W	

Address	Format	Designation	Unit	Remarks
821	float	_FFT_PL3[4]	W	
823	float	_FFT_PL3[5]	W	
825	float	_FFT_PL3[6]	W	
827	float	_FFT_PL3[7]	W	
829	float	_FFT_PL3[8]	W	
831	float	_FFT_PL3[9]	W	
833	float	_FFT_PL3[10]	W	
835	float	_FFT_PL3[11]	W	
837	float	_FFT_PL3[12]	W	
839	float	_FFT_PL3[13]	W	
841	float	_FFT_PL3[14]	W	
843	float	_FFT_PL3[15]	W	
845	float	_FFT_PL3[16]	W	
847	float	_FFT_PL3[17]	W	
849	float	_FFT_PL3[18]	W	
851	float	_FFT_PL3[19]	W	
853	float	_FFT_PL3[20]	W	
855	float	_FFT_PL3[21]	W	
857	float	_FFT_PL3[22]	W	
859	float	_FFT_PL3[23]	W	
861	float	_FFT_PL3[24]	W	
863	float	_FFT_PL3[25]	W	
865	float	_FFT_PL3[26]	W	
867	float	_FFT_PL3[27]	W	
869	float	_FFT_PL3[28]	W	
871	float	_FFT_PL3[29]	W	
873	float	_FFT_PL3[30]	W	
875	float	_FFT_PL3[31]	W	
877	float	_FFT_PL3[32]	W	
879	float	_FFT_PL3[33]	W	
881	float	_FFT_PL3[34]	W	
883	float	_FFT_PL3[35]	W	
885	float	_FFT_PL3[36]	W	
887	float	_FFT_PL3[37]	W	
889	float	_FFT_PL3[38]	W	
891	float	_FFT_PL3[39]	W	
893	float	_FFT_PL4[0]	W	
895	float	_FFT_PL4[1]	W	
897	float	_FFT_PL4[2]	W	
899	float	_FFT_PL4[3]	W	
901	float	_FFT_PL4[4]	W	
903	float	_FFT_PL4[5]	W	
905	float	_FFT_PL4[6]	W	
907	float	_FFT_PL4[7]	W	
909	float	_FFT_PL4[8]	W	
911	float	_FFT_PL4[9]	W	
913	float	_FFT_PL4[10]	W	
915	float	_FFT_PL4[11]	W	
917	float	_FFT_PL4[12]	W	
919	float	_FFT_PL4[13]	W	
921	float	_FFT_PL4[14]	W	
923	float	_FFT_PL4[15]	W	
925	float	_FFT_PL4[16]	W	
927	float	_FFT_PL4[17]	W	
929	float	_FFT_PL4[18]	W	
931	float	_FFT_PL4[19]	W	
933	float	_FFT_PL4[20]	W	
935	float	_FFT_PL4[21]	W	
937	float	_FFT_PL4[22]	W	

Address	Format	Designation	Unit	Remarks
939	float	_FFT_PL4[23]	W	
941	float	_FFT_PL4[24]	W	
943	float	_FFT_PL4[25]	W	
945	float	_FFT_PL4[26]	W	
947	float	_FFT_PL4[27]	W	
949	float	_FFT_PL4[28]	W	
951	float	_FFT_PL4[29]	W	
953	float	_FFT_PL4[30]	W	
955	float	_FFT_PL4[31]	W	
957	float	_FFT_PL4[32]	W	
959	float	_FFT_PL4[33]	W	
961	float	_FFT_PL4[34]	W	
963	float	_FFT_PL4[35]	W	
965	float	_FFT_PL4[36]	W	
967	float	_FFT_PL4[37]	W	
969	float	_FFT_PL4[38]	W	
971	float	_FFT_PL4[39]	W	
973	float	_FFT_QL1[0]	VAr	
975	float	_FFT_QL1[1]	VAr	
977	float	_FFT_QL1[2]	VAr	
979	float	_FFT_QL1[3]	VAr	
981	float	_FFT_QL1[4]	VAr	
983	float	_FFT_QL1[5]	VAr	
985	float	_FFT_QL1[6]	VAr	
987	float	_FFT_QL1[7]	VAr	
989	float	_FFT_QL1[8]	VAr	
991	float	_FFT_QL1[9]	VAr	
993	float	_FFT_QL1[10]	VAr	
995	float	_FFT_QL1[11]	VAr	
997	float	_FFT_QL1[12]	VAr	
999	float	_FFT_QL1[13]	VAr	
1001	float	_FFT_QL1[14]	VAr	
1003	float	_FFT_QL1[15]	VAr	
1005	float	_FFT_QL1[16]	VAr	
1007	float	_FFT_QL1[17]	VAr	
1009	float	_FFT_QL1[18]	VAr	
1011	float	_FFT_QL1[19]	VAr	
1013	float	_FFT_QL1[20]	VAr	
1015	float	_FFT_QL1[21]	VAr	
1017	float	_FFT_QL1[22]	VAr	
1019	float	_FFT_QL1[23]	VAr	
1021	float	_FFT_QL1[24]	VAr	
1023	float	_FFT_QL1[25]	VAr	
1025	float	_FFT_QL1[26]	VAr	
1027	float	_FFT_QL1[27]	VAr	
1029	float	_FFT_QL1[28]	VAr	
1031	float	_FFT_QL1[29]	VAr	
1033	float	_FFT_QL1[30]	VAr	
1035	float	_FFT_QL1[31]	VAr	
1037	float	_FFT_QL1[32]	VAr	
1039	float	_FFT_QL1[33]	VAr	
1041	float	_FFT_QL1[34]	VAr	
1043	float	_FFT_QL1[35]	VAr	
1045	float	_FFT_QL1[36]	VAr	
1047	float	_FFT_QL1[37]	VAr	
1049	float	_FFT_QL1[38]	VAr	
1051	float	_FFT_QL1[39]	VAr	
1053	float	_FFT_QL2[0]	VAr	
1055	float	_FFT_QL2[1]	VAr	

Address	Format	Designation	Unit	Remarks
1057	float	_FFT_QL2[2]	VAr	
1059	float	_FFT_QL2[3]	VAr	
1061	float	_FFT_QL2[4]	VAr	
1063	float	_FFT_QL2[5]	VAr	
1065	float	_FFT_QL2[6]	VAr	
1067	float	_FFT_QL2[7]	VAr	
1069	float	_FFT_QL2[8]	VAr	
1071	float	_FFT_QL2[9]	VAr	
1073	float	_FFT_QL2[10]	VAr	
1075	float	_FFT_QL2[11]	VAr	
1077	float	_FFT_QL2[12]	VAr	
1079	float	_FFT_QL2[13]	VAr	
1081	float	_FFT_QL2[14]	VAr	
1083	float	_FFT_QL2[15]	VAr	
1085	float	_FFT_QL2[16]	VAr	
1087	float	_FFT_QL2[17]	VAr	
1089	float	_FFT_QL2[18]	VAr	
1091	float	_FFT_QL2[19]	VAr	
1093	float	_FFT_QL2[20]	VAr	
1095	float	_FFT_QL2[21]	VAr	
1097	float	_FFT_QL2[22]	VAr	
1099	float	_FFT_QL2[23]	VAr	
1101	float	_FFT_QL2[24]	VAr	
1103	float	_FFT_QL2[25]	VAr	
1105	float	_FFT_QL2[26]	VAr	
1107	float	_FFT_QL2[27]	VAr	
1109	float	_FFT_QL2[28]	VAr	
1111	float	_FFT_QL2[29]	VAr	
1113	float	_FFT_QL2[30]	VAr	
1115	float	_FFT_QL2[31]	VAr	
1117	float	_FFT_QL2[32]	VAr	
1119	float	_FFT_QL2[33]	VAr	
1121	float	_FFT_QL2[34]	VAr	
1123	float	_FFT_QL2[35]	VAr	
1125	float	_FFT_QL2[36]	VAr	
1127	float	_FFT_QL2[37]	VAr	
1129	float	_FFT_QL2[38]	VAr	
1131	float	_FFT_QL2[39]	VAr	
1133	float	_FFT_QL3[0]	VAr	
1135	float	_FFT_QL3[1]	VAr	
1137	float	_FFT_QL3[2]	VAr	
1139	float	_FFT_QL3[3]	VAr	
1141	float	_FFT_QL3[4]	VAr	
1143	float	_FFT_QL3[5]	VAr	
1145	float	_FFT_QL3[6]	VAr	
1147	float	_FFT_QL3[7]	VAr	
1149	float	_FFT_QL3[8]	VAr	
1151	float	_FFT_QL3[9]	VAr	
1153	float	_FFT_QL3[10]	VAr	
1155	float	_FFT_QL3[11]	VAr	
1157	float	_FFT_QL3[12]	VAr	
1159	float	_FFT_QL3[13]	VAr	
1161	float	_FFT_QL3[14]	VAr	
1163	float	_FFT_QL3[15]	VAr	
1165	float	_FFT_QL3[16]	VAr	
1167	float	_FFT_QL3[17]	VAr	
1169	float	_FFT_QL3[18]	VAr	
1171	float	_FFT_QL3[19]	VAr	
1173	float	_FFT_QL3[20]	VAr	

Address	Format	Designation	Unit	Remarks
1175	float	_FFT_QL3[21]	VAr	
1177	float	_FFT_QL3[22]	VAr	
1179	float	_FFT_QL3[23]	VAr	
1181	float	_FFT_QL3[24]	VAr	
1183	float	_FFT_QL3[25]	VAr	
1185	float	_FFT_QL3[26]	VAr	
1187	float	_FFT_QL3[27]	VAr	
1189	float	_FFT_QL3[28]	VAr	
1191	float	_FFT_QL3[29]	VAr	
1193	float	_FFT_QL3[30]	VAr	
1195	float	_FFT_QL3[31]	VAr	
1197	float	_FFT_QL3[32]	VAr	
1199	float	_FFT_QL3[33]	VAr	
1201	float	_FFT_QL3[34]	VAr	
1203	float	_FFT_QL3[35]	VAr	
1205	float	_FFT_QL3[36]	VAr	
1207	float	_FFT_QL3[37]	VAr	
1209	float	_FFT_QL3[38]	VAr	
1211	float	_FFT_QL3[39]	VAr	
1213	float	_FFT_QL4[0]	VAr	
1215	float	_FFT_QL4[1]	VAr	
1217	float	_FFT_QL4[2]	VAr	
1219	float	_FFT_QL4[3]	VAr	
1221	float	_FFT_QL4[4]	VAr	
1223	float	_FFT_QL4[5]	VAr	
1225	float	_FFT_QL4[6]	VAr	
1227	float	_FFT_QL4[7]	VAr	
1229	float	_FFT_QL4[8]	VAr	
1231	float	_FFT_QL4[9]	VAr	
1233	float	_FFT_QL4[10]	VAr	
1235	float	_FFT_QL4[11]	VAr	
1237	float	_FFT_QL4[12]	VAr	
1239	float	_FFT_QL4[13]	VAr	
1241	float	_FFT_QL4[14]	VAr	
1243	float	_FFT_QL4[15]	VAr	
1245	float	_FFT_QL4[16]	VAr	
1247	float	_FFT_QL4[17]	VAr	
1249	float	_FFT_QL4[18]	VAr	
1251	float	_FFT_QL4[19]	VAr	
1253	float	_FFT_QL4[20]	VAr	
1255	float	_FFT_QL4[21]	VAr	
1257	float	_FFT_QL4[22]	VAr	
1259	float	_FFT_QL4[23]	VAr	
1261	float	_FFT_QL4[24]	VAr	
1263	float	_FFT_QL4[25]	VAr	
1265	float	_FFT_QL4[26]	VAr	
1267	float	_FFT_QL4[27]	VAr	
1269	float	_FFT_QL4[28]	VAr	
1271	float	_FFT_QL4[29]	VAr	
1273	float	_FFT_QL4[30]	VAr	
1275	float	_FFT_QL4[31]	VAr	
1277	float	_FFT_QL4[32]	VAr	
1279	float	_FFT_QL4[33]	VAr	
1281	float	_FFT_QL4[34]	VAr	
1283	float	_FFT_QL4[35]	VAr	
1285	float	_FFT_QL4[36]	VAr	
1287	float	_FFT_QL4[37]	VAr	
1289	float	_FFT_QL4[38]	VAr	
1291	float	_FFT_QL4[39]	VAr	



Address	Format	Designation	Unit	Remarks
1293	float	_THD_ULN[0]	%	Total Harmonic Distortion, UL1-N
1295	float	_THD_ULN[1]	%	Total Harmonic Distortion, UL2-N
1297	float	_THD_ULN[2]	%	Total Harmonic Distortion, UL3-N
1299	float	_THD_ULN[3]	%	Total Harmonic Distortion, UL4-N
1301	float	_THD_ILN[0]	%	Total Harmonic Distortion, IL1
1303	float	_THD_ILN[1]	%	Total Harmonic Distortion, IL2
1305	float	_THD_ILN[2]	%	Total Harmonic Distortion, IL3
1307	float	_THD_ILN[3]	%	Total Harmonic Distortion, IL4
1309	float	_KFACT[0]		K-Factor, L1
1311	float	_KFACT[1]		K-Factor, L2
1313	float	_KFACT[2]		K-Factor, L3
1315	float	_KFACT[3]		K-Factor, L4
1317	float	_ULN[0]	V	Voltage L1-N
1319	float	_ULN[1]	V	Voltage L2-N
1321	float	_ULN[2]	V	Voltage L3-N
1323	float	_ULN[3]	V	Voltage L4-N
1325	float	_ILN[0]	A	Current L1
1327	float	_ILN[1]	A	Current L2
1329	float	_ILN[2]	A	Current L3
1331	float	_ILN[3]	A	Current L4
1333	float	_PLN[0]	W	Real Power L1
1335	float	_PLN[1]	W	Real Power L2
1337	float	_PLN[2]	W	Real Power L3
1339	float	_PLN[3]	W	Real Power L4
1341	float	_QLN[0]	VAr	Reactive Power L1
1343	float	_QLN[1]	VAr	Reactive Power L2
1345	float	_QLN[2]	VAr	Reactive Power L3
1347	float	_QLN[3]	VAr	Reactive Power L4
1349	float	_SLN[0]	VA	Apparent Power L1
1351	float	_SLN[1]	VA	Apparent Power L2
1353	float	_SLN[2]	VA	Apparent Power L3
1355	float	_SLN[3]	VA	Apparent Power L4
1357	float	_ULL[0]	V	Voltage UL1-L2
1359	float	_ULL[1]	V	Voltage UL2-L3
1361	float	_ULL[2]	V	Voltage UL3-L1
1363	float	_I_SUM3	A	Sum IL1 + IL2 + IL3
1365	float	_I_SUM	A	Sum IL1 + IL2 + IL3 + IL4
1367	float	_S_SUM3	VA	Sum SL1 + SL2 + SL3
1369	float	_P_SUM3	W	Sum PL1 + PL2 + PL3
1371	float	_Q_SUM3	VAr	Sum QL1 + QL2 + QL3
1373	float	_COS_SUM3		Sum CosL1 + CosL2 + CosL3
1375	float	_S_SUM	VA	Sum SL1 + SL2 + SL3 + SL4
1377	float	_P_SUM	W	Sum PL1 + PL2 + PL3 + PL4
1379	float	_Q_SUM	VAr	Sum QL1 + QL2 + QL3 + QL4
1381	float	_COS_SUM		Sum CosL1 +CosL2 +CosL3 +CosL4
1383	float	_ULN_RE[0]	V	Voltage L1, Real Part
1385	float	_ULN_RE[1]	V	Voltage L2, Real Part
1387	float	_ULN_RE[2]	V	Voltage L3, Real Part
1389	float	_ULN_RE[3]	V	Voltage L4, Real Part
1391	float	_ULN_IM[0]	V	Voltage L1, Imaginary Part
1393	float	_ULN_IM[1]	V	Voltage L2, Imaginary Part
1395	float	_ULN_IM[2]	V	Voltage L3, Imaginary Part
1397	float	_ULN_IM[3]	V	Voltage L4, Imaginary Part
1399	float	_ILN_RE[0]	A	Current L1, Real Part
1401	float	_ILN_RE[1]	A	Current L2, Real Part
1403	float	_ILN_RE[2]	A	Current L3, Real Part
1405	float	_ILN_RE[3]	A	Current L4, Real Part

Address	Format	Designation	Unit	Remarks
1407	float	_ILN_IM[0]	A	Current L1, Imaginary Part
1409	float	_ILN_IM[1]	A	Current L2, Imaginary Part
1411	float	_ILN_IM[2]	A	Current L3, Imaginary Part
1413	float	_ILN_IM[3]	A	Current L4, Imaginary Part
1415	float	_PHASE[0]	°	Phase UL1 IL1
1417	float	_PHASE[1]	°	Phase UL2 IL2
1419	float	_PHASE[2]	°	Phase UL3 IL3
1421	float	_PHASE[3]	°	Phase UL4 IL4
1423	float	_COS_PHI[0]		CosPhi L1
1425	float	_COS_PHI[1]		CosPhi L2
1427	float	_COS_PHI[2]		CosPhi L3
1429	float	_COS_PHI[3]		CosPhi L4
1431	float	_IND_CAP[0]		Q Sign in L1
1433	float	_IND_CAP[1]		Q Sign in L2
1435	float	_IND_CAP[2]		Q Sign in L3
1437	float	_IND_CAP[3]		Q Sign in L4
1439	float	_FREQ	Hz	Frequency
1441	float	_N	V	Zero Sequence System
1443	float	_M	V	Positive Sequence System
1445	float	_G	V	Negative Sequence System
1447	float	_SYM	%	Unbalance = Negative Sequence System / Positive Sequence System
1449	float	_PHASE_SEQ		Rotating Field, 1=right, 0=none, -1=lefts
1451	float	_IN	A	Zero Sequence System
1453	float	_IM	A	Positive Sequence System
1455	float	_IG	A	Negative Sequence System
1457	float	_S0_POWER[0]	W	Pulse Input 1, Power per Impulse
1459	float	_S0_POWER[1]	W	Pulse Input 2, Power per Impulse
1461	float	_EXT_TEMPERATUR	°C	Temperature
1463	float	_FFT_UL1_AVG[0]	V	1. Harmonic, UL1, Mean Value
1465	float	_FFT_UL1_AVG[1]	V	2. Harmonic, UL1, Mean Value
1467	float	_FFT_UL1_AVG[2]	V	3. Harmonic, UL1, Mean Value
1469	float	_FFT_UL1_AVG[3]	V	4. Harmonic, UL1, Mean Value
1471	float	_FFT_UL1_AVG[4]	V	5. Harmonic, UL1, Mean Value
1473	float	_FFT_UL1_AVG[5]	V	6. Harmonic, UL1, Mean Value
1475	float	_FFT_UL1_AVG[6]	V	7. Harmonic, UL1, Mean Value
1477	float	_FFT_UL1_AVG[7]	V	8. Harmonic, UL1, Mean Value
1479	float	_FFT_UL1_AVG[8]	V	9. Harmonic, UL1, Mean Value
1481	float	_FFT_UL1_AVG[9]	V	10. Harmonic, UL1, Mean Value
1483	float	_FFT_UL1_AVG[10]	V	11. Harmonic, UL1, Mean Value
1485	float	_FFT_UL1_AVG[11]	V	12. Harmonic, UL1, Mean Value
1487	float	_FFT_UL1_AVG[12]	V	13. Harmonic, UL1, Mean Value
1489	float	_FFT_UL1_AVG[13]	V	14. Harmonic, UL1, Mean Value
1491	float	_FFT_UL1_AVG[14]	V	15. Harmonic, UL1, Mean Value
1493	float	_FFT_UL1_AVG[15]	V	16. Harmonic, UL1, Mean Value
1495	float	_FFT_UL1_AVG[16]	V	17. Harmonic, UL1, Mean Value
1497	float	_FFT_UL1_AVG[17]	V	18. Harmonic, UL1, Mean Value
1499	float	_FFT_UL1_AVG[18]	V	19. Harmonic, UL1, Mean Value
1501	float	_FFT_UL1_AVG[19]	V	20. Harmonic, UL1, Mean Value
1503	float	_FFT_UL1_AVG[20]	V	21. Harmonic, UL1, Mean Value
1505	float	_FFT_UL1_AVG[21]	V	22. Harmonic, UL1, Mean Value
1507	float	_FFT_UL1_AVG[22]	V	23. Harmonic, UL1, Mean Value
1509	float	_FFT_UL1_AVG[23]	V	24. Harmonic, UL1, Mean Value
1511	float	_FFT_UL1_AVG[24]	V	25. Harmonic, UL1, Mean Value
1513	float	_FFT_UL1_AVG[25]	V	26. Harmonic, UL1, Mean Value
1515	float	_FFT_UL1_AVG[26]	V	27. Harmonic, UL1, Mean Value
1517	float	_FFT_UL1_AVG[27]	V	28. Harmonic, UL1, Mean Value
1519	float	_FFT_UL1_AVG[28]	V	29. Harmonic, UL1, Mean Value
1521	float	_FFT_UL1_AVG[29]	V	30. Harmonic, UL1, Mean Value

Address	Format	Designation	Unit	Remarks
1523	float	_FFT_UL1_AVG[30]	V	31. Harmonic, UL1, Mean Value
1525	float	_FFT_UL1_AVG[31]	V	32. Harmonic, UL1, Mean Value
1527	float	_FFT_UL1_AVG[32]	V	33. Harmonic, UL1, Mean Value
1529	float	_FFT_UL1_AVG[33]	V	34. Harmonic, UL1, Mean Value
1531	float	_FFT_UL1_AVG[34]	V	35. Harmonic, UL1, Mean Value
1533	float	_FFT_UL1_AVG[35]	V	36. Harmonic, UL1, Mean Value
1535	float	_FFT_UL1_AVG[36]	V	37. Harmonic, UL1, Mean Value
1537	float	_FFT_UL1_AVG[37]	V	38. Harmonic, UL1, Mean Value
1539	float	_FFT_UL1_AVG[38]	V	39. Harmonic, UL1, Mean Value
1541	float	_FFT_UL1_AVG[39]	V	40. Harmonic, UL1, Mean Value
1543	float	_FFT_UL2_AVG[0]	V	
1545	float	_FFT_UL2_AVG[1]	V	
1547	float	_FFT_UL2_AVG[2]	V	
1549	float	_FFT_UL2_AVG[3]	V	
1551	float	_FFT_UL2_AVG[4]	V	
1553	float	_FFT_UL2_AVG[5]	V	
1555	float	_FFT_UL2_AVG[6]	V	
1557	float	_FFT_UL2_AVG[7]	V	
1559	float	_FFT_UL2_AVG[8]	V	
1561	float	_FFT_UL2_AVG[9]	V	
1563	float	_FFT_UL2_AVG[10]	V	
1565	float	_FFT_UL2_AVG[11]	V	
1567	float	_FFT_UL2_AVG[12]	V	
1569	float	_FFT_UL2_AVG[13]	V	
1571	float	_FFT_UL2_AVG[14]	V	
1573	float	_FFT_UL2_AVG[15]	V	
1575	float	_FFT_UL2_AVG[16]	V	
1577	float	_FFT_UL2_AVG[17]	V	
1579	float	_FFT_UL2_AVG[18]	V	
1581	float	_FFT_UL2_AVG[19]	V	
1583	float	_FFT_UL2_AVG[20]	V	
1585	float	_FFT_UL2_AVG[21]	V	
1587	float	_FFT_UL2_AVG[22]	V	
1589	float	_FFT_UL2_AVG[23]	V	
1591	float	_FFT_UL2_AVG[24]	V	
1593	float	_FFT_UL2_AVG[25]	V	
1595	float	_FFT_UL2_AVG[26]	V	
1597	float	_FFT_UL2_AVG[27]	V	
1599	float	_FFT_UL2_AVG[28]	V	
1601	float	_FFT_UL2_AVG[29]	V	
1603	float	_FFT_UL2_AVG[30]	V	
1605	float	_FFT_UL2_AVG[31]	V	
1607	float	_FFT_UL2_AVG[32]	V	
1609	float	_FFT_UL2_AVG[33]	V	
1611	float	_FFT_UL2_AVG[34]	V	
1613	float	_FFT_UL2_AVG[35]	V	
1615	float	_FFT_UL2_AVG[36]	V	
1617	float	_FFT_UL2_AVG[37]	V	
1619	float	_FFT_UL2_AVG[38]	V	
1621	float	_FFT_UL2_AVG[39]	V	
1623	float	_FFT_UL3_AVG[0]	V	
1625	float	_FFT_UL3_AVG[1]	V	
1627	float	_FFT_UL3_AVG[2]	V	
1629	float	_FFT_UL3_AVG[3]	V	
1631	float	_FFT_UL3_AVG[4]	V	
1633	float	_FFT_UL3_AVG[5]	V	
1635	float	_FFT_UL3_AVG[6]	V	
1637	float	_FFT_UL3_AVG[7]	V	
1639	float	_FFT_UL3_AVG[8]	V	

Address	Format	Designation	Unit	Remarks
1641	float	_FFT_UL3_AVG[9]	V	
1643	float	_FFT_UL3_AVG[10]	V	
1645	float	_FFT_UL3_AVG[11]	V	
1647	float	_FFT_UL3_AVG[12]	V	
1649	float	_FFT_UL3_AVG[13]	V	
1651	float	_FFT_UL3_AVG[14]	V	
1653	float	_FFT_UL3_AVG[15]	V	
1655	float	_FFT_UL3_AVG[16]	V	
1657	float	_FFT_UL3_AVG[17]	V	
1659	float	_FFT_UL3_AVG[18]	V	
1661	float	_FFT_UL3_AVG[19]	V	
1663	float	_FFT_UL3_AVG[20]	V	
1665	float	_FFT_UL3_AVG[21]	V	
1667	float	_FFT_UL3_AVG[22]	V	
1669	float	_FFT_UL3_AVG[23]	V	
1671	float	_FFT_UL3_AVG[24]	V	
1673	float	_FFT_UL3_AVG[25]	V	
1675	float	_FFT_UL3_AVG[26]	V	
1677	float	_FFT_UL3_AVG[27]	V	
1679	float	_FFT_UL3_AVG[28]	V	
1681	float	_FFT_UL3_AVG[29]	V	
1683	float	_FFT_UL3_AVG[30]	V	
1685	float	_FFT_UL3_AVG[31]	V	
1687	float	_FFT_UL3_AVG[32]	V	
1689	float	_FFT_UL3_AVG[33]	V	
1691	float	_FFT_UL3_AVG[34]	V	
1693	float	_FFT_UL3_AVG[35]	V	
1695	float	_FFT_UL3_AVG[36]	V	
1697	float	_FFT_UL3_AVG[37]	V	
1699	float	_FFT_UL3_AVG[38]	V	
1701	float	_FFT_UL3_AVG[39]	V	
1703	float	_FFT_UL4_AVG[0]	V	
1705	float	_FFT_UL4_AVG[1]	V	
1707	float	_FFT_UL4_AVG[2]	V	
1709	float	_FFT_UL4_AVG[3]	V	
1711	float	_FFT_UL4_AVG[4]	V	
1713	float	_FFT_UL4_AVG[5]	V	
1715	float	_FFT_UL4_AVG[6]	V	
1717	float	_FFT_UL4_AVG[7]	V	
1719	float	_FFT_UL4_AVG[8]	V	
1721	float	_FFT_UL4_AVG[9]	V	
1723	float	_FFT_UL4_AVG[10]	V	
1725	float	_FFT_UL4_AVG[11]	V	
1727	float	_FFT_UL4_AVG[12]	V	
1729	float	_FFT_UL4_AVG[13]	V	
1731	float	_FFT_UL4_AVG[14]	V	
1733	float	_FFT_UL4_AVG[15]	V	
1735	float	_FFT_UL4_AVG[16]	V	
1737	float	_FFT_UL4_AVG[17]	V	
1739	float	_FFT_UL4_AVG[18]	V	
1741	float	_FFT_UL4_AVG[19]	V	
1743	float	_FFT_UL4_AVG[20]	V	
1745	float	_FFT_UL4_AVG[21]	V	
1747	float	_FFT_UL4_AVG[22]	V	
1749	float	_FFT_UL4_AVG[23]	V	
1751	float	_FFT_UL4_AVG[24]	V	
1753	float	_FFT_UL4_AVG[25]	V	
1755	float	_FFT_UL4_AVG[26]	V	
1757	float	_FFT_UL4_AVG[27]	V	

Address	Format	Designation	Unit	Remarks
1759	float	_FFT_UL4_AVG[28]	V	
1761	float	_FFT_UL4_AVG[29]	V	
1763	float	_FFT_UL4_AVG[30]	V	
1765	float	_FFT_UL4_AVG[31]	V	
1767	float	_FFT_UL4_AVG[32]	V	
1769	float	_FFT_UL4_AVG[33]	V	
1771	float	_FFT_UL4_AVG[34]	V	
1773	float	_FFT_UL4_AVG[35]	V	
1775	float	_FFT_UL4_AVG[36]	V	
1777	float	_FFT_UL4_AVG[37]	V	
1779	float	_FFT_UL4_AVG[38]	V	
1781	float	_FFT_UL4_AVG[39]	V	
1783	float	_FFT_IL1_AVG[0]	A	
1785	float	_FFT_IL1_AVG[1]	A	
1787	float	_FFT_IL1_AVG[2]	A	
1789	float	_FFT_IL1_AVG[3]	A	
1791	float	_FFT_IL1_AVG[4]	A	
1793	float	_FFT_IL1_AVG[5]	A	
1795	float	_FFT_IL1_AVG[6]	A	
1797	float	_FFT_IL1_AVG[7]	A	
1799	float	_FFT_IL1_AVG[8]	A	
1801	float	_FFT_IL1_AVG[9]	A	
1803	float	_FFT_IL1_AVG[10]	A	
1805	float	_FFT_IL1_AVG[11]	A	
1807	float	_FFT_IL1_AVG[12]	A	
1809	float	_FFT_IL1_AVG[13]	A	
1811	float	_FFT_IL1_AVG[14]	A	
1813	float	_FFT_IL1_AVG[15]	A	
1815	float	_FFT_IL1_AVG[16]	A	
1817	float	_FFT_IL1_AVG[17]	A	
1819	float	_FFT_IL1_AVG[18]	A	
1821	float	_FFT_IL1_AVG[19]	A	
1823	float	_FFT_IL1_AVG[20]	A	
1825	float	_FFT_IL1_AVG[21]	A	
1827	float	_FFT_IL1_AVG[22]	A	
1829	float	_FFT_IL1_AVG[23]	A	
1831	float	_FFT_IL1_AVG[24]	A	
1833	float	_FFT_IL1_AVG[25]	A	
1835	float	_FFT_IL1_AVG[26]	A	
1837	float	_FFT_IL1_AVG[27]	A	
1839	float	_FFT_IL1_AVG[28]	A	
1841	float	_FFT_IL1_AVG[29]	A	
1843	float	_FFT_IL1_AVG[30]	A	
1845	float	_FFT_IL1_AVG[31]	A	
1847	float	_FFT_IL1_AVG[32]	A	
1849	float	_FFT_IL1_AVG[33]	A	
1851	float	_FFT_IL1_AVG[34]	A	
1853	float	_FFT_IL1_AVG[35]	A	
1855	float	_FFT_IL1_AVG[36]	A	
1857	float	_FFT_IL1_AVG[37]	A	
1859	float	_FFT_IL1_AVG[38]	A	
1861	float	_FFT_IL1_AVG[39]	A	
1863	float	_FFT_IL2_AVG[0]	A	
1865	float	_FFT_IL2_AVG[1]	A	
1867	float	_FFT_IL2_AVG[2]	A	
1869	float	_FFT_IL2_AVG[3]	A	
1871	float	_FFT_IL2_AVG[4]	A	
1873	float	_FFT_IL2_AVG[5]	A	
1875	float	_FFT_IL2_AVG[6]	A	

Address	Format	Designation	Unit	Remarks
1877	float	_FFT_IL2_AVG[7]	A	
1879	float	_FFT_IL2_AVG[8]	A	
1881	float	_FFT_IL2_AVG[9]	A	
1883	float	_FFT_IL2_AVG[10]	A	
1885	float	_FFT_IL2_AVG[11]	A	
1887	float	_FFT_IL2_AVG[12]	A	
1889	float	_FFT_IL2_AVG[13]	A	
1891	float	_FFT_IL2_AVG[14]	A	
1893	float	_FFT_IL2_AVG[15]	A	
1895	float	_FFT_IL2_AVG[16]	A	
1897	float	_FFT_IL2_AVG[17]	A	
1899	float	_FFT_IL2_AVG[18]	A	
1901	float	_FFT_IL2_AVG[19]	A	
1903	float	_FFT_IL2_AVG[20]	A	
1905	float	_FFT_IL2_AVG[21]	A	
1907	float	_FFT_IL2_AVG[22]	A	
1909	float	_FFT_IL2_AVG[23]	A	
1911	float	_FFT_IL2_AVG[24]	A	
1913	float	_FFT_IL2_AVG[25]	A	
1915	float	_FFT_IL2_AVG[26]	A	
1917	float	_FFT_IL2_AVG[27]	A	
1919	float	_FFT_IL2_AVG[28]	A	
1921	float	_FFT_IL2_AVG[29]	A	
1923	float	_FFT_IL2_AVG[30]	A	
1925	float	_FFT_IL2_AVG[31]	A	
1927	float	_FFT_IL2_AVG[32]	A	
1929	float	_FFT_IL2_AVG[33]	A	
1931	float	_FFT_IL2_AVG[34]	A	
1933	float	_FFT_IL2_AVG[35]	A	
1935	float	_FFT_IL2_AVG[36]	A	
1937	float	_FFT_IL2_AVG[37]	A	
1939	float	_FFT_IL2_AVG[38]	A	
1941	float	_FFT_IL2_AVG[39]	A	
1943	float	_FFT_IL3_AVG[0]	A	
1945	float	_FFT_IL3_AVG[1]	A	
1947	float	_FFT_IL3_AVG[2]	A	
1949	float	_FFT_IL3_AVG[3]	A	
1951	float	_FFT_IL3_AVG[4]	A	
1953	float	_FFT_IL3_AVG[5]	A	
1955	float	_FFT_IL3_AVG[6]	A	
1957	float	_FFT_IL3_AVG[7]	A	
1959	float	_FFT_IL3_AVG[8]	A	
1961	float	_FFT_IL3_AVG[9]	A	
1963	float	_FFT_IL3_AVG[10]	A	
1965	float	_FFT_IL3_AVG[11]	A	
1967	float	_FFT_IL3_AVG[12]	A	
1969	float	_FFT_IL3_AVG[13]	A	
1971	float	_FFT_IL3_AVG[14]	A	
1973	float	_FFT_IL3_AVG[15]	A	
1975	float	_FFT_IL3_AVG[16]	A	
1977	float	_FFT_IL3_AVG[17]	A	
1979	float	_FFT_IL3_AVG[18]	A	
1981	float	_FFT_IL3_AVG[19]	A	
1983	float	_FFT_IL3_AVG[20]	A	
1985	float	_FFT_IL3_AVG[21]	A	
1987	float	_FFT_IL3_AVG[22]	A	
1989	float	_FFT_IL3_AVG[23]	A	
1991	float	_FFT_IL3_AVG[24]	A	
1993	float	_FFT_IL3_AVG[25]	A	

Address	Format	Designation	Unit	Remarks
1995	float	_FFT_IL3_AVG[26]	A	
1997	float	_FFT_IL3_AVG[27]	A	
1999	float	_FFT_IL3_AVG[28]	A	
2001	float	_FFT_IL3_AVG[29]	A	
2003	float	_FFT_IL3_AVG[30]	A	
2005	float	_FFT_IL3_AVG[31]	A	
2007	float	_FFT_IL3_AVG[32]	A	
2009	float	_FFT_IL3_AVG[33]	A	
2011	float	_FFT_IL3_AVG[34]	A	
2013	float	_FFT_IL3_AVG[35]	A	
2015	float	_FFT_IL3_AVG[36]	A	
2017	float	_FFT_IL3_AVG[37]	A	
2019	float	_FFT_IL3_AVG[38]	A	
2021	float	_FFT_IL3_AVG[39]	A	
2023	float	_FFT_IL4_AVG[0]	A	
2025	float	_FFT_IL4_AVG[1]	A	
2027	float	_FFT_IL4_AVG[2]	A	
2029	float	_FFT_IL4_AVG[3]	A	
2031	float	_FFT_IL4_AVG[4]	A	
2033	float	_FFT_IL4_AVG[5]	A	
2035	float	_FFT_IL4_AVG[6]	A	
2037	float	_FFT_IL4_AVG[7]	A	
2039	float	_FFT_IL4_AVG[8]	A	
2041	float	_FFT_IL4_AVG[9]	A	
2043	float	_FFT_IL4_AVG[10]	A	
2045	float	_FFT_IL4_AVG[11]	A	
2047	float	_FFT_IL4_AVG[12]	A	
2049	float	_FFT_IL4_AVG[13]	A	
2051	float	_FFT_IL4_AVG[14]	A	
2053	float	_FFT_IL4_AVG[15]	A	
2055	float	_FFT_IL4_AVG[16]	A	
2057	float	_FFT_IL4_AVG[17]	A	
2059	float	_FFT_IL4_AVG[18]	A	
2061	float	_FFT_IL4_AVG[19]	A	
2063	float	_FFT_IL4_AVG[20]	A	
2065	float	_FFT_IL4_AVG[21]	A	
2067	float	_FFT_IL4_AVG[22]	A	
2069	float	_FFT_IL4_AVG[23]	A	
2071	float	_FFT_IL4_AVG[24]	A	
2073	float	_FFT_IL4_AVG[25]	A	
2075	float	_FFT_IL4_AVG[26]	A	
2077	float	_FFT_IL4_AVG[27]	A	
2079	float	_FFT_IL4_AVG[28]	A	
2081	float	_FFT_IL4_AVG[29]	A	
2083	float	_FFT_IL4_AVG[30]	A	
2085	float	_FFT_IL4_AVG[31]	A	
2087	float	_FFT_IL4_AVG[32]	A	
2089	float	_FFT_IL4_AVG[33]	A	
2091	float	_FFT_IL4_AVG[34]	A	
2093	float	_FFT_IL4_AVG[35]	A	
2095	float	_FFT_IL4_AVG[36]	A	
2097	float	_FFT_IL4_AVG[37]	A	
2099	float	_FFT_IL4_AVG[38]	A	
2101	float	_FFT_IL4_AVG[39]	A	
2103	float	_FFT_PL1_AVG[0]	W	
2105	float	_FFT_PL1_AVG[1]	W	
2107	float	_FFT_PL1_AVG[2]	W	
2109	float	_FFT_PL1_AVG[3]	W	
2111	float	_FFT_PL1_AVG[4]	W	



Address	Format	Designation	Unit	Remarks
2113	float	_FFT_PL1_AVG[5]	W	
2115	float	_FFT_PL1_AVG[6]	W	
2117	float	_FFT_PL1_AVG[7]	W	
2119	float	_FFT_PL1_AVG[8]	W	
2121	float	_FFT_PL1_AVG[9]	W	
2123	float	_FFT_PL1_AVG[10]	W	
2125	float	_FFT_PL1_AVG[11]	W	
2127	float	_FFT_PL1_AVG[12]	W	
2129	float	_FFT_PL1_AVG[13]	W	
2131	float	_FFT_PL1_AVG[14]	W	
2133	float	_FFT_PL1_AVG[15]	W	
2135	float	_FFT_PL1_AVG[16]	W	
2137	float	_FFT_PL1_AVG[17]	W	
2139	float	_FFT_PL1_AVG[18]	W	
2141	float	_FFT_PL1_AVG[19]	W	
2143	float	_FFT_PL1_AVG[20]	W	
2145	float	_FFT_PL1_AVG[21]	W	
2147	float	_FFT_PL1_AVG[22]	W	
2149	float	_FFT_PL1_AVG[23]	W	
2151	float	_FFT_PL1_AVG[24]	W	
2153	float	_FFT_PL1_AVG[25]	W	
2155	float	_FFT_PL1_AVG[26]	W	
2157	float	_FFT_PL1_AVG[27]	W	
2159	float	_FFT_PL1_AVG[28]	W	
2161	float	_FFT_PL1_AVG[29]	W	
2163	float	_FFT_PL1_AVG[30]	W	
2165	float	_FFT_PL1_AVG[31]	W	
2167	float	_FFT_PL1_AVG[32]	W	
2169	float	_FFT_PL1_AVG[33]	W	
2171	float	_FFT_PL1_AVG[34]	W	
2173	float	_FFT_PL1_AVG[35]	W	
2175	float	_FFT_PL1_AVG[36]	W	
2177	float	_FFT_PL1_AVG[37]	W	
2179	float	_FFT_PL1_AVG[38]	W	
2181	float	_FFT_PL1_AVG[39]	W	
2183	float	_FFT_PL2_AVG[0]	W	
2185	float	_FFT_PL2_AVG[1]	W	
2187	float	_FFT_PL2_AVG[2]	W	
2189	float	_FFT_PL2_AVG[3]	W	
2191	float	_FFT_PL2_AVG[4]	W	
2193	float	_FFT_PL2_AVG[5]	W	
2195	float	_FFT_PL2_AVG[6]	W	
2197	float	_FFT_PL2_AVG[7]	W	
2199	float	_FFT_PL2_AVG[8]	W	
2201	float	_FFT_PL2_AVG[9]	W	
2203	float	_FFT_PL2_AVG[10]	W	
2205	float	_FFT_PL2_AVG[11]	W	
2207	float	_FFT_PL2_AVG[12]	W	
2209	float	_FFT_PL2_AVG[13]	W	
2211	float	_FFT_PL2_AVG[14]	W	
2213	float	_FFT_PL2_AVG[15]	W	
2215	float	_FFT_PL2_AVG[16]	W	
2217	float	_FFT_PL2_AVG[17]	W	
2219	float	_FFT_PL2_AVG[18]	W	
2221	float	_FFT_PL2_AVG[19]	W	
2223	float	_FFT_PL2_AVG[20]	W	
2225	float	_FFT_PL2_AVG[21]	W	
2227	float	_FFT_PL2_AVG[22]	W	
2229	float	_FFT_PL2_AVG[23]	W	



Address	Format	Designation	Unit	Remarks
2231	float	_FFT_PL2_AVG[24]	W	
2233	float	_FFT_PL2_AVG[25]	W	
2235	float	_FFT_PL2_AVG[26]	W	
2237	float	_FFT_PL2_AVG[27]	W	
2239	float	_FFT_PL2_AVG[28]	W	
2241	float	_FFT_PL2_AVG[29]	W	
2243	float	_FFT_PL2_AVG[30]	W	
2245	float	_FFT_PL2_AVG[31]	W	
2247	float	_FFT_PL2_AVG[32]	W	
2249	float	_FFT_PL2_AVG[33]	W	
2251	float	_FFT_PL2_AVG[34]	W	
2253	float	_FFT_PL2_AVG[35]	W	
2255	float	_FFT_PL2_AVG[36]	W	
2257	float	_FFT_PL2_AVG[37]	W	
2259	float	_FFT_PL2_AVG[38]	W	
2261	float	_FFT_PL2_AVG[39]	W	
2263	float	_FFT_PL3_AVG[0]	W	
2265	float	_FFT_PL3_AVG[1]	W	
2267	float	_FFT_PL3_AVG[2]	W	
2269	float	_FFT_PL3_AVG[3]	W	
2271	float	_FFT_PL3_AVG[4]	W	
2273	float	_FFT_PL3_AVG[5]	W	
2275	float	_FFT_PL3_AVG[6]	W	
2277	float	_FFT_PL3_AVG[7]	W	
2279	float	_FFT_PL3_AVG[8]	W	
2281	float	_FFT_PL3_AVG[9]	W	
2283	float	_FFT_PL3_AVG[10]	W	
2285	float	_FFT_PL3_AVG[11]	W	
2287	float	_FFT_PL3_AVG[12]	W	
2289	float	_FFT_PL3_AVG[13]	W	
2291	float	_FFT_PL3_AVG[14]	W	
2293	float	_FFT_PL3_AVG[15]	W	
2295	float	_FFT_PL3_AVG[16]	W	
2297	float	_FFT_PL3_AVG[17]	W	
2299	float	_FFT_PL3_AVG[18]	W	
2301	float	_FFT_PL3_AVG[19]	W	
2303	float	_FFT_PL3_AVG[20]	W	
2305	float	_FFT_PL3_AVG[21]	W	
2307	float	_FFT_PL3_AVG[22]	W	
2309	float	_FFT_PL3_AVG[23]	W	
2311	float	_FFT_PL3_AVG[24]	W	
2313	float	_FFT_PL3_AVG[25]	W	
2315	float	_FFT_PL3_AVG[26]	W	
2317	float	_FFT_PL3_AVG[27]	W	
2319	float	_FFT_PL3_AVG[28]	W	
2321	float	_FFT_PL3_AVG[29]	W	
2323	float	_FFT_PL3_AVG[30]	W	
2325	float	_FFT_PL3_AVG[31]	W	
2327	float	_FFT_PL3_AVG[32]	W	
2329	float	_FFT_PL3_AVG[33]	W	
2331	float	_FFT_PL3_AVG[34]	W	
2333	float	_FFT_PL3_AVG[35]	W	
2335	float	_FFT_PL3_AVG[36]	W	
2337	float	_FFT_PL3_AVG[37]	W	
2339	float	_FFT_PL3_AVG[38]	W	
2341	float	_FFT_PL3_AVG[39]	W	
2343	float	_FFT_PL4_AVG[0]	W	
2345	float	_FFT_PL4_AVG[1]	W	
2347	float	_FFT_PL4_AVG[2]	W	

Address	Format	Designation	Unit	Remarks
2349	float	_FFT_PL4_AVG[3]	W	
2351	float	_FFT_PL4_AVG[4]	W	
2353	float	_FFT_PL4_AVG[5]	W	
2355	float	_FFT_PL4_AVG[6]	W	
2357	float	_FFT_PL4_AVG[7]	W	
2359	float	_FFT_PL4_AVG[8]	W	
2361	float	_FFT_PL4_AVG[9]	W	
2363	float	_FFT_PL4_AVG[10]	W	
2365	float	_FFT_PL4_AVG[11]	W	
2367	float	_FFT_PL4_AVG[12]	W	
2369	float	_FFT_PL4_AVG[13]	W	
2371	float	_FFT_PL4_AVG[14]	W	
2373	float	_FFT_PL4_AVG[15]	W	
2375	float	_FFT_PL4_AVG[16]	W	
2377	float	_FFT_PL4_AVG[17]	W	
2379	float	_FFT_PL4_AVG[18]	W	
2381	float	_FFT_PL4_AVG[19]	W	
2383	float	_FFT_PL4_AVG[20]	W	
2385	float	_FFT_PL4_AVG[21]	W	
2387	float	_FFT_PL4_AVG[22]	W	
2389	float	_FFT_PL4_AVG[23]	W	
2391	float	_FFT_PL4_AVG[24]	W	
2393	float	_FFT_PL4_AVG[25]	W	
2395	float	_FFT_PL4_AVG[26]	W	
2397	float	_FFT_PL4_AVG[27]	W	
2399	float	_FFT_PL4_AVG[28]	W	
2401	float	_FFT_PL4_AVG[29]	W	
2403	float	_FFT_PL4_AVG[30]	W	
2405	float	_FFT_PL4_AVG[31]	W	
2407	float	_FFT_PL4_AVG[32]	W	
2409	float	_FFT_PL4_AVG[33]	W	
2411	float	_FFT_PL4_AVG[34]	W	
2413	float	_FFT_PL4_AVG[35]	W	
2415	float	_FFT_PL4_AVG[36]	W	
2417	float	_FFT_PL4_AVG[37]	W	
2419	float	_FFT_PL4_AVG[38]	W	
2421	float	_FFT_PL4_AVG[39]	W	
2423	float	_FFT_QL1_AVG[0]	VAr	
2425	float	_FFT_QL1_AVG[1]	VAr	
2427	float	_FFT_QL1_AVG[2]	VAr	
2429	float	_FFT_QL1_AVG[3]	VAr	
2431	float	_FFT_QL1_AVG[4]	VAr	
2433	float	_FFT_QL1_AVG[5]	VAr	
2435	float	_FFT_QL1_AVG[6]	VAr	
2437	float	_FFT_QL1_AVG[7]	VAr	
2439	float	_FFT_QL1_AVG[8]	VAr	
2441	float	_FFT_QL1_AVG[9]	VAr	
2443	float	_FFT_QL1_AVG[10]	VAr	
2445	float	_FFT_QL1_AVG[11]	VAr	
2447	float	_FFT_QL1_AVG[12]	VAr	
2449	float	_FFT_QL1_AVG[13]	VAr	
2451	float	_FFT_QL1_AVG[14]	VAr	
2453	float	_FFT_QL1_AVG[15]	VAr	
2455	float	_FFT_QL1_AVG[16]	VAr	
2457	float	_FFT_QL1_AVG[17]	VAr	
2459	float	_FFT_QL1_AVG[18]	VAr	
2461	float	_FFT_QL1_AVG[19]	VAr	
2463	float	_FFT_QL1_AVG[20]	VAr	
2465	float	_FFT_QL1_AVG[21]	VAr	

Address	Format	Designation	Unit	Remarks
2467	float	_FFT_QL1_AVG[22]	VAr	
2469	float	_FFT_QL1_AVG[23]	VAr	
2471	float	_FFT_QL1_AVG[24]	VAr	
2473	float	_FFT_QL1_AVG[25]	VAr	
2475	float	_FFT_QL1_AVG[26]	VAr	
2477	float	_FFT_QL1_AVG[27]	VAr	
2479	float	_FFT_QL1_AVG[28]	VAr	
2481	float	_FFT_QL1_AVG[29]	VAr	
2483	float	_FFT_QL1_AVG[30]	VAr	
2485	float	_FFT_QL1_AVG[31]	VAr	
2487	float	_FFT_QL1_AVG[32]	VAr	
2489	float	_FFT_QL1_AVG[33]	VAr	
2491	float	_FFT_QL1_AVG[34]	VAr	
2493	float	_FFT_QL1_AVG[35]	VAr	
2495	float	_FFT_QL1_AVG[36]	VAr	
2497	float	_FFT_QL1_AVG[37]	VAr	
2499	float	_FFT_QL1_AVG[38]	VAr	
2501	float	_FFT_QL1_AVG[39]	VAr	
2503	float	_FFT_QL2_AVG[0]	VAr	
2505	float	_FFT_QL2_AVG[1]	VAr	
2507	float	_FFT_QL2_AVG[2]	VAr	
2509	float	_FFT_QL2_AVG[3]	VAr	
2511	float	_FFT_QL2_AVG[4]	VAr	
2513	float	_FFT_QL2_AVG[5]	VAr	
2515	float	_FFT_QL2_AVG[6]	VAr	
2517	float	_FFT_QL2_AVG[7]	VAr	
2519	float	_FFT_QL2_AVG[8]	VAr	
2521	float	_FFT_QL2_AVG[9]	VAr	
2523	float	_FFT_QL2_AVG[10]	VAr	
2525	float	_FFT_QL2_AVG[11]	VAr	
2527	float	_FFT_QL2_AVG[12]	VAr	
2529	float	_FFT_QL2_AVG[13]	VAr	
2531	float	_FFT_QL2_AVG[14]	VAr	
2533	float	_FFT_QL2_AVG[15]	VAr	
2535	float	_FFT_QL2_AVG[16]	VAr	
2537	float	_FFT_QL2_AVG[17]	VAr	
2539	float	_FFT_QL2_AVG[18]	VAr	
2541	float	_FFT_QL2_AVG[19]	VAr	
2543	float	_FFT_QL2_AVG[20]	VAr	
2545	float	_FFT_QL2_AVG[21]	VAr	
2547	float	_FFT_QL2_AVG[22]	VAr	
2549	float	_FFT_QL2_AVG[23]	VAr	
2551	float	_FFT_QL2_AVG[24]	VAr	
2553	float	_FFT_QL2_AVG[25]	VAr	
2555	float	_FFT_QL2_AVG[26]	VAr	
2557	float	_FFT_QL2_AVG[27]	VAr	
2559	float	_FFT_QL2_AVG[28]	VAr	
2561	float	_FFT_QL2_AVG[29]	VAr	
2563	float	_FFT_QL2_AVG[30]	VAr	
2565	float	_FFT_QL2_AVG[31]	VAr	
2567	float	_FFT_QL2_AVG[32]	VAr	
2569	float	_FFT_QL2_AVG[33]	VAr	
2571	float	_FFT_QL2_AVG[34]	VAr	
2573	float	_FFT_QL2_AVG[35]	VAr	
2575	float	_FFT_QL2_AVG[36]	VAr	
2577	float	_FFT_QL2_AVG[37]	VAr	
2579	float	_FFT_QL2_AVG[38]	VAr	
2581	float	_FFT_QL2_AVG[39]	VAr	
2583	float	_FFT_QL3_AVG[0]	VAr	

Address	Format	Designation	Unit	Remarks
2585	float	_FFT_QL3_AVG[1]	VAr	
2587	float	_FFT_QL3_AVG[2]	VAr	
2589	float	_FFT_QL3_AVG[3]	VAr	
2591	float	_FFT_QL3_AVG[4]	VAr	
2593	float	_FFT_QL3_AVG[5]	VAr	
2595	float	_FFT_QL3_AVG[6]	VAr	
2597	float	_FFT_QL3_AVG[7]	VAr	
2599	float	_FFT_QL3_AVG[8]	VAr	
2601	float	_FFT_QL3_AVG[9]	VAr	
2603	float	_FFT_QL3_AVG[10]	VAr	
2605	float	_FFT_QL3_AVG[11]	VAr	
2607	float	_FFT_QL3_AVG[12]	VAr	
2609	float	_FFT_QL3_AVG[13]	VAr	
2611	float	_FFT_QL3_AVG[14]	VAr	
2613	float	_FFT_QL3_AVG[15]	VAr	
2615	float	_FFT_QL3_AVG[16]	VAr	
2617	float	_FFT_QL3_AVG[17]	VAr	
2619	float	_FFT_QL3_AVG[18]	VAr	
2621	float	_FFT_QL3_AVG[19]	VAr	
2623	float	_FFT_QL3_AVG[20]	VAr	
2625	float	_FFT_QL3_AVG[21]	VAr	
2627	float	_FFT_QL3_AVG[22]	VAr	
2629	float	_FFT_QL3_AVG[23]	VAr	
2631	float	_FFT_QL3_AVG[24]	VAr	
2633	float	_FFT_QL3_AVG[25]	VAr	
2635	float	_FFT_QL3_AVG[26]	VAr	
2637	float	_FFT_QL3_AVG[27]	VAr	
2639	float	_FFT_QL3_AVG[28]	VAr	
2641	float	_FFT_QL3_AVG[29]	VAr	
2643	float	_FFT_QL3_AVG[30]	VAr	
2645	float	_FFT_QL3_AVG[31]	VAr	
2647	float	_FFT_QL3_AVG[32]	VAr	
2649	float	_FFT_QL3_AVG[33]	VAr	
2651	float	_FFT_QL3_AVG[34]	VAr	
2653	float	_FFT_QL3_AVG[35]	VAr	
2655	float	_FFT_QL3_AVG[36]	VAr	
2657	float	_FFT_QL3_AVG[37]	VAr	
2659	float	_FFT_QL3_AVG[38]	VAr	
2661	float	_FFT_QL3_AVG[39]	VAr	
2663	float	_FFT_QL4_AVG[0]	VAr	
2665	float	_FFT_QL4_AVG[1]	VAr	
2667	float	_FFT_QL4_AVG[2]	VAr	
2669	float	_FFT_QL4_AVG[3]	VAr	
2671	float	_FFT_QL4_AVG[4]	VAr	
2673	float	_FFT_QL4_AVG[5]	VAr	
2675	float	_FFT_QL4_AVG[6]	VAr	
2677	float	_FFT_QL4_AVG[7]	VAr	
2679	float	_FFT_QL4_AVG[8]	VAr	
2681	float	_FFT_QL4_AVG[9]	VAr	
2683	float	_FFT_QL4_AVG[10]	VAr	
2685	float	_FFT_QL4_AVG[11]	VAr	
2687	float	_FFT_QL4_AVG[12]	VAr	
2689	float	_FFT_QL4_AVG[13]	VAr	
2691	float	_FFT_QL4_AVG[14]	VAr	
2693	float	_FFT_QL4_AVG[15]	VAr	
2695	float	_FFT_QL4_AVG[16]	VAr	
2697	float	_FFT_QL4_AVG[17]	VAr	
2699	float	_FFT_QL4_AVG[18]	VAr	
2701	float	_FFT_QL4_AVG[19]	VAr	

Address	Format	Designation	Unit	Remarks
2703	float	_FFT_QL4_AVG[20]	VAr	
2705	float	_FFT_QL4_AVG[21]	VAr	
2707	float	_FFT_QL4_AVG[22]	VAr	
2709	float	_FFT_QL4_AVG[23]	VAr	
2711	float	_FFT_QL4_AVG[24]	VAr	
2713	float	_FFT_QL4_AVG[25]	VAr	
2715	float	_FFT_QL4_AVG[26]	VAr	
2717	float	_FFT_QL4_AVG[27]	VAr	
2719	float	_FFT_QL4_AVG[28]	VAr	
2721	float	_FFT_QL4_AVG[29]	VAr	
2723	float	_FFT_QL4_AVG[30]	VAr	
2725	float	_FFT_QL4_AVG[31]	VAr	
2727	float	_FFT_QL4_AVG[32]	VAr	
2729	float	_FFT_QL4_AVG[33]	VAr	
2731	float	_FFT_QL4_AVG[34]	VAr	
2733	float	_FFT_QL4_AVG[35]	VAr	
2735	float	_FFT_QL4_AVG[36]	VAr	
2737	float	_FFT_QL4_AVG[37]	VAr	
2739	float	_FFT_QL4_AVG[38]	VAr	
2741	float	_FFT_QL4_AVG[39]	VAr	
2743	float	_THD_ULN_AVG[0]	%	
2745	float	_THD_ULN_AVG[1]	%	
2747	float	_THD_ULN_AVG[2]	%	
2749	float	_THD_ULN_AVG[3]	%	
2751	float	_THD_ILN_AVG[0]	%	
2753	float	_THD_ILN_AVG[1]	%	
2755	float	_THD_ILN_AVG[2]	%	
2757	float	_THD_ILN_AVG[3]	%	
2759	float	_KFACT_AVG[0]		
2761	float	_KFACT_AVG[1]		
2763	float	_KFACT_AVG[2]		
2765	float	_KFACT_AVG[3]		
2767	float	_ULN_AVG[0]	V	
2769	float	_ULN_AVG[1]	V	
2771	float	_ULN_AVG[2]	V	
2773	float	_ULN_AVG[3]	V	
2775	float	_ILN_AVG[0]	A	
2777	float	_ILN_AVG[1]	A	
2779	float	_ILN_AVG[2]	A	
2781	float	_ILN_AVG[3]	A	
2783	float	_PLN_AVG[0]	W	
2785	float	_PLN_AVG[1]	W	
2787	float	_PLN_AVG[2]	W	
2789	float	_PLN_AVG[3]	W	
2791	float	_QLN_AVG[0]	VAr	
2793	float	_QLN_AVG[1]	VAr	
2795	float	_QLN_AVG[2]	VAr	
2797	float	_QLN_AVG[3]	VAr	
2799	float	_SLN_AVG[0]	VA	
2801	float	_SLN_AVG[1]	VA	
2803	float	_SLN_AVG[2]	VA	
2805	float	_SLN_AVG[3]	VA	
2807	float	_ULL_AVG[0]	V	
2809	float	_ULL_AVG[1]	V	
2811	float	_ULL_AVG[2]	V	
2813	float	_I_SUM3_AVG	A	
2815	float	_I_SUM_AVG	A	
2817	float	_S_SUM3_AVG	VA	
2819	float	_P_SUM3_AVG	W	

Address	Format	Designation	Unit	Remarks
2821	float	_Q_SUM3_AVG	VAr	
2823	float	_S_SUM_AVG	VA	
2825	float	_P_SUM_AVG	W	
2827	float	_Q_SUM_AVG	VAr	
2829	float	_FREQ_AVG	Hz	
2831	float	_N_AVG	V	
2833	float	_M_AVG	V	
2835	float	_G_AVG	V	
2837	float	_SYM_AVG	%	
2839	float	_IN_AVG	A	
2841	float	_IM_AVG	A	
2843	float	_IG_AVG	A	
2845	float	_S0_POWER_AVG[0]	W	
2847	float	_S0_POWER_AVG[1]	W	
2849	float	_EXT_TEMPERATUR_AVG	°C	
2851	float	_FFT_UL1_MIN[0]	V	
2853	float	_FFT_UL1_MIN[1]	V	
2855	float	_FFT_UL1_MIN[2]	V	
2857	float	_FFT_UL1_MIN[3]	V	
2859	float	_FFT_UL1_MIN[4]	V	
2861	float	_FFT_UL1_MIN[5]	V	
2863	float	_FFT_UL1_MIN[6]	V	
2865	float	_FFT_UL1_MIN[7]	V	
2867	float	_FFT_UL1_MIN[8]	V	
2869	float	_FFT_UL1_MIN[9]	V	
2871	float	_FFT_UL1_MIN[10]	V	
2873	float	_FFT_UL1_MIN[11]	V	
2875	float	_FFT_UL1_MIN[12]	V	
2877	float	_FFT_UL1_MIN[13]	V	
2879	float	_FFT_UL1_MIN[14]	V	
2881	float	_FFT_UL1_MIN[15]	V	
2883	float	_FFT_UL1_MIN[16]	V	
2885	float	_FFT_UL1_MIN[17]	V	
2887	float	_FFT_UL1_MIN[18]	V	
2889	float	_FFT_UL1_MIN[19]	V	
2891	float	_FFT_UL1_MIN[20]	V	
2893	float	_FFT_UL1_MIN[21]	V	
2895	float	_FFT_UL1_MIN[22]	V	
2897	float	_FFT_UL1_MIN[23]	V	
2899	float	_FFT_UL1_MIN[24]	V	
2901	float	_FFT_UL1_MIN[25]	V	
2903	float	_FFT_UL1_MIN[26]	V	
2905	float	_FFT_UL1_MIN[27]	V	
2907	float	_FFT_UL1_MIN[28]	V	
2909	float	_FFT_UL1_MIN[29]	V	
2911	float	_FFT_UL1_MIN[30]	V	
2913	float	_FFT_UL1_MIN[31]	V	
2915	float	_FFT_UL1_MIN[32]	V	
2917	float	_FFT_UL1_MIN[33]	V	
2919	float	_FFT_UL1_MIN[34]	V	
2921	float	_FFT_UL1_MIN[35]	V	
2923	float	_FFT_UL1_MIN[36]	V	
2925	float	_FFT_UL1_MIN[37]	V	
2927	float	_FFT_UL1_MIN[38]	V	
2929	float	_FFT_UL1_MIN[39]	V	
2931	float	_FFT_UL2_MIN[0]	V	
2933	float	_FFT_UL2_MIN[1]	V	
2935	float	_FFT_UL2_MIN[2]	V	
2937	float	_FFT_UL2_MIN[3]	V	

Address	Format	Designation	Unit	Remarks
2939	float	_FFT_UL2_MIN[4]	V	
2941	float	_FFT_UL2_MIN[5]	V	
2943	float	_FFT_UL2_MIN[6]	V	
2945	float	_FFT_UL2_MIN[7]	V	
2947	float	_FFT_UL2_MIN[8]	V	
2949	float	_FFT_UL2_MIN[9]	V	
2951	float	_FFT_UL2_MIN[10]	V	
2953	float	_FFT_UL2_MIN[11]	V	
2955	float	_FFT_UL2_MIN[12]	V	
2957	float	_FFT_UL2_MIN[13]	V	
2959	float	_FFT_UL2_MIN[14]	V	
2961	float	_FFT_UL2_MIN[15]	V	
2963	float	_FFT_UL2_MIN[16]	V	
2965	float	_FFT_UL2_MIN[17]	V	
2967	float	_FFT_UL2_MIN[18]	V	
2969	float	_FFT_UL2_MIN[19]	V	
2971	float	_FFT_UL2_MIN[20]	V	
2973	float	_FFT_UL2_MIN[21]	V	
2975	float	_FFT_UL2_MIN[22]	V	
2977	float	_FFT_UL2_MIN[23]	V	
2979	float	_FFT_UL2_MIN[24]	V	
2981	float	_FFT_UL2_MIN[25]	V	
2983	float	_FFT_UL2_MIN[26]	V	
2985	float	_FFT_UL2_MIN[27]	V	
2987	float	_FFT_UL2_MIN[28]	V	
2989	float	_FFT_UL2_MIN[29]	V	
2991	float	_FFT_UL2_MIN[30]	V	
2993	float	_FFT_UL2_MIN[31]	V	
2995	float	_FFT_UL2_MIN[32]	V	
2997	float	_FFT_UL2_MIN[33]	V	
2999	float	_FFT_UL2_MIN[34]	V	
3001	float	_FFT_UL2_MIN[35]	V	
3003	float	_FFT_UL2_MIN[36]	V	
3005	float	_FFT_UL2_MIN[37]	V	
3007	float	_FFT_UL2_MIN[38]	V	
3009	float	_FFT_UL2_MIN[39]	V	
3011	float	_FFT_UL3_MIN[0]	V	
3013	float	_FFT_UL3_MIN[1]	V	
3015	float	_FFT_UL3_MIN[2]	V	
3017	float	_FFT_UL3_MIN[3]	V	
3019	float	_FFT_UL3_MIN[4]	V	
3021	float	_FFT_UL3_MIN[5]	V	
3023	float	_FFT_UL3_MIN[6]	V	
3025	float	_FFT_UL3_MIN[7]	V	
3027	float	_FFT_UL3_MIN[8]	V	
3029	float	_FFT_UL3_MIN[9]	V	
3031	float	_FFT_UL3_MIN[10]	V	
3033	float	_FFT_UL3_MIN[11]	V	
3035	float	_FFT_UL3_MIN[12]	V	
3037	float	_FFT_UL3_MIN[13]	V	
3039	float	_FFT_UL3_MIN[14]	V	
3041	float	_FFT_UL3_MIN[15]	V	
3043	float	_FFT_UL3_MIN[16]	V	
3045	float	_FFT_UL3_MIN[17]	V	
3047	float	_FFT_UL3_MIN[18]	V	
3049	float	_FFT_UL3_MIN[19]	V	
3051	float	_FFT_UL3_MIN[20]	V	
3053	float	_FFT_UL3_MIN[21]	V	
3055	float	_FFT_UL3_MIN[22]	V	



Address	Format	Designation	Unit	Remarks
3057	float	_FFT_UL3_MIN[23]	V	
3059	float	_FFT_UL3_MIN[24]	V	
3061	float	_FFT_UL3_MIN[25]	V	
3063	float	_FFT_UL3_MIN[26]	V	
3065	float	_FFT_UL3_MIN[27]	V	
3067	float	_FFT_UL3_MIN[28]	V	
3069	float	_FFT_UL3_MIN[29]	V	
3071	float	_FFT_UL3_MIN[30]	V	
3073	float	_FFT_UL3_MIN[31]	V	
3075	float	_FFT_UL3_MIN[32]	V	
3077	float	_FFT_UL3_MIN[33]	V	
3079	float	_FFT_UL3_MIN[34]	V	
3081	float	_FFT_UL3_MIN[35]	V	
3083	float	_FFT_UL3_MIN[36]	V	
3085	float	_FFT_UL3_MIN[37]	V	
3087	float	_FFT_UL3_MIN[38]	V	
3089	float	_FFT_UL3_MIN[39]	V	
3091	float	_FFT_UL4_MIN[0]	V	
3093	float	_FFT_UL4_MIN[1]	V	
3095	float	_FFT_UL4_MIN[2]	V	
3097	float	_FFT_UL4_MIN[3]	V	
3099	float	_FFT_UL4_MIN[4]	V	
3101	float	_FFT_UL4_MIN[5]	V	
3103	float	_FFT_UL4_MIN[6]	V	
3105	float	_FFT_UL4_MIN[7]	V	
3107	float	_FFT_UL4_MIN[8]	V	
3109	float	_FFT_UL4_MIN[9]	V	
3111	float	_FFT_UL4_MIN[10]	V	
3113	float	_FFT_UL4_MIN[11]	V	
3115	float	_FFT_UL4_MIN[12]	V	
3117	float	_FFT_UL4_MIN[13]	V	
3119	float	_FFT_UL4_MIN[14]	V	
3121	float	_FFT_UL4_MIN[15]	V	
3123	float	_FFT_UL4_MIN[16]	V	
3125	float	_FFT_UL4_MIN[17]	V	
3127	float	_FFT_UL4_MIN[18]	V	
3129	float	_FFT_UL4_MIN[19]	V	
3131	float	_FFT_UL4_MIN[20]	V	
3133	float	_FFT_UL4_MIN[21]	V	
3135	float	_FFT_UL4_MIN[22]	V	
3137	float	_FFT_UL4_MIN[23]	V	
3139	float	_FFT_UL4_MIN[24]	V	
3141	float	_FFT_UL4_MIN[25]	V	
3143	float	_FFT_UL4_MIN[26]	V	
3145	float	_FFT_UL4_MIN[27]	V	
3147	float	_FFT_UL4_MIN[28]	V	
3149	float	_FFT_UL4_MIN[29]	V	
3151	float	_FFT_UL4_MIN[30]	V	
3153	float	_FFT_UL4_MIN[31]	V	
3155	float	_FFT_UL4_MIN[32]	V	
3157	float	_FFT_UL4_MIN[33]	V	
3159	float	_FFT_UL4_MIN[34]	V	
3161	float	_FFT_UL4_MIN[35]	V	
3163	float	_FFT_UL4_MIN[36]	V	
3165	float	_FFT_UL4_MIN[37]	V	
3167	float	_FFT_UL4_MIN[38]	V	
3169	float	_FFT_UL4_MIN[39]	V	
3171	float	_THD_ULN_MIN[0]	%	
3173	float	_THD_ULN_MIN[1]	%	



Address	Format	Designation	Unit	Remarks
3175	float	_THD_ULN_MIN[2]	%	
3177	float	_THD_ULN_MIN[3]	%	
3179	float	_ULN_MIN[0]	V	
3181	float	_ULN_MIN[1]	V	
3183	float	_ULN_MIN[2]	V	
3185	float	_ULN_MIN[3]	V	
3187	float	_ULL_MIN[0]	V	
3189	float	_ULL_MIN[1]	V	
3191	float	_ULL_MIN[2]	V	
3193	float	_FREQ_MIN	Hz	
3195	float	_N_MIN	V	
3197	float	_M_MIN	V	
3199	float	_G_MIN	V	
3201	float	_SYM_MIN	%	
3203	float	_EXT_TEMPERATUR_MIN	°C	
3205	float	_FFT_UL1_MAX[0]	V	
3207	float	_FFT_UL1_MAX[1]	V	
3209	float	_FFT_UL1_MAX[2]	V	
3211	float	_FFT_UL1_MAX[3]	V	
3213	float	_FFT_UL1_MAX[4]	V	
3215	float	_FFT_UL1_MAX[5]	V	
3217	float	_FFT_UL1_MAX[6]	V	
3219	float	_FFT_UL1_MAX[7]	V	
3221	float	_FFT_UL1_MAX[8]	V	
3223	float	_FFT_UL1_MAX[9]	V	
3225	float	_FFT_UL1_MAX[10]	V	
3227	float	_FFT_UL1_MAX[11]	V	
3229	float	_FFT_UL1_MAX[12]	V	
3231	float	_FFT_UL1_MAX[13]	V	
3233	float	_FFT_UL1_MAX[14]	V	
3235	float	_FFT_UL1_MAX[15]	V	
3237	float	_FFT_UL1_MAX[16]	V	
3239	float	_FFT_UL1_MAX[17]	V	
3241	float	_FFT_UL1_MAX[18]	V	
3243	float	_FFT_UL1_MAX[19]	V	
3245	float	_FFT_UL1_MAX[20]	V	
3247	float	_FFT_UL1_MAX[21]	V	
3249	float	_FFT_UL1_MAX[22]	V	
3251	float	_FFT_UL1_MAX[23]	V	
3253	float	_FFT_UL1_MAX[24]	V	
3255	float	_FFT_UL1_MAX[25]	V	
3257	float	_FFT_UL1_MAX[26]	V	
3259	float	_FFT_UL1_MAX[27]	V	
3261	float	_FFT_UL1_MAX[28]	V	
3263	float	_FFT_UL1_MAX[29]	V	
3265	float	_FFT_UL1_MAX[30]	V	
3267	float	_FFT_UL1_MAX[31]	V	
3269	float	_FFT_UL1_MAX[32]	V	
3271	float	_FFT_UL1_MAX[33]	V	
3273	float	_FFT_UL1_MAX[34]	V	
3275	float	_FFT_UL1_MAX[35]	V	
3277	float	_FFT_UL1_MAX[36]	V	
3279	float	_FFT_UL1_MAX[37]	V	
3281	float	_FFT_UL1_MAX[38]	V	
3283	float	_FFT_UL1_MAX[39]	V	
3285	float	_FFT_UL2_MAX[0]	V	
3287	float	_FFT_UL2_MAX[1]	V	
3289	float	_FFT_UL2_MAX[2]	V	
3291	float	_FFT_UL2_MAX[3]	V	

Address	Format	Designation	Unit	Remarks
3293	float	_FFT_UL2_MAX[4]	V	
3295	float	_FFT_UL2_MAX[5]	V	
3297	float	_FFT_UL2_MAX[6]	V	
3299	float	_FFT_UL2_MAX[7]	V	
3301	float	_FFT_UL2_MAX[8]	V	
3303	float	_FFT_UL2_MAX[9]	V	
3305	float	_FFT_UL2_MAX[10]	V	
3307	float	_FFT_UL2_MAX[11]	V	
3309	float	_FFT_UL2_MAX[12]	V	
3311	float	_FFT_UL2_MAX[13]	V	
3313	float	_FFT_UL2_MAX[14]	V	
3315	float	_FFT_UL2_MAX[15]	V	
3317	float	_FFT_UL2_MAX[16]	V	
3319	float	_FFT_UL2_MAX[17]	V	
3321	float	_FFT_UL2_MAX[18]	V	
3323	float	_FFT_UL2_MAX[19]	V	
3325	float	_FFT_UL2_MAX[20]	V	
3327	float	_FFT_UL2_MAX[21]	V	
3329	float	_FFT_UL2_MAX[22]	V	
3331	float	_FFT_UL2_MAX[23]	V	
3333	float	_FFT_UL2_MAX[24]	V	
3335	float	_FFT_UL2_MAX[25]	V	
3337	float	_FFT_UL2_MAX[26]	V	
3339	float	_FFT_UL2_MAX[27]	V	
3341	float	_FFT_UL2_MAX[28]	V	
3343	float	_FFT_UL2_MAX[29]	V	
3345	float	_FFT_UL2_MAX[30]	V	
3347	float	_FFT_UL2_MAX[31]	V	
3349	float	_FFT_UL2_MAX[32]	V	
3351	float	_FFT_UL2_MAX[33]	V	
3353	float	_FFT_UL2_MAX[34]	V	
3355	float	_FFT_UL2_MAX[35]	V	
3357	float	_FFT_UL2_MAX[36]	V	
3359	float	_FFT_UL2_MAX[37]	V	
3361	float	_FFT_UL2_MAX[38]	V	
3363	float	_FFT_UL2_MAX[39]	V	
3365	float	_FFT_UL3_MAX[0]	V	
3367	float	_FFT_UL3_MAX[1]	V	
3369	float	_FFT_UL3_MAX[2]	V	
3371	float	_FFT_UL3_MAX[3]	V	
3373	float	_FFT_UL3_MAX[4]	V	
3375	float	_FFT_UL3_MAX[5]	V	
3377	float	_FFT_UL3_MAX[6]	V	
3379	float	_FFT_UL3_MAX[7]	V	
3381	float	_FFT_UL3_MAX[8]	V	
3383	float	_FFT_UL3_MAX[9]	V	
3385	float	_FFT_UL3_MAX[10]	V	
3387	float	_FFT_UL3_MAX[11]	V	
3389	float	_FFT_UL3_MAX[12]	V	
3391	float	_FFT_UL3_MAX[13]	V	
3393	float	_FFT_UL3_MAX[14]	V	
3395	float	_FFT_UL3_MAX[15]	V	
3397	float	_FFT_UL3_MAX[16]	V	
3399	float	_FFT_UL3_MAX[17]	V	
3401	float	_FFT_UL3_MAX[18]	V	
3403	float	_FFT_UL3_MAX[19]	V	
3405	float	_FFT_UL3_MAX[20]	V	
3407	float	_FFT_UL3_MAX[21]	V	
3409	float	_FFT_UL3_MAX[22]	V	

Address	Format	Designation	Unit	Remarks
3411	float	_FFT_UL3_MAX[23]	V	
3413	float	_FFT_UL3_MAX[24]	V	
3415	float	_FFT_UL3_MAX[25]	V	
3417	float	_FFT_UL3_MAX[26]	V	
3419	float	_FFT_UL3_MAX[27]	V	
3421	float	_FFT_UL3_MAX[28]	V	
3423	float	_FFT_UL3_MAX[29]	V	
3425	float	_FFT_UL3_MAX[30]	V	
3427	float	_FFT_UL3_MAX[31]	V	
3429	float	_FFT_UL3_MAX[32]	V	
3431	float	_FFT_UL3_MAX[33]	V	
3433	float	_FFT_UL3_MAX[34]	V	
3435	float	_FFT_UL3_MAX[35]	V	
3437	float	_FFT_UL3_MAX[36]	V	
3439	float	_FFT_UL3_MAX[37]	V	
3441	float	_FFT_UL3_MAX[38]	V	
3443	float	_FFT_UL3_MAX[39]	V	
3445	float	_FFT_UL4_MAX[0]	V	
3447	float	_FFT_UL4_MAX[1]	V	
3449	float	_FFT_UL4_MAX[2]	V	
3451	float	_FFT_UL4_MAX[3]	V	
3453	float	_FFT_UL4_MAX[4]	V	
3455	float	_FFT_UL4_MAX[5]	V	
3457	float	_FFT_UL4_MAX[6]	V	
3459	float	_FFT_UL4_MAX[7]	V	
3461	float	_FFT_UL4_MAX[8]	V	
3463	float	_FFT_UL4_MAX[9]	V	
3465	float	_FFT_UL4_MAX[10]	V	
3467	float	_FFT_UL4_MAX[11]	V	
3469	float	_FFT_UL4_MAX[12]	V	
3471	float	_FFT_UL4_MAX[13]	V	
3473	float	_FFT_UL4_MAX[14]	V	
3475	float	_FFT_UL4_MAX[15]	V	
3477	float	_FFT_UL4_MAX[16]	V	
3479	float	_FFT_UL4_MAX[17]	V	
3481	float	_FFT_UL4_MAX[18]	V	
3483	float	_FFT_UL4_MAX[19]	V	
3485	float	_FFT_UL4_MAX[20]	V	
3487	float	_FFT_UL4_MAX[21]	V	
3489	float	_FFT_UL4_MAX[22]	V	
3491	float	_FFT_UL4_MAX[23]	V	
3493	float	_FFT_UL4_MAX[24]	V	
3495	float	_FFT_UL4_MAX[25]	V	
3497	float	_FFT_UL4_MAX[26]	V	
3499	float	_FFT_UL4_MAX[27]	V	
3501	float	_FFT_UL4_MAX[28]	V	
3503	float	_FFT_UL4_MAX[29]	V	
3505	float	_FFT_UL4_MAX[30]	V	
3507	float	_FFT_UL4_MAX[31]	V	
3509	float	_FFT_UL4_MAX[32]	V	
3511	float	_FFT_UL4_MAX[33]	V	
3513	float	_FFT_UL4_MAX[34]	V	
3515	float	_FFT_UL4_MAX[35]	V	
3517	float	_FFT_UL4_MAX[36]	V	
3519	float	_FFT_UL4_MAX[37]	V	
3521	float	_FFT_UL4_MAX[38]	V	
3523	float	_FFT_UL4_MAX[39]	V	
3525	float	_FFT_IL1_MAX[0]	A	
3527	float	_FFT_IL1_MAX[1]	A	

Address	Format	Designation	Unit	Remarks
3529	float	_FFT_IL1_MAX[2]	A	
3531	float	_FFT_IL1_MAX[3]	A	
3533	float	_FFT_IL1_MAX[4]	A	
3535	float	_FFT_IL1_MAX[5]	A	
3537	float	_FFT_IL1_MAX[6]	A	
3539	float	_FFT_IL1_MAX[7]	A	
3541	float	_FFT_IL1_MAX[8]	A	
3543	float	_FFT_IL1_MAX[9]	A	
3545	float	_FFT_IL1_MAX[10]	A	
3547	float	_FFT_IL1_MAX[11]	A	
3549	float	_FFT_IL1_MAX[12]	A	
3551	float	_FFT_IL1_MAX[13]	A	
3553	float	_FFT_IL1_MAX[14]	A	
3555	float	_FFT_IL1_MAX[15]	A	
3557	float	_FFT_IL1_MAX[16]	A	
3559	float	_FFT_IL1_MAX[17]	A	
3561	float	_FFT_IL1_MAX[18]	A	
3563	float	_FFT_IL1_MAX[19]	A	
3565	float	_FFT_IL1_MAX[20]	A	
3567	float	_FFT_IL1_MAX[21]	A	
3569	float	_FFT_IL1_MAX[22]	A	
3571	float	_FFT_IL1_MAX[23]	A	
3573	float	_FFT_IL1_MAX[24]	A	
3575	float	_FFT_IL1_MAX[25]	A	
3577	float	_FFT_IL1_MAX[26]	A	
3579	float	_FFT_IL1_MAX[27]	A	
3581	float	_FFT_IL1_MAX[28]	A	
3583	float	_FFT_IL1_MAX[29]	A	
3585	float	_FFT_IL1_MAX[30]	A	
3587	float	_FFT_IL1_MAX[31]	A	
3589	float	_FFT_IL1_MAX[32]	A	
3591	float	_FFT_IL1_MAX[33]	A	
3593	float	_FFT_IL1_MAX[34]	A	
3595	float	_FFT_IL1_MAX[35]	A	
3597	float	_FFT_IL1_MAX[36]	A	
3599	float	_FFT_IL1_MAX[37]	A	
3601	float	_FFT_IL1_MAX[38]	A	
3603	float	_FFT_IL1_MAX[39]	A	
3605	float	_FFT_IL2_MAX[0]	A	
3607	float	_FFT_IL2_MAX[1]	A	
3609	float	_FFT_IL2_MAX[2]	A	
3611	float	_FFT_IL2_MAX[3]	A	
3613	float	_FFT_IL2_MAX[4]	A	
3615	float	_FFT_IL2_MAX[5]	A	
3617	float	_FFT_IL2_MAX[6]	A	
3619	float	_FFT_IL2_MAX[7]	A	
3621	float	_FFT_IL2_MAX[8]	A	
3623	float	_FFT_IL2_MAX[9]	A	
3625	float	_FFT_IL2_MAX[10]	A	
3627	float	_FFT_IL2_MAX[11]	A	
3629	float	_FFT_IL2_MAX[12]	A	
3631	float	_FFT_IL2_MAX[13]	A	
3633	float	_FFT_IL2_MAX[14]	A	
3635	float	_FFT_IL2_MAX[15]	A	
3637	float	_FFT_IL2_MAX[16]	A	
3639	float	_FFT_IL2_MAX[17]	A	
3641	float	_FFT_IL2_MAX[18]	A	
3643	float	_FFT_IL2_MAX[19]	A	
3645	float	_FFT_IL2_MAX[20]	A	

Address	Format	Designation	Unit	Remarks
3647	float	_FFT_IL2_MAX[21]	A	
3649	float	_FFT_IL2_MAX[22]	A	
3651	float	_FFT_IL2_MAX[23]	A	
3653	float	_FFT_IL2_MAX[24]	A	
3655	float	_FFT_IL2_MAX[25]	A	
3657	float	_FFT_IL2_MAX[26]	A	
3659	float	_FFT_IL2_MAX[27]	A	
3661	float	_FFT_IL2_MAX[28]	A	
3663	float	_FFT_IL2_MAX[29]	A	
3665	float	_FFT_IL2_MAX[30]	A	
3667	float	_FFT_IL2_MAX[31]	A	
3669	float	_FFT_IL2_MAX[32]	A	
3671	float	_FFT_IL2_MAX[33]	A	
3673	float	_FFT_IL2_MAX[34]	A	
3675	float	_FFT_IL2_MAX[35]	A	
3677	float	_FFT_IL2_MAX[36]	A	
3679	float	_FFT_IL2_MAX[37]	A	
3681	float	_FFT_IL2_MAX[38]	A	
3683	float	_FFT_IL2_MAX[39]	A	
3685	float	_FFT_IL3_MAX[0]	A	
3687	float	_FFT_IL3_MAX[1]	A	
3689	float	_FFT_IL3_MAX[2]	A	
3691	float	_FFT_IL3_MAX[3]	A	
3693	float	_FFT_IL3_MAX[4]	A	
3695	float	_FFT_IL3_MAX[5]	A	
3697	float	_FFT_IL3_MAX[6]	A	
3699	float	_FFT_IL3_MAX[7]	A	
3701	float	_FFT_IL3_MAX[8]	A	
3703	float	_FFT_IL3_MAX[9]	A	
3705	float	_FFT_IL3_MAX[10]	A	
3707	float	_FFT_IL3_MAX[11]	A	
3709	float	_FFT_IL3_MAX[12]	A	
3711	float	_FFT_IL3_MAX[13]	A	
3713	float	_FFT_IL3_MAX[14]	A	
3715	float	_FFT_IL3_MAX[15]	A	
3717	float	_FFT_IL3_MAX[16]	A	
3719	float	_FFT_IL3_MAX[17]	A	
3721	float	_FFT_IL3_MAX[18]	A	
3723	float	_FFT_IL3_MAX[19]	A	
3725	float	_FFT_IL3_MAX[20]	A	
3727	float	_FFT_IL3_MAX[21]	A	
3729	float	_FFT_IL3_MAX[22]	A	
3731	float	_FFT_IL3_MAX[23]	A	
3733	float	_FFT_IL3_MAX[24]	A	
3735	float	_FFT_IL3_MAX[25]	A	
3737	float	_FFT_IL3_MAX[26]	A	
3739	float	_FFT_IL3_MAX[27]	A	
3741	float	_FFT_IL3_MAX[28]	A	
3743	float	_FFT_IL3_MAX[29]	A	
3745	float	_FFT_IL3_MAX[30]	A	
3747	float	_FFT_IL3_MAX[31]	A	
3749	float	_FFT_IL3_MAX[32]	A	
3751	float	_FFT_IL3_MAX[33]	A	
3753	float	_FFT_IL3_MAX[34]	A	
3755	float	_FFT_IL3_MAX[35]	A	
3757	float	_FFT_IL3_MAX[36]	A	
3759	float	_FFT_IL3_MAX[37]	A	
3761	float	_FFT_IL3_MAX[38]	A	
3763	float	_FFT_IL3_MAX[39]	A	

Address	Format	Designation	Unit	Remarks
3765	float	_FFT_IL4_MAX[0]	A	
3767	float	_FFT_IL4_MAX[1]	A	
3769	float	_FFT_IL4_MAX[2]	A	
3771	float	_FFT_IL4_MAX[3]	A	
3773	float	_FFT_IL4_MAX[4]	A	
3775	float	_FFT_IL4_MAX[5]	A	
3777	float	_FFT_IL4_MAX[6]	A	
3779	float	_FFT_IL4_MAX[7]	A	
3781	float	_FFT_IL4_MAX[8]	A	
3783	float	_FFT_IL4_MAX[9]	A	
3785	float	_FFT_IL4_MAX[10]	A	
3787	float	_FFT_IL4_MAX[11]	A	
3789	float	_FFT_IL4_MAX[12]	A	
3791	float	_FFT_IL4_MAX[13]	A	
3793	float	_FFT_IL4_MAX[14]	A	
3795	float	_FFT_IL4_MAX[15]	A	
3797	float	_FFT_IL4_MAX[16]	A	
3799	float	_FFT_IL4_MAX[17]	A	
3801	float	_FFT_IL4_MAX[18]	A	
3803	float	_FFT_IL4_MAX[19]	A	
3805	float	_FFT_IL4_MAX[20]	A	
3807	float	_FFT_IL4_MAX[21]	A	
3809	float	_FFT_IL4_MAX[22]	A	
3811	float	_FFT_IL4_MAX[23]	A	
3813	float	_FFT_IL4_MAX[24]	A	
3815	float	_FFT_IL4_MAX[25]	A	
3817	float	_FFT_IL4_MAX[26]	A	
3819	float	_FFT_IL4_MAX[27]	A	
3821	float	_FFT_IL4_MAX[28]	A	
3823	float	_FFT_IL4_MAX[29]	A	
3825	float	_FFT_IL4_MAX[30]	A	
3827	float	_FFT_IL4_MAX[31]	A	
3829	float	_FFT_IL4_MAX[32]	A	
3831	float	_FFT_IL4_MAX[33]	A	
3833	float	_FFT_IL4_MAX[34]	A	
3835	float	_FFT_IL4_MAX[35]	A	
3837	float	_FFT_IL4_MAX[36]	A	
3839	float	_FFT_IL4_MAX[37]	A	
3841	float	_FFT_IL4_MAX[38]	A	
3843	float	_FFT_IL4_MAX[39]	A	
3845	float	_FFT_PL1_MAX[0]	W	
3847	float	_FFT_PL1_MAX[1]	W	
3849	float	_FFT_PL1_MAX[2]	W	
3851	float	_FFT_PL1_MAX[3]	W	
3853	float	_FFT_PL1_MAX[4]	W	
3855	float	_FFT_PL1_MAX[5]	W	
3857	float	_FFT_PL1_MAX[6]	W	
3859	float	_FFT_PL1_MAX[7]	W	
3861	float	_FFT_PL1_MAX[8]	W	
3863	float	_FFT_PL1_MAX[9]	W	
3865	float	_FFT_PL1_MAX[10]	W	
3867	float	_FFT_PL1_MAX[11]	W	
3869	float	_FFT_PL1_MAX[12]	W	
3871	float	_FFT_PL1_MAX[13]	W	
3873	float	_FFT_PL1_MAX[14]	W	
3875	float	_FFT_PL1_MAX[15]	W	
3877	float	_FFT_PL1_MAX[16]	W	
3879	float	_FFT_PL1_MAX[17]	W	
3881	float	_FFT_PL1_MAX[18]	W	

Address	Format	Designation	Unit	Remarks
3883	float	_FFT_PL1_MAX[19]	W	
3885	float	_FFT_PL1_MAX[20]	W	
3887	float	_FFT_PL1_MAX[21]	W	
3889	float	_FFT_PL1_MAX[22]	W	
3891	float	_FFT_PL1_MAX[23]	W	
3893	float	_FFT_PL1_MAX[24]	W	
3895	float	_FFT_PL1_MAX[25]	W	
3897	float	_FFT_PL1_MAX[26]	W	
3899	float	_FFT_PL1_MAX[27]	W	
3901	float	_FFT_PL1_MAX[28]	W	
3903	float	_FFT_PL1_MAX[29]	W	
3905	float	_FFT_PL1_MAX[30]	W	
3907	float	_FFT_PL1_MAX[31]	W	
3909	float	_FFT_PL1_MAX[32]	W	
3911	float	_FFT_PL1_MAX[33]	W	
3913	float	_FFT_PL1_MAX[34]	W	
3915	float	_FFT_PL1_MAX[35]	W	
3917	float	_FFT_PL1_MAX[36]	W	
3919	float	_FFT_PL1_MAX[37]	W	
3921	float	_FFT_PL1_MAX[38]	W	
3923	float	_FFT_PL1_MAX[39]	W	
3925	float	_FFT_PL2_MAX[0]	W	
3927	float	_FFT_PL2_MAX[1]	W	
3929	float	_FFT_PL2_MAX[2]	W	
3931	float	_FFT_PL2_MAX[3]	W	
3933	float	_FFT_PL2_MAX[4]	W	
3935	float	_FFT_PL2_MAX[5]	W	
3937	float	_FFT_PL2_MAX[6]	W	
3939	float	_FFT_PL2_MAX[7]	W	
3941	float	_FFT_PL2_MAX[8]	W	
3943	float	_FFT_PL2_MAX[9]	W	
3945	float	_FFT_PL2_MAX[10]	W	
3947	float	_FFT_PL2_MAX[11]	W	
3949	float	_FFT_PL2_MAX[12]	W	
3951	float	_FFT_PL2_MAX[13]	W	
3953	float	_FFT_PL2_MAX[14]	W	
3955	float	_FFT_PL2_MAX[15]	W	
3957	float	_FFT_PL2_MAX[16]	W	
3959	float	_FFT_PL2_MAX[17]	W	
3961	float	_FFT_PL2_MAX[18]	W	
3963	float	_FFT_PL2_MAX[19]	W	
3965	float	_FFT_PL2_MAX[20]	W	
3967	float	_FFT_PL2_MAX[21]	W	
3969	float	_FFT_PL2_MAX[22]	W	
3971	float	_FFT_PL2_MAX[23]	W	
3973	float	_FFT_PL2_MAX[24]	W	
3975	float	_FFT_PL2_MAX[25]	W	
3977	float	_FFT_PL2_MAX[26]	W	
3979	float	_FFT_PL2_MAX[27]	W	
3981	float	_FFT_PL2_MAX[28]	W	
3983	float	_FFT_PL2_MAX[29]	W	
3985	float	_FFT_PL2_MAX[30]	W	
3987	float	_FFT_PL2_MAX[31]	W	
3989	float	_FFT_PL2_MAX[32]	W	
3991	float	_FFT_PL2_MAX[33]	W	
3993	float	_FFT_PL2_MAX[34]	W	
3995	float	_FFT_PL2_MAX[35]	W	
3997	float	_FFT_PL2_MAX[36]	W	
3999	float	_FFT_PL2_MAX[37]	W	



Address	Format	Designation	Unit	Remarks
4001	float	_FFT_PL2_MAX[38]	W	
4003	float	_FFT_PL2_MAX[39]	W	
4005	float	_FFT_PL3_MAX[0]	W	
4007	float	_FFT_PL3_MAX[1]	W	
4009	float	_FFT_PL3_MAX[2]	W	
4011	float	_FFT_PL3_MAX[3]	W	
4013	float	_FFT_PL3_MAX[4]	W	
4015	float	_FFT_PL3_MAX[5]	W	
4017	float	_FFT_PL3_MAX[6]	W	
4019	float	_FFT_PL3_MAX[7]	W	
4021	float	_FFT_PL3_MAX[8]	W	
4023	float	_FFT_PL3_MAX[9]	W	
4025	float	_FFT_PL3_MAX[10]	W	
4027	float	_FFT_PL3_MAX[11]	W	
4029	float	_FFT_PL3_MAX[12]	W	
4031	float	_FFT_PL3_MAX[13]	W	
4033	float	_FFT_PL3_MAX[14]	W	
4035	float	_FFT_PL3_MAX[15]	W	
4037	float	_FFT_PL3_MAX[16]	W	
4039	float	_FFT_PL3_MAX[17]	W	
4041	float	_FFT_PL3_MAX[18]	W	
4043	float	_FFT_PL3_MAX[19]	W	
4045	float	_FFT_PL3_MAX[20]	W	
4047	float	_FFT_PL3_MAX[21]	W	
4049	float	_FFT_PL3_MAX[22]	W	
4051	float	_FFT_PL3_MAX[23]	W	
4053	float	_FFT_PL3_MAX[24]	W	
4055	float	_FFT_PL3_MAX[25]	W	
4057	float	_FFT_PL3_MAX[26]	W	
4059	float	_FFT_PL3_MAX[27]	W	
4061	float	_FFT_PL3_MAX[28]	W	
4063	float	_FFT_PL3_MAX[29]	W	
4065	float	_FFT_PL3_MAX[30]	W	
4067	float	_FFT_PL3_MAX[31]	W	
4069	float	_FFT_PL3_MAX[32]	W	
4071	float	_FFT_PL3_MAX[33]	W	
4073	float	_FFT_PL3_MAX[34]	W	
4075	float	_FFT_PL3_MAX[35]	W	
4077	float	_FFT_PL3_MAX[36]	W	
4079	float	_FFT_PL3_MAX[37]	W	
4081	float	_FFT_PL3_MAX[38]	W	
4083	float	_FFT_PL3_MAX[39]	W	
4085	float	_FFT_PL4_MAX[0]	W	
4087	float	_FFT_PL4_MAX[1]	W	
4089	float	_FFT_PL4_MAX[2]	W	
4091	float	_FFT_PL4_MAX[3]	W	
4093	float	_FFT_PL4_MAX[4]	W	
4095	float	_FFT_PL4_MAX[5]	W	
4097	float	_FFT_PL4_MAX[6]	W	
4099	float	_FFT_PL4_MAX[7]	W	
4101	float	_FFT_PL4_MAX[8]	W	
4103	float	_FFT_PL4_MAX[9]	W	
4105	float	_FFT_PL4_MAX[10]	W	
4107	float	_FFT_PL4_MAX[11]	W	
4109	float	_FFT_PL4_MAX[12]	W	
4111	float	_FFT_PL4_MAX[13]	W	
4113	float	_FFT_PL4_MAX[14]	W	
4115	float	_FFT_PL4_MAX[15]	W	
4117	float	_FFT_PL4_MAX[16]	W	



Address	Format	Designation	Unit	Remarks
4119	float	_FFT_PL4_MAX[17]	W	
4121	float	_FFT_PL4_MAX[18]	W	
4123	float	_FFT_PL4_MAX[19]	W	
4125	float	_FFT_PL4_MAX[20]	W	
4127	float	_FFT_PL4_MAX[21]	W	
4129	float	_FFT_PL4_MAX[22]	W	
4131	float	_FFT_PL4_MAX[23]	W	
4133	float	_FFT_PL4_MAX[24]	W	
4135	float	_FFT_PL4_MAX[25]	W	
4137	float	_FFT_PL4_MAX[26]	W	
4139	float	_FFT_PL4_MAX[27]	W	
4141	float	_FFT_PL4_MAX[28]	W	
4143	float	_FFT_PL4_MAX[29]	W	
4145	float	_FFT_PL4_MAX[30]	W	
4147	float	_FFT_PL4_MAX[31]	W	
4149	float	_FFT_PL4_MAX[32]	W	
4151	float	_FFT_PL4_MAX[33]	W	
4153	float	_FFT_PL4_MAX[34]	W	
4155	float	_FFT_PL4_MAX[35]	W	
4157	float	_FFT_PL4_MAX[36]	W	
4159	float	_FFT_PL4_MAX[37]	W	
4161	float	_FFT_PL4_MAX[38]	W	
4163	float	_FFT_PL4_MAX[39]	W	
4165	float	_FFT_QL1_MAX[0]	VAr	
4167	float	_FFT_QL1_MAX[1]	VAr	
4169	float	_FFT_QL1_MAX[2]	VAr	
4171	float	_FFT_QL1_MAX[3]	VAr	
4173	float	_FFT_QL1_MAX[4]	VAr	
4175	float	_FFT_QL1_MAX[5]	VAr	
4177	float	_FFT_QL1_MAX[6]	VAr	
4179	float	_FFT_QL1_MAX[7]	VAr	
4181	float	_FFT_QL1_MAX[8]	VAr	
4183	float	_FFT_QL1_MAX[9]	VAr	
4185	float	_FFT_QL1_MAX[10]	VAr	
4187	float	_FFT_QL1_MAX[11]	VAr	
4189	float	_FFT_QL1_MAX[12]	VAr	
4191	float	_FFT_QL1_MAX[13]	VAr	
4193	float	_FFT_QL1_MAX[14]	VAr	
4195	float	_FFT_QL1_MAX[15]	VAr	
4197	float	_FFT_QL1_MAX[16]	VAr	
4199	float	_FFT_QL1_MAX[17]	VAr	
4201	float	_FFT_QL1_MAX[18]	VAr	
4203	float	_FFT_QL1_MAX[19]	VAr	
4205	float	_FFT_QL1_MAX[20]	VAr	
4207	float	_FFT_QL1_MAX[21]	VAr	
4209	float	_FFT_QL1_MAX[22]	VAr	
4211	float	_FFT_QL1_MAX[23]	VAr	
4213	float	_FFT_QL1_MAX[24]	VAr	
4215	float	_FFT_QL1_MAX[25]	VAr	
4217	float	_FFT_QL1_MAX[26]	VAr	
4219	float	_FFT_QL1_MAX[27]	VAr	
4221	float	_FFT_QL1_MAX[28]	VAr	
4223	float	_FFT_QL1_MAX[29]	VAr	
4225	float	_FFT_QL1_MAX[30]	VAr	
4227	float	_FFT_QL1_MAX[31]	VAr	
4229	float	_FFT_QL1_MAX[32]	VAr	
4231	float	_FFT_QL1_MAX[33]	VAr	
4233	float	_FFT_QL1_MAX[34]	VAr	
4235	float	_FFT_QL1_MAX[35]	VAr	

Address	Format	Designation	Unit	Remarks
4237	float	_FFT_QL1_MAX[36]	VAr	
4239	float	_FFT_QL1_MAX[37]	VAr	
4241	float	_FFT_QL1_MAX[38]	VAr	
4243	float	_FFT_QL1_MAX[39]	VAr	
4245	float	_FFT_QL2_MAX[0]	VAr	
4247	float	_FFT_QL2_MAX[1]	VAr	
4249	float	_FFT_QL2_MAX[2]	VAr	
4251	float	_FFT_QL2_MAX[3]	VAr	
4253	float	_FFT_QL2_MAX[4]	VAr	
4255	float	_FFT_QL2_MAX[5]	VAr	
4257	float	_FFT_QL2_MAX[6]	VAr	
4259	float	_FFT_QL2_MAX[7]	VAr	
4261	float	_FFT_QL2_MAX[8]	VAr	
4263	float	_FFT_QL2_MAX[9]	VAr	
4265	float	_FFT_QL2_MAX[10]	VAr	
4267	float	_FFT_QL2_MAX[11]	VAr	
4269	float	_FFT_QL2_MAX[12]	VAr	
4271	float	_FFT_QL2_MAX[13]	VAr	
4273	float	_FFT_QL2_MAX[14]	VAr	
4275	float	_FFT_QL2_MAX[15]	VAr	
4277	float	_FFT_QL2_MAX[16]	VAr	
4279	float	_FFT_QL2_MAX[17]	VAr	
4281	float	_FFT_QL2_MAX[18]	VAr	
4283	float	_FFT_QL2_MAX[19]	VAr	
4285	float	_FFT_QL2_MAX[20]	VAr	
4287	float	_FFT_QL2_MAX[21]	VAr	
4289	float	_FFT_QL2_MAX[22]	VAr	
4291	float	_FFT_QL2_MAX[23]	VAr	
4293	float	_FFT_QL2_MAX[24]	VAr	
4295	float	_FFT_QL2_MAX[25]	VAr	
4297	float	_FFT_QL2_MAX[26]	VAr	
4299	float	_FFT_QL2_MAX[27]	VAr	
4301	float	_FFT_QL2_MAX[28]	VAr	
4303	float	_FFT_QL2_MAX[29]	VAr	
4305	float	_FFT_QL2_MAX[30]	VAr	
4307	float	_FFT_QL2_MAX[31]	VAr	
4309	float	_FFT_QL2_MAX[32]	VAr	
4311	float	_FFT_QL2_MAX[33]	VAr	
4313	float	_FFT_QL2_MAX[34]	VAr	
4315	float	_FFT_QL2_MAX[35]	VAr	
4317	float	_FFT_QL2_MAX[36]	VAr	
4319	float	_FFT_QL2_MAX[37]	VAr	
4321	float	_FFT_QL2_MAX[38]	VAr	
4323	float	_FFT_QL2_MAX[39]	VAr	
4325	float	_FFT_QL3_MAX[0]	VAr	
4327	float	_FFT_QL3_MAX[1]	VAr	
4329	float	_FFT_QL3_MAX[2]	VAr	
4331	float	_FFT_QL3_MAX[3]	VAr	
4333	float	_FFT_QL3_MAX[4]	VAr	
4335	float	_FFT_QL3_MAX[5]	VAr	
4337	float	_FFT_QL3_MAX[6]	VAr	
4339	float	_FFT_QL3_MAX[7]	VAr	
4341	float	_FFT_QL3_MAX[8]	VAr	
4343	float	_FFT_QL3_MAX[9]	VAr	
4345	float	_FFT_QL3_MAX[10]	VAr	
4347	float	_FFT_QL3_MAX[11]	VAr	
4349	float	_FFT_QL3_MAX[12]	VAr	
4351	float	_FFT_QL3_MAX[13]	VAr	
4353	float	_FFT_QL3_MAX[14]	VAr	

Address	Format	Designation	Unit	Remarks
4355	float	_FFT_QL3_MAX[15]	VAr	
4357	float	_FFT_QL3_MAX[16]	VAr	
4359	float	_FFT_QL3_MAX[17]	VAr	
4361	float	_FFT_QL3_MAX[18]	VAr	
4363	float	_FFT_QL3_MAX[19]	VAr	
4365	float	_FFT_QL3_MAX[20]	VAr	
4367	float	_FFT_QL3_MAX[21]	VAr	
4369	float	_FFT_QL3_MAX[22]	VAr	
4371	float	_FFT_QL3_MAX[23]	VAr	
4373	float	_FFT_QL3_MAX[24]	VAr	
4375	float	_FFT_QL3_MAX[25]	VAr	
4377	float	_FFT_QL3_MAX[26]	VAr	
4379	float	_FFT_QL3_MAX[27]	VAr	
4381	float	_FFT_QL3_MAX[28]	VAr	
4383	float	_FFT_QL3_MAX[29]	VAr	
4385	float	_FFT_QL3_MAX[30]	VAr	
4387	float	_FFT_QL3_MAX[31]	VAr	
4389	float	_FFT_QL3_MAX[32]	VAr	
4391	float	_FFT_QL3_MAX[33]	VAr	
4393	float	_FFT_QL3_MAX[34]	VAr	
4395	float	_FFT_QL3_MAX[35]	VAr	
4397	float	_FFT_QL3_MAX[36]	VAr	
4399	float	_FFT_QL3_MAX[37]	VAr	
4401	float	_FFT_QL3_MAX[38]	VAr	
4403	float	_FFT_QL3_MAX[39]	VAr	
4405	float	_FFT_QL4_MAX[0]	VAr	
4407	float	_FFT_QL4_MAX[1]	VAr	
4409	float	_FFT_QL4_MAX[2]	VAr	
4411	float	_FFT_QL4_MAX[3]	VAr	
4413	float	_FFT_QL4_MAX[4]	VAr	
4415	float	_FFT_QL4_MAX[5]	VAr	
4417	float	_FFT_QL4_MAX[6]	VAr	
4419	float	_FFT_QL4_MAX[7]	VAr	
4421	float	_FFT_QL4_MAX[8]	VAr	
4423	float	_FFT_QL4_MAX[9]	VAr	
4425	float	_FFT_QL4_MAX[10]	VAr	
4427	float	_FFT_QL4_MAX[11]	VAr	
4429	float	_FFT_QL4_MAX[12]	VAr	
4431	float	_FFT_QL4_MAX[13]	VAr	
4433	float	_FFT_QL4_MAX[14]	VAr	
4435	float	_FFT_QL4_MAX[15]	VAr	
4437	float	_FFT_QL4_MAX[16]	VAr	
4439	float	_FFT_QL4_MAX[17]	VAr	
4441	float	_FFT_QL4_MAX[18]	VAr	
4443	float	_FFT_QL4_MAX[19]	VAr	
4445	float	_FFT_QL4_MAX[20]	VAr	
4447	float	_FFT_QL4_MAX[21]	VAr	
4449	float	_FFT_QL4_MAX[22]	VAr	
4451	float	_FFT_QL4_MAX[23]	VAr	
4453	float	_FFT_QL4_MAX[24]	VAr	
4455	float	_FFT_QL4_MAX[25]	VAr	
4457	float	_FFT_QL4_MAX[26]	VAr	
4459	float	_FFT_QL4_MAX[27]	VAr	
4461	float	_FFT_QL4_MAX[28]	VAr	
4463	float	_FFT_QL4_MAX[29]	VAr	
4465	float	_FFT_QL4_MAX[30]	VAr	
4467	float	_FFT_QL4_MAX[31]	VAr	
4469	float	_FFT_QL4_MAX[32]	VAr	
4471	float	_FFT_QL4_MAX[33]	VAr	

Address	Format	Designation	Unit	Remarks
4473	float	_FFT_QL4_MAX[34]	VAr	
4475	float	_FFT_QL4_MAX[35]	VAr	
4477	float	_FFT_QL4_MAX[36]	VAr	
4479	float	_FFT_QL4_MAX[37]	VAr	
4481	float	_FFT_QL4_MAX[38]	VAr	
4483	float	_FFT_QL4_MAX[39]	VAr	
4485	float	_THD_ULN_MAX[0]	%	
4487	float	_THD_ULN_MAX[1]	%	
4489	float	_THD_ULN_MAX[2]	%	
4491	float	_THD_ULN_MAX[3]	%	
4493	float	_THD_ILN_MAX[0]	%	
4495	float	_THD_ILN_MAX[1]	%	
4497	float	_THD_ILN_MAX[2]	%	
4499	float	_THD_ILN_MAX[3]	%	
4501	float	_KFACT_MAX[0]		
4503	float	_KFACT_MAX[1]		
4505	float	_KFACT_MAX[2]		
4507	float	_KFACT_MAX[3]		
4509	float	_ULN_MAX[0]	V	
4511	float	_ULN_MAX[1]	V	
4513	float	_ULN_MAX[2]	V	
4515	float	_ULN_MAX[3]	V	
4517	float	_ILN_MAX[0]	A	
4519	float	_ILN_MAX[1]	A	
4521	float	_ILN_MAX[2]	A	
4523	float	_ILN_MAX[3]	A	
4525	float	_PLN_MAX[0]	W	
4527	float	_PLN_MAX[1]	W	
4529	float	_PLN_MAX[2]	W	
4531	float	_PLN_MAX[3]	W	
4533	float	_QLN_MAX[0]	VAr	
4535	float	_QLN_MAX[1]	VAr	
4537	float	_QLN_MAX[2]	VAr	
4539	float	_QLN_MAX[3]	VAr	
4541	float	_SLN_MAX[0]	VA	
4543	float	_SLN_MAX[1]	VA	
4545	float	_SLN_MAX[2]	VA	
4547	float	_SLN_MAX[3]	VA	
4549	float	_ULL_MAX[0]	V	
4551	float	_ULL_MAX[1]	V	
4553	float	_ULL_MAX[2]	V	
4555	float	_I_SUM3_MAX	A	
4557	float	_I_SUM_MAX	A	
4559	float	_S_SUM3_MAX	VA	
4561	float	_P_SUM3_MAX	W	
4563	float	_Q_SUM3_MAX	VAr	
4565	float	_S_SUM_MAX	VA	
4567	float	_P_SUM_MAX	W	
4569	float	_Q_SUM_MAX	VAr	
4571	float	_FREQ_MAX	Hz	
4573	float	_N_MAX	V	
4575	float	_M_MAX	V	
4577	float	_G_MAX	V	
4579	float	_SYM_MAX	%	
4581	float	_IN_MAX	A	
4583	float	_IM_MAX	A	
4585	float	_IG_MAX	A	
4587	float	_S0_POWER_MAX[0]	W	
4589	float	_S0_POWER_MAX[1]	W	

Address	Format	Designation	Unit	Remarks
4591	float	_EXT_TEMPERATUR_MAX	°C	
4593	short	_FFT_UL1_AVG_T[0]	n	Average time see table on page 2
4594	short	_FFT_UL1_AVG_T[1]	n	
4595	short	_FFT_UL1_AVG_T[2]	n	
4596	short	_FFT_UL1_AVG_T[3]	n	
4597	short	_FFT_UL1_AVG_T[4]	n	
4598	short	_FFT_UL1_AVG_T[5]	n	
4599	short	_FFT_UL1_AVG_T[6]	n	
4600	short	_FFT_UL1_AVG_T[7]	n	
4601	short	_FFT_UL1_AVG_T[8]	n	
4602	short	_FFT_UL1_AVG_T[9]	n	
4603	short	_FFT_UL1_AVG_T[10]	n	
4604	short	_FFT_UL1_AVG_T[11]	n	
4605	short	_FFT_UL1_AVG_T[12]	n	
4606	short	_FFT_UL1_AVG_T[13]	n	
4607	short	_FFT_UL1_AVG_T[14]	n	
4608	short	_FFT_UL1_AVG_T[15]	n	
4609	short	_FFT_UL1_AVG_T[16]	n	
4610	short	_FFT_UL1_AVG_T[17]	n	
4611	short	_FFT_UL1_AVG_T[18]	n	
4612	short	_FFT_UL1_AVG_T[19]	n	
4613	short	_FFT_UL1_AVG_T[20]	n	
4614	short	_FFT_UL1_AVG_T[21]	n	
4615	short	_FFT_UL1_AVG_T[22]	n	
4616	short	_FFT_UL1_AVG_T[23]	n	
4617	short	_FFT_UL1_AVG_T[24]	n	
4618	short	_FFT_UL1_AVG_T[25]	n	
4619	short	_FFT_UL1_AVG_T[26]	n	
4620	short	_FFT_UL1_AVG_T[27]	n	
4621	short	_FFT_UL1_AVG_T[28]	n	
4622	short	_FFT_UL1_AVG_T[29]	n	
4623	short	_FFT_UL1_AVG_T[30]	n	
4624	short	_FFT_UL1_AVG_T[31]	n	
4625	short	_FFT_UL1_AVG_T[32]	n	
4626	short	_FFT_UL1_AVG_T[33]	n	
4627	short	_FFT_UL1_AVG_T[34]	n	
4628	short	_FFT_UL1_AVG_T[35]	n	
4629	short	_FFT_UL1_AVG_T[36]	n	
4630	short	_FFT_UL1_AVG_T[37]	n	Average time see table on page 2
4631	short	_FFT_UL1_AVG_T[38]	n	
4632	short	_FFT_UL1_AVG_T[39]	n	
4633	short	_FFT_UL2_AVG_T[0]	n	
4634	short	_FFT_UL2_AVG_T[1]	n	
4635	short	_FFT_UL2_AVG_T[2]	n	
4636	short	_FFT_UL2_AVG_T[3]	n	
4637	short	_FFT_UL2_AVG_T[4]	n	
4638	short	_FFT_UL2_AVG_T[5]	n	
4639	short	_FFT_UL2_AVG_T[6]	n	
4640	short	_FFT_UL2_AVG_T[7]	n	
4641	short	_FFT_UL2_AVG_T[8]	n	
4642	short	_FFT_UL2_AVG_T[9]	n	
4643	short	_FFT_UL2_AVG_T[10]	n	
4644	short	_FFT_UL2_AVG_T[11]	n	
4645	short	_FFT_UL2_AVG_T[12]	n	
4646	short	_FFT_UL2_AVG_T[13]	n	
4647	short	_FFT_UL2_AVG_T[14]	n	
4648	short	_FFT_UL2_AVG_T[15]	n	
4649	short	_FFT_UL2_AVG_T[16]	n	
4650	short	_FFT_UL2_AVG_T[17]	n	

Address	Format	Designation	Unit	Remarks
4651	short	_FFT_UL2_AVG_T[18]	n	
4652	short	_FFT_UL2_AVG_T[19]	n	
4653	short	_FFT_UL2_AVG_T[20]	n	
4654	short	_FFT_UL2_AVG_T[21]	n	
4655	short	_FFT_UL2_AVG_T[22]	n	
4656	short	_FFT_UL2_AVG_T[23]	n	
4657	short	_FFT_UL2_AVG_T[24]	n	
4658	short	_FFT_UL2_AVG_T[25]	n	
4659	short	_FFT_UL2_AVG_T[26]	n	
4660	short	_FFT_UL2_AVG_T[27]	n	
4661	short	_FFT_UL2_AVG_T[28]	n	
4662	short	_FFT_UL2_AVG_T[29]	n	
4663	short	_FFT_UL2_AVG_T[30]	n	
4664	short	_FFT_UL2_AVG_T[31]	n	
4665	short	_FFT_UL2_AVG_T[32]	n	
4666	short	_FFT_UL2_AVG_T[33]	n	
4667	short	_FFT_UL2_AVG_T[34]	n	
4668	short	_FFT_UL2_AVG_T[35]	n	
4669	short	_FFT_UL2_AVG_T[36]	n	
4670	short	_FFT_UL2_AVG_T[37]	n	
4671	short	_FFT_UL2_AVG_T[38]	n	
4672	short	_FFT_UL2_AVG_T[39]	n	
4673	short	_FFT_UL3_AVG_T[0]	n	
4674	short	_FFT_UL3_AVG_T[1]	n	
4675	short	_FFT_UL3_AVG_T[2]	n	
4676	short	_FFT_UL3_AVG_T[3]	n	
4677	short	_FFT_UL3_AVG_T[4]	n	
4678	short	_FFT_UL3_AVG_T[5]	n	
4679	short	_FFT_UL3_AVG_T[6]	n	
4680	short	_FFT_UL3_AVG_T[7]	n	
4681	short	_FFT_UL3_AVG_T[8]	n	
4682	short	_FFT_UL3_AVG_T[9]	n	
4683	short	_FFT_UL3_AVG_T[10]	n	
4684	short	_FFT_UL3_AVG_T[11]	n	
4685	short	_FFT_UL3_AVG_T[12]	n	
4686	short	_FFT_UL3_AVG_T[13]	n	
4687	short	_FFT_UL3_AVG_T[14]	n	
4688	short	_FFT_UL3_AVG_T[15]	n	
4689	short	_FFT_UL3_AVG_T[16]	n	
4690	short	_FFT_UL3_AVG_T[17]	n	Average time see table on page 2
4691	short	_FFT_UL3_AVG_T[18]	n	
4692	short	_FFT_UL3_AVG_T[19]	n	
4693	short	_FFT_UL3_AVG_T[20]	n	
4694	short	_FFT_UL3_AVG_T[21]	n	
4695	short	_FFT_UL3_AVG_T[22]	n	
4696	short	_FFT_UL3_AVG_T[23]	n	
4697	short	_FFT_UL3_AVG_T[24]	n	
4698	short	_FFT_UL3_AVG_T[25]	n	
4699	short	_FFT_UL3_AVG_T[26]	n	
4700	short	_FFT_UL3_AVG_T[27]	n	
4701	short	_FFT_UL3_AVG_T[28]	n	
4702	short	_FFT_UL3_AVG_T[29]	n	
4703	short	_FFT_UL3_AVG_T[30]	n	
4704	short	_FFT_UL3_AVG_T[31]	n	
4705	short	_FFT_UL3_AVG_T[32]	n	
4706	short	_FFT_UL3_AVG_T[33]	n	
4707	short	_FFT_UL3_AVG_T[34]	n	
4708	short	_FFT_UL3_AVG_T[35]	n	
4709	short	_FFT_UL3_AVG_T[36]	n	

Address	Format	Designation	Unit	Remarks
4710	short	_FFT_UL3_AVG_T[37]	n	
4711	short	_FFT_UL3_AVG_T[38]	n	
4712	short	_FFT_UL3_AVG_T[39]	n	
4713	short	_FFT_UL4_AVG_T[0]	n	
4714	short	_FFT_UL4_AVG_T[1]	n	
4715	short	_FFT_UL4_AVG_T[2]	n	
4716	short	_FFT_UL4_AVG_T[3]	n	
4717	short	_FFT_UL4_AVG_T[4]	n	
4718	short	_FFT_UL4_AVG_T[5]	n	
4719	short	_FFT_UL4_AVG_T[6]	n	
4720	short	_FFT_UL4_AVG_T[7]	n	
4721	short	_FFT_UL4_AVG_T[8]	n	
4722	short	_FFT_UL4_AVG_T[9]	n	
4723	short	_FFT_UL4_AVG_T[10]	n	
4724	short	_FFT_UL4_AVG_T[11]	n	
4725	short	_FFT_UL4_AVG_T[12]	n	
4726	short	_FFT_UL4_AVG_T[13]	n	
4727	short	_FFT_UL4_AVG_T[14]	n	
4728	short	_FFT_UL4_AVG_T[15]	n	
4729	short	_FFT_UL4_AVG_T[16]	n	
4730	short	_FFT_UL4_AVG_T[17]	n	
4731	short	_FFT_UL4_AVG_T[18]	n	
4732	short	_FFT_UL4_AVG_T[19]	n	
4733	short	_FFT_UL4_AVG_T[20]	n	
4734	short	_FFT_UL4_AVG_T[21]	n	
4735	short	_FFT_UL4_AVG_T[22]	n	
4736	short	_FFT_UL4_AVG_T[23]	n	
4737	short	_FFT_UL4_AVG_T[24]	n	
4738	short	_FFT_UL4_AVG_T[25]	n	
4739	short	_FFT_UL4_AVG_T[26]	n	
4740	short	_FFT_UL4_AVG_T[27]	n	
4741	short	_FFT_UL4_AVG_T[28]	n	
4742	short	_FFT_UL4_AVG_T[29]	n	
4743	short	_FFT_UL4_AVG_T[30]	n	
4744	short	_FFT_UL4_AVG_T[31]	n	
4745	short	_FFT_UL4_AVG_T[32]	n	
4746	short	_FFT_UL4_AVG_T[33]	n	
4747	short	_FFT_UL4_AVG_T[34]	n	
4748	short	_FFT_UL4_AVG_T[35]	n	
4749	short	_FFT_UL4_AVG_T[36]	n	
4750	short	_FFT_UL4_AVG_T[37]	n	Average time see table on page 2
4751	short	_FFT_UL4_AVG_T[38]	n	
4752	short	_FFT_UL4_AVG_T[39]	n	
4753	short	_FFT_IL1_AVG_T[0]	n	
4754	short	_FFT_IL1_AVG_T[1]	n	
4755	short	_FFT_IL1_AVG_T[2]	n	
4756	short	_FFT_IL1_AVG_T[3]	n	
4757	short	_FFT_IL1_AVG_T[4]	n	
4758	short	_FFT_IL1_AVG_T[5]	n	
4759	short	_FFT_IL1_AVG_T[6]	n	
4760	short	_FFT_IL1_AVG_T[7]	n	
4761	short	_FFT_IL1_AVG_T[8]	n	
4762	short	_FFT_IL1_AVG_T[9]	n	
4763	short	_FFT_IL1_AVG_T[10]	n	
4764	short	_FFT_IL1_AVG_T[11]	n	
4765	short	_FFT_IL1_AVG_T[12]	n	
4766	short	_FFT_IL1_AVG_T[13]	n	
4767	short	_FFT_IL1_AVG_T[14]	n	
4768	short	_FFT_IL1_AVG_T[15]	n	



Address	Format	Designation	Unit	Remarks
4769	short	_FFT_IL1_AVG_T[16]	n	
4770	short	_FFT_IL1_AVG_T[17]	n	
4771	short	_FFT_IL1_AVG_T[18]	n	
4772	short	_FFT_IL1_AVG_T[19]	n	
4773	short	_FFT_IL1_AVG_T[20]	n	
4774	short	_FFT_IL1_AVG_T[21]	n	
4775	short	_FFT_IL1_AVG_T[22]	n	
4776	short	_FFT_IL1_AVG_T[23]	n	
4777	short	_FFT_IL1_AVG_T[24]	n	
4778	short	_FFT_IL1_AVG_T[25]	n	
4779	short	_FFT_IL1_AVG_T[26]	n	
4780	short	_FFT_IL1_AVG_T[27]	n	
4781	short	_FFT_IL1_AVG_T[28]	n	
4782	short	_FFT_IL1_AVG_T[29]	n	
4783	short	_FFT_IL1_AVG_T[30]	n	
4784	short	_FFT_IL1_AVG_T[31]	n	
4785	short	_FFT_IL1_AVG_T[32]	n	
4786	short	_FFT_IL1_AVG_T[33]	n	
4787	short	_FFT_IL1_AVG_T[34]	n	
4788	short	_FFT_IL1_AVG_T[35]	n	
4789	short	_FFT_IL1_AVG_T[36]	n	
4790	short	_FFT_IL1_AVG_T[37]	n	
4791	short	_FFT_IL1_AVG_T[38]	n	
4792	short	_FFT_IL1_AVG_T[39]	n	
4793	short	_FFT_IL2_AVG_T[0]	n	
4794	short	_FFT_IL2_AVG_T[1]	n	
4795	short	_FFT_IL2_AVG_T[2]	n	
4796	short	_FFT_IL2_AVG_T[3]	n	
4797	short	_FFT_IL2_AVG_T[4]	n	
4798	short	_FFT_IL2_AVG_T[5]	n	
4799	short	_FFT_IL2_AVG_T[6]	n	
4800	short	_FFT_IL2_AVG_T[7]	n	
4801	short	_FFT_IL2_AVG_T[8]	n	
4802	short	_FFT_IL2_AVG_T[9]	n	
4803	short	_FFT_IL2_AVG_T[10]	n	
4804	short	_FFT_IL2_AVG_T[11]	n	
4805	short	_FFT_IL2_AVG_T[12]	n	
4806	short	_FFT_IL2_AVG_T[13]	n	
4807	short	_FFT_IL2_AVG_T[14]	n	
4808	short	_FFT_IL2_AVG_T[15]	n	
4809	short	_FFT_IL2_AVG_T[16]	n	
4810	short	_FFT_IL2_AVG_T[17]	n	Average time see table on page 2
4811	short	_FFT_IL2_AVG_T[18]	n	
4812	short	_FFT_IL2_AVG_T[19]	n	
4813	short	_FFT_IL2_AVG_T[20]	n	
4814	short	_FFT_IL2_AVG_T[21]	n	
4815	short	_FFT_IL2_AVG_T[22]	n	
4816	short	_FFT_IL2_AVG_T[23]	n	
4817	short	_FFT_IL2_AVG_T[24]	n	
4818	short	_FFT_IL2_AVG_T[25]	n	
4819	short	_FFT_IL2_AVG_T[26]	n	
4820	short	_FFT_IL2_AVG_T[27]	n	
4821	short	_FFT_IL2_AVG_T[28]	n	
4822	short	_FFT_IL2_AVG_T[29]	n	
4823	short	_FFT_IL2_AVG_T[30]	n	
4824	short	_FFT_IL2_AVG_T[31]	n	
4825	short	_FFT_IL2_AVG_T[32]	n	
4826	short	_FFT_IL2_AVG_T[33]	n	
4827	short	_FFT_IL2_AVG_T[34]	n	



Address	Format	Designation	Unit	Remarks
4828	short	_FFT_IL2_AVG_T[35]	n	
4829	short	_FFT_IL2_AVG_T[36]	n	
4830	short	_FFT_IL2_AVG_T[37]	n	
4831	short	_FFT_IL2_AVG_T[38]	n	
4832	short	_FFT_IL2_AVG_T[39]	n	
4833	short	_FFT_IL3_AVG_T[0]	n	
4834	short	_FFT_IL3_AVG_T[1]	n	
4835	short	_FFT_IL3_AVG_T[2]	n	
4836	short	_FFT_IL3_AVG_T[3]	n	
4837	short	_FFT_IL3_AVG_T[4]	n	
4838	short	_FFT_IL3_AVG_T[5]	n	
4839	short	_FFT_IL3_AVG_T[6]	n	
4840	short	_FFT_IL3_AVG_T[7]	n	
4841	short	_FFT_IL3_AVG_T[8]	n	
4842	short	_FFT_IL3_AVG_T[9]	n	
4843	short	_FFT_IL3_AVG_T[10]	n	
4844	short	_FFT_IL3_AVG_T[11]	n	
4845	short	_FFT_IL3_AVG_T[12]	n	
4846	short	_FFT_IL3_AVG_T[13]	n	
4847	short	_FFT_IL3_AVG_T[14]	n	
4848	short	_FFT_IL3_AVG_T[15]	n	
4849	short	_FFT_IL3_AVG_T[16]	n	
4850	short	_FFT_IL3_AVG_T[17]	n	
4851	short	_FFT_IL3_AVG_T[18]	n	
4852	short	_FFT_IL3_AVG_T[19]	n	
4853	short	_FFT_IL3_AVG_T[20]	n	
4854	short	_FFT_IL3_AVG_T[21]	n	
4855	short	_FFT_IL3_AVG_T[22]	n	
4856	short	_FFT_IL3_AVG_T[23]	n	
4857	short	_FFT_IL3_AVG_T[24]	n	
4858	short	_FFT_IL3_AVG_T[25]	n	
4859	short	_FFT_IL3_AVG_T[26]	n	
4860	short	_FFT_IL3_AVG_T[27]	n	
4861	short	_FFT_IL3_AVG_T[28]	n	
4862	short	_FFT_IL3_AVG_T[29]	n	
4863	short	_FFT_IL3_AVG_T[30]	n	
4864	short	_FFT_IL3_AVG_T[31]	n	
4865	short	_FFT_IL3_AVG_T[32]	n	
4866	short	_FFT_IL3_AVG_T[33]	n	
4867	short	_FFT_IL3_AVG_T[34]	n	
4868	short	_FFT_IL3_AVG_T[35]	n	
4869	short	_FFT_IL3_AVG_T[36]	n	
4870	short	_FFT_IL3_AVG_T[37]	n	Average time see table on page 2
4871	short	_FFT_IL3_AVG_T[38]	n	
4872	short	_FFT_IL3_AVG_T[39]	n	
4873	short	_FFT_IL4_AVG_T[0]	n	
4874	short	_FFT_IL4_AVG_T[1]	n	
4875	short	_FFT_IL4_AVG_T[2]	n	
4876	short	_FFT_IL4_AVG_T[3]	n	
4877	short	_FFT_IL4_AVG_T[4]	n	
4878	short	_FFT_IL4_AVG_T[5]	n	
4879	short	_FFT_IL4_AVG_T[6]	n	
4880	short	_FFT_IL4_AVG_T[7]	n	
4881	short	_FFT_IL4_AVG_T[8]	n	
4882	short	_FFT_IL4_AVG_T[9]	n	
4883	short	_FFT_IL4_AVG_T[10]	n	
4884	short	_FFT_IL4_AVG_T[11]	n	
4885	short	_FFT_IL4_AVG_T[12]	n	
4886	short	_FFT_IL4_AVG_T[13]	n	

Address	Format	Designation	Unit	Remarks
4887	short	_FFT_IL4_AVG_T[14]	n	
4888	short	_FFT_IL4_AVG_T[15]	n	
4889	short	_FFT_IL4_AVG_T[16]	n	
4890	short	_FFT_IL4_AVG_T[17]	n	
4891	short	_FFT_IL4_AVG_T[18]	n	
4892	short	_FFT_IL4_AVG_T[19]	n	
4893	short	_FFT_IL4_AVG_T[20]	n	
4894	short	_FFT_IL4_AVG_T[21]	n	
4895	short	_FFT_IL4_AVG_T[22]	n	
4896	short	_FFT_IL4_AVG_T[23]	n	
4897	short	_FFT_IL4_AVG_T[24]	n	
4898	short	_FFT_IL4_AVG_T[25]	n	
4899	short	_FFT_IL4_AVG_T[26]	n	
4900	short	_FFT_IL4_AVG_T[27]	n	
4901	short	_FFT_IL4_AVG_T[28]	n	
4902	short	_FFT_IL4_AVG_T[29]	n	
4903	short	_FFT_IL4_AVG_T[30]	n	
4904	short	_FFT_IL4_AVG_T[31]	n	
4905	short	_FFT_IL4_AVG_T[32]	n	
4906	short	_FFT_IL4_AVG_T[33]	n	
4907	short	_FFT_IL4_AVG_T[34]	n	
4908	short	_FFT_IL4_AVG_T[35]	n	
4909	short	_FFT_IL4_AVG_T[36]	n	
4910	short	_FFT_IL4_AVG_T[37]	n	
4911	short	_FFT_IL4_AVG_T[38]	n	
4912	short	_FFT_IL4_AVG_T[39]	n	
4913	short	_FFT_PL1_AVG_T[0]	n	
4914	short	_FFT_PL1_AVG_T[1]	n	
4915	short	_FFT_PL1_AVG_T[2]	n	
4916	short	_FFT_PL1_AVG_T[3]	n	
4917	short	_FFT_PL1_AVG_T[4]	n	
4918	short	_FFT_PL1_AVG_T[5]	n	
4919	short	_FFT_PL1_AVG_T[6]	n	
4920	short	_FFT_PL1_AVG_T[7]	n	
4921	short	_FFT_PL1_AVG_T[8]	n	
4922	short	_FFT_PL1_AVG_T[9]	n	
4923	short	_FFT_PL1_AVG_T[10]	n	
4924	short	_FFT_PL1_AVG_T[11]	n	
4925	short	_FFT_PL1_AVG_T[12]	n	
4926	short	_FFT_PL1_AVG_T[13]	n	
4927	short	_FFT_PL1_AVG_T[14]	n	
4928	short	_FFT_PL1_AVG_T[15]	n	
4929	short	_FFT_PL1_AVG_T[16]	n	
4930	short	_FFT_PL1_AVG_T[17]	n	Average time see table on page 2
4931	short	_FFT_PL1_AVG_T[18]	n	
4932	short	_FFT_PL1_AVG_T[19]	n	
4933	short	_FFT_PL1_AVG_T[20]	n	
4934	short	_FFT_PL1_AVG_T[21]	n	
4935	short	_FFT_PL1_AVG_T[22]	n	
4936	short	_FFT_PL1_AVG_T[23]	n	
4937	short	_FFT_PL1_AVG_T[24]	n	
4938	short	_FFT_PL1_AVG_T[25]	n	
4939	short	_FFT_PL1_AVG_T[26]	n	
4940	short	_FFT_PL1_AVG_T[27]	n	
4941	short	_FFT_PL1_AVG_T[28]	n	
4942	short	_FFT_PL1_AVG_T[29]	n	
4943	short	_FFT_PL1_AVG_T[30]	n	
4944	short	_FFT_PL1_AVG_T[31]	n	
4945	short	_FFT_PL1_AVG_T[32]	n	

Address	Format	Designation	Unit	Remarks
4946	short	_FFT_PL1_AVG_T[33]	n	
4947	short	_FFT_PL1_AVG_T[34]	n	
4948	short	_FFT_PL1_AVG_T[35]	n	
4949	short	_FFT_PL1_AVG_T[36]	n	
4950	short	_FFT_PL1_AVG_T[37]	n	
4951	short	_FFT_PL1_AVG_T[38]	n	
4952	short	_FFT_PL1_AVG_T[39]	n	
4953	short	_FFT_PL2_AVG_T[0]	n	
4954	short	_FFT_PL2_AVG_T[1]	n	
4955	short	_FFT_PL2_AVG_T[2]	n	
4956	short	_FFT_PL2_AVG_T[3]	n	
4957	short	_FFT_PL2_AVG_T[4]	n	
4958	short	_FFT_PL2_AVG_T[5]	n	
4959	short	_FFT_PL2_AVG_T[6]	n	
4960	short	_FFT_PL2_AVG_T[7]	n	
4961	short	_FFT_PL2_AVG_T[8]	n	
4962	short	_FFT_PL2_AVG_T[9]	n	
4963	short	_FFT_PL2_AVG_T[10]	n	
4964	short	_FFT_PL2_AVG_T[11]	n	
4965	short	_FFT_PL2_AVG_T[12]	n	
4966	short	_FFT_PL2_AVG_T[13]	n	
4967	short	_FFT_PL2_AVG_T[14]	n	
4968	short	_FFT_PL2_AVG_T[15]	n	
4969	short	_FFT_PL2_AVG_T[16]	n	
4970	short	_FFT_PL2_AVG_T[17]	n	
4971	short	_FFT_PL2_AVG_T[18]	n	
4972	short	_FFT_PL2_AVG_T[19]	n	
4973	short	_FFT_PL2_AVG_T[20]	n	
4974	short	_FFT_PL2_AVG_T[21]	n	
4975	short	_FFT_PL2_AVG_T[22]	n	
4976	short	_FFT_PL2_AVG_T[23]	n	
4977	short	_FFT_PL2_AVG_T[24]	n	
4978	short	_FFT_PL2_AVG_T[25]	n	
4979	short	_FFT_PL2_AVG_T[26]	n	
4980	short	_FFT_PL2_AVG_T[27]	n	
4981	short	_FFT_PL2_AVG_T[28]	n	
4982	short	_FFT_PL2_AVG_T[29]	n	
4983	short	_FFT_PL2_AVG_T[30]	n	
4984	short	_FFT_PL2_AVG_T[31]	n	
4985	short	_FFT_PL2_AVG_T[32]	n	
4986	short	_FFT_PL2_AVG_T[33]	n	
4987	short	_FFT_PL2_AVG_T[34]	n	
4988	short	_FFT_PL2_AVG_T[35]	n	
4989	short	_FFT_PL2_AVG_T[36]	n	
4990	short	_FFT_PL2_AVG_T[37]	n	Average time see table on page 2
4991	short	_FFT_PL2_AVG_T[38]	n	
4992	short	_FFT_PL2_AVG_T[39]	n	
4993	short	_FFT_PL3_AVG_T[0]	n	
4994	short	_FFT_PL3_AVG_T[1]	n	
4995	short	_FFT_PL3_AVG_T[2]	n	
4996	short	_FFT_PL3_AVG_T[3]	n	
4997	short	_FFT_PL3_AVG_T[4]	n	
4998	short	_FFT_PL3_AVG_T[5]	n	
4999	short	_FFT_PL3_AVG_T[6]	n	
5000	short	_FFT_PL3_AVG_T[7]	n	
5001	short	_FFT_PL3_AVG_T[8]	n	
5002	short	_FFT_PL3_AVG_T[9]	n	
5003	short	_FFT_PL3_AVG_T[10]	n	
5004	short	_FFT_PL3_AVG_T[11]	n	

Address	Format	Designation	Unit	Remarks
5005	short	_FFT_PL3_AVG_T[12]	n	
5006	short	_FFT_PL3_AVG_T[13]	n	
5007	short	_FFT_PL3_AVG_T[14]	n	
5008	short	_FFT_PL3_AVG_T[15]	n	
5009	short	_FFT_PL3_AVG_T[16]	n	
5010	short	_FFT_PL3_AVG_T[17]	n	
5011	short	_FFT_PL3_AVG_T[18]	n	
5012	short	_FFT_PL3_AVG_T[19]	n	
5013	short	_FFT_PL3_AVG_T[20]	n	
5014	short	_FFT_PL3_AVG_T[21]	n	
5015	short	_FFT_PL3_AVG_T[22]	n	
5016	short	_FFT_PL3_AVG_T[23]	n	
5017	short	_FFT_PL3_AVG_T[24]	n	
5018	short	_FFT_PL3_AVG_T[25]	n	
5019	short	_FFT_PL3_AVG_T[26]	n	
5020	short	_FFT_PL3_AVG_T[27]	n	
5021	short	_FFT_PL3_AVG_T[28]	n	
5022	short	_FFT_PL3_AVG_T[29]	n	
5023	short	_FFT_PL3_AVG_T[30]	n	
5024	short	_FFT_PL3_AVG_T[31]	n	
5025	short	_FFT_PL3_AVG_T[32]	n	
5026	short	_FFT_PL3_AVG_T[33]	n	
5027	short	_FFT_PL3_AVG_T[34]	n	
5028	short	_FFT_PL3_AVG_T[35]	n	
5029	short	_FFT_PL3_AVG_T[36]	n	
5030	short	_FFT_PL3_AVG_T[37]	n	
5031	short	_FFT_PL3_AVG_T[38]	n	
5032	short	_FFT_PL3_AVG_T[39]	n	
5033	short	_FFT_PL4_AVG_T[0]	n	
5034	short	_FFT_PL4_AVG_T[1]	n	
5035	short	_FFT_PL4_AVG_T[2]	n	
5036	short	_FFT_PL4_AVG_T[3]	n	
5037	short	_FFT_PL4_AVG_T[4]	n	
5038	short	_FFT_PL4_AVG_T[5]	n	
5039	short	_FFT_PL4_AVG_T[6]	n	
5040	short	_FFT_PL4_AVG_T[7]	n	
5041	short	_FFT_PL4_AVG_T[8]	n	
5042	short	_FFT_PL4_AVG_T[9]	n	
5043	short	_FFT_PL4_AVG_T[10]	n	
5044	short	_FFT_PL4_AVG_T[11]	n	
5045	short	_FFT_PL4_AVG_T[12]	n	
5046	short	_FFT_PL4_AVG_T[13]	n	
5047	short	_FFT_PL4_AVG_T[14]	n	
5048	short	_FFT_PL4_AVG_T[15]	n	
5049	short	_FFT_PL4_AVG_T[16]	n	
5050	short	_FFT_PL4_AVG_T[17]	n	Average time see table on page 2
5051	short	_FFT_PL4_AVG_T[18]	n	
5052	short	_FFT_PL4_AVG_T[19]	n	
5053	short	_FFT_PL4_AVG_T[20]	n	
5054	short	_FFT_PL4_AVG_T[21]	n	
5055	short	_FFT_PL4_AVG_T[22]	n	
5056	short	_FFT_PL4_AVG_T[23]	n	
5057	short	_FFT_PL4_AVG_T[24]	n	
5058	short	_FFT_PL4_AVG_T[25]	n	
5059	short	_FFT_PL4_AVG_T[26]	n	
5060	short	_FFT_PL4_AVG_T[27]	n	
5061	short	_FFT_PL4_AVG_T[28]	n	
5062	short	_FFT_PL4_AVG_T[29]	n	
5063	short	_FFT_PL4_AVG_T[30]	n	

Address	Format	Designation	Unit	Remarks
5064	short	_FFT_PL4_AVG_T[31]	n	
5065	short	_FFT_PL4_AVG_T[32]	n	
5066	short	_FFT_PL4_AVG_T[33]	n	
5067	short	_FFT_PL4_AVG_T[34]	n	
5068	short	_FFT_PL4_AVG_T[35]	n	
5069	short	_FFT_PL4_AVG_T[36]	n	
5070	short	_FFT_PL4_AVG_T[37]	n	
5071	short	_FFT_PL4_AVG_T[38]	n	
5072	short	_FFT_PL4_AVG_T[39]	n	
5073	short	_FFT_QL1_AVG_T[0]	n	
5074	short	_FFT_QL1_AVG_T[1]	n	
5075	short	_FFT_QL1_AVG_T[2]	n	
5076	short	_FFT_QL1_AVG_T[3]	n	
5077	short	_FFT_QL1_AVG_T[4]	n	
5078	short	_FFT_QL1_AVG_T[5]	n	
5079	short	_FFT_QL1_AVG_T[6]	n	
5080	short	_FFT_QL1_AVG_T[7]	n	
5081	short	_FFT_QL1_AVG_T[8]	n	
5082	short	_FFT_QL1_AVG_T[9]	n	
5083	short	_FFT_QL1_AVG_T[10]	n	
5084	short	_FFT_QL1_AVG_T[11]	n	
5085	short	_FFT_QL1_AVG_T[12]	n	
5086	short	_FFT_QL1_AVG_T[13]	n	
5087	short	_FFT_QL1_AVG_T[14]	n	
5088	short	_FFT_QL1_AVG_T[15]	n	
5089	short	_FFT_QL1_AVG_T[16]	n	
5090	short	_FFT_QL1_AVG_T[17]	n	
5091	short	_FFT_QL1_AVG_T[18]	n	
5092	short	_FFT_QL1_AVG_T[19]	n	
5093	short	_FFT_QL1_AVG_T[20]	n	
5094	short	_FFT_QL1_AVG_T[21]	n	
5095	short	_FFT_QL1_AVG_T[22]	n	
5096	short	_FFT_QL1_AVG_T[23]	n	
5097	short	_FFT_QL1_AVG_T[24]	n	
5098	short	_FFT_QL1_AVG_T[25]	n	
5099	short	_FFT_QL1_AVG_T[26]	n	
5100	short	_FFT_QL1_AVG_T[27]	n	
5101	short	_FFT_QL1_AVG_T[28]	n	
5102	short	_FFT_QL1_AVG_T[29]	n	
5103	short	_FFT_QL1_AVG_T[30]	n	
5104	short	_FFT_QL1_AVG_T[31]	n	
5105	short	_FFT_QL1_AVG_T[32]	n	
5106	short	_FFT_QL1_AVG_T[33]	n	
5107	short	_FFT_QL1_AVG_T[34]	n	
5108	short	_FFT_QL1_AVG_T[35]	n	
5109	short	_FFT_QL1_AVG_T[36]	n	
5110	short	_FFT_QL1_AVG_T[37]	n	Average time see table on page 2
5111	short	_FFT_QL1_AVG_T[38]	n	
5112	short	_FFT_QL1_AVG_T[39]	n	
5113	short	_FFT_QL2_AVG_T[0]	n	
5114	short	_FFT_QL2_AVG_T[1]	n	
5115	short	_FFT_QL2_AVG_T[2]	n	
5116	short	_FFT_QL2_AVG_T[3]	n	
5117	short	_FFT_QL2_AVG_T[4]	n	
5118	short	_FFT_QL2_AVG_T[5]	n	
5119	short	_FFT_QL2_AVG_T[6]	n	
5120	short	_FFT_QL2_AVG_T[7]	n	
5121	short	_FFT_QL2_AVG_T[8]	n	
5122	short	_FFT_QL2_AVG_T[9]	n	

Address	Format	Designation	Unit	Remarks
5123	short	_FFT_QL2_AVG_T[10]	n	
5124	short	_FFT_QL2_AVG_T[11]	n	
5125	short	_FFT_QL2_AVG_T[12]	n	
5126	short	_FFT_QL2_AVG_T[13]	n	
5127	short	_FFT_QL2_AVG_T[14]	n	
5128	short	_FFT_QL2_AVG_T[15]	n	
5129	short	_FFT_QL2_AVG_T[16]	n	
5130	short	_FFT_QL2_AVG_T[17]	n	
5131	short	_FFT_QL2_AVG_T[18]	n	
5132	short	_FFT_QL2_AVG_T[19]	n	
5133	short	_FFT_QL2_AVG_T[20]	n	
5134	short	_FFT_QL2_AVG_T[21]	n	
5135	short	_FFT_QL2_AVG_T[22]	n	
5136	short	_FFT_QL2_AVG_T[23]	n	
5137	short	_FFT_QL2_AVG_T[24]	n	
5138	short	_FFT_QL2_AVG_T[25]	n	
5139	short	_FFT_QL2_AVG_T[26]	n	
5140	short	_FFT_QL2_AVG_T[27]	n	
5141	short	_FFT_QL2_AVG_T[28]	n	
5142	short	_FFT_QL2_AVG_T[29]	n	
5143	short	_FFT_QL2_AVG_T[30]	n	
5144	short	_FFT_QL2_AVG_T[31]	n	
5145	short	_FFT_QL2_AVG_T[32]	n	
5146	short	_FFT_QL2_AVG_T[33]	n	
5147	short	_FFT_QL2_AVG_T[34]	n	
5148	short	_FFT_QL2_AVG_T[35]	n	
5149	short	_FFT_QL2_AVG_T[36]	n	
5150	short	_FFT_QL2_AVG_T[37]	n	
5151	short	_FFT_QL2_AVG_T[38]	n	
5152	short	_FFT_QL2_AVG_T[39]	n	
5153	short	_FFT_QL3_AVG_T[0]	n	
5154	short	_FFT_QL3_AVG_T[1]	n	
5155	short	_FFT_QL3_AVG_T[2]	n	
5156	short	_FFT_QL3_AVG_T[3]	n	
5157	short	_FFT_QL3_AVG_T[4]	n	
5158	short	_FFT_QL3_AVG_T[5]	n	
5159	short	_FFT_QL3_AVG_T[6]	n	
5160	short	_FFT_QL3_AVG_T[7]	n	
5161	short	_FFT_QL3_AVG_T[8]	n	
5162	short	_FFT_QL3_AVG_T[9]	n	
5163	short	_FFT_QL3_AVG_T[10]	n	
5164	short	_FFT_QL3_AVG_T[11]	n	
5165	short	_FFT_QL3_AVG_T[12]	n	
5166	short	_FFT_QL3_AVG_T[13]	n	
5167	short	_FFT_QL3_AVG_T[14]	n	
5168	short	_FFT_QL3_AVG_T[15]	n	
5169	short	_FFT_QL3_AVG_T[16]	n	
5170	short	_FFT_QL3_AVG_T[17]	n	Average time see table on page 2
5171	short	_FFT_QL3_AVG_T[18]	n	
5172	short	_FFT_QL3_AVG_T[19]	n	
5173	short	_FFT_QL3_AVG_T[20]	n	
5174	short	_FFT_QL3_AVG_T[21]	n	
5175	short	_FFT_QL3_AVG_T[22]	n	
5176	short	_FFT_QL3_AVG_T[23]	n	
5177	short	_FFT_QL3_AVG_T[24]	n	
5178	short	_FFT_QL3_AVG_T[25]	n	
5179	short	_FFT_QL3_AVG_T[26]	n	
5180	short	_FFT_QL3_AVG_T[27]	n	
5181	short	_FFT_QL3_AVG_T[28]	n	

Address	Format	Designation	Unit	Remarks
5182	short	_FFT_QL3_AVG_T[29]	n	
5183	short	_FFT_QL3_AVG_T[30]	n	
5184	short	_FFT_QL3_AVG_T[31]	n	
5185	short	_FFT_QL3_AVG_T[32]	n	
5186	short	_FFT_QL3_AVG_T[33]	n	
5187	short	_FFT_QL3_AVG_T[34]	n	
5188	short	_FFT_QL3_AVG_T[35]	n	
5189	short	_FFT_QL3_AVG_T[36]	n	
5190	short	_FFT_QL3_AVG_T[37]	n	
5191	short	_FFT_QL3_AVG_T[38]	n	
5192	short	_FFT_QL3_AVG_T[39]	n	
5193	short	_FFT_QL4_AVG_T[0]	n	
5194	short	_FFT_QL4_AVG_T[1]	n	
5195	short	_FFT_QL4_AVG_T[2]	n	
5196	short	_FFT_QL4_AVG_T[3]	n	
5197	short	_FFT_QL4_AVG_T[4]	n	
5198	short	_FFT_QL4_AVG_T[5]	n	
5199	short	_FFT_QL4_AVG_T[6]	n	
5200	short	_FFT_QL4_AVG_T[7]	n	
5201	short	_FFT_QL4_AVG_T[8]	n	
5202	short	_FFT_QL4_AVG_T[9]	n	
5203	short	_FFT_QL4_AVG_T[10]	n	
5204	short	_FFT_QL4_AVG_T[11]	n	
5205	short	_FFT_QL4_AVG_T[12]	n	
5206	short	_FFT_QL4_AVG_T[13]	n	
5207	short	_FFT_QL4_AVG_T[14]	n	
5208	short	_FFT_QL4_AVG_T[15]	n	
5209	short	_FFT_QL4_AVG_T[16]	n	
5210	short	_FFT_QL4_AVG_T[17]	n	
5211	short	_FFT_QL4_AVG_T[18]	n	
5212	short	_FFT_QL4_AVG_T[19]	n	
5213	short	_FFT_QL4_AVG_T[20]	n	
5214	short	_FFT_QL4_AVG_T[21]	n	
5215	short	_FFT_QL4_AVG_T[22]	n	
5216	short	_FFT_QL4_AVG_T[23]	n	
5217	short	_FFT_QL4_AVG_T[24]	n	
5218	short	_FFT_QL4_AVG_T[25]	n	
5219	short	_FFT_QL4_AVG_T[26]	n	
5220	short	_FFT_QL4_AVG_T[27]	n	
5221	short	_FFT_QL4_AVG_T[28]	n	
5222	short	_FFT_QL4_AVG_T[29]	n	
5223	short	_FFT_QL4_AVG_T[30]	n	
5224	short	_FFT_QL4_AVG_T[31]	n	
5225	short	_FFT_QL4_AVG_T[32]	n	
5226	short	_FFT_QL4_AVG_T[33]	n	
5227	short	_FFT_QL4_AVG_T[34]	n	
5228	short	_FFT_QL4_AVG_T[35]	n	
5229	short	_FFT_QL4_AVG_T[36]	n	
5230	short	_FFT_QL4_AVG_T[37]	n	Average time see table on page 2
5231	short	_FFT_QL4_AVG_T[38]	n	
5232	short	_FFT_QL4_AVG_T[39]	n	
5233	short	_THD_ULN_AVG_T[0]	n	
5234	short	_THD_ULN_AVG_T[1]	n	
5235	short	_THD_ULN_AVG_T[2]	n	
5236	short	_THD_ULN_AVG_T[3]	n	
5237	short	_THD_ILN_AVG_T[0]	n	
5238	short	_THD_ILN_AVG_T[1]	n	
5239	short	_THD_ILN_AVG_T[2]	n	
5240	short	_THD_ILN_AVG_T[3]	n	



Address	Format	Designation	Unit	Remarks
5241	short	_KFACT_AVG_T[0]	n	
5242	short	_KFACT_AVG_T[1]	n	
5243	short	_KFACT_AVG_T[2]	n	
5244	short	_KFACT_AVG_T[3]	n	
5245	short	_ULN_AVG_T[0]	n	
5246	short	_ULN_AVG_T[1]	n	
5247	short	_ULN_AVG_T[2]	n	
5248	short	_ULN_AVG_T[3]	n	
5249	short	_ILN_AVG_T[0]	n	
5250	short	_ILN_AVG_T[1]	n	
5251	short	_ILN_AVG_T[2]	n	
5252	short	_ILN_AVG_T[3]	n	
5253	short	_PLN_AVG_T[0]	n	
5254	short	_PLN_AVG_T[1]	n	
5255	short	_PLN_AVG_T[2]	n	
5256	short	_PLN_AVG_T[3]	n	
5257	short	_QLN_AVG_T[0]	n	
5258	short	_QLN_AVG_T[1]	n	
5259	short	_QLN_AVG_T[2]	n	
5260	short	_QLN_AVG_T[3]	n	
5261	short	_SLN_AVG_T[0]	n	
5262	short	_SLN_AVG_T[1]	n	
5263	short	_SLN_AVG_T[2]	n	
5264	short	_SLN_AVG_T[3]	n	
5265	short	_ULL_AVG_T[0]	n	
5266	short	_ULL_AVG_T[1]	n	
5267	short	_ULL_AVG_T[2]	n	
5268	short	_I_SUM3_AVG_T	n	
5269	short	_I_SUM_AVG_T	n	
5270	short	_S_SUM3_AVG_T	n	
5271	short	_P_SUM3_AVG_T	n	
5272	short	_Q_SUM3_AVG_T	n	
5273	short	_S_SUM_AVG_T	n	
5274	short	_P_SUM_AVG_T	n	
5275	short	_Q_SUM_AVG_T	n	
5276	short	_FREQ_AVG_T	n	
5277	short	_N_AVG_T	n	
5278	short	_M_AVG_T	n	
5279	short	_G_AVG_T	n	
5280	short	_SYM_AVG_T	n	
5281	short	_IN_AVG_T	n	
5282	short	_IM_AVG_T	n	
5283	short	_IG_AVG_T	n	
5284	short	_S0_POWER_AVG_T[0]	n	
5285	short	_S0_POWER_AVG_T[1]	n	
5286	short	_EXT_TEMPERATUR_AVG_T	°C	
5287	uint	_FFT_UL1_MIN_T[0]	s	Time stamp (UTC-time)
5289	uint	_FFT_UL1_MIN_T[1]	s	
5291	uint	_FFT_UL1_MIN_T[2]	s	
5293	uint	_FFT_UL1_MIN_T[3]	s	Time stamp (UTC-time)
5295	uint	_FFT_UL1_MIN_T[4]	s	
5297	uint	_FFT_UL1_MIN_T[5]	s	
5299	uint	_FFT_UL1_MIN_T[6]	s	
5301	uint	_FFT_UL1_MIN_T[7]	s	
5303	uint	_FFT_UL1_MIN_T[8]	s	
5305	uint	_FFT_UL1_MIN_T[9]	s	
5307	uint	_FFT_UL1_MIN_T[10]	s	
5309	uint	_FFT_UL1_MIN_T[11]	s	
5311	uint	_FFT_UL1_MIN_T[12]	s	



Address	Format	Designation	Unit	Remarks
5313	uint	_FFT_UL1_MIN_T[13]	s	
5315	uint	_FFT_UL1_MIN_T[14]	s	
5317	uint	_FFT_UL1_MIN_T[15]	s	
5319	uint	_FFT_UL1_MIN_T[16]	s	
5321	uint	_FFT_UL1_MIN_T[17]	s	
5323	uint	_FFT_UL1_MIN_T[18]	s	
5325	uint	_FFT_UL1_MIN_T[19]	s	
5327	uint	_FFT_UL1_MIN_T[20]	s	
5329	uint	_FFT_UL1_MIN_T[21]	s	
5331	uint	_FFT_UL1_MIN_T[22]	s	
5333	uint	_FFT_UL1_MIN_T[23]	s	
5335	uint	_FFT_UL1_MIN_T[24]	s	
5337	uint	_FFT_UL1_MIN_T[25]	s	
5339	uint	_FFT_UL1_MIN_T[26]	s	
5341	uint	_FFT_UL1_MIN_T[27]	s	
5343	uint	_FFT_UL1_MIN_T[28]	s	
5345	uint	_FFT_UL1_MIN_T[29]	s	
5347	uint	_FFT_UL1_MIN_T[30]	s	
5349	uint	_FFT_UL1_MIN_T[31]	s	
5351	uint	_FFT_UL1_MIN_T[32]	s	
5353	uint	_FFT_UL1_MIN_T[33]	s	
5355	uint	_FFT_UL1_MIN_T[34]	s	
5357	uint	_FFT_UL1_MIN_T[35]	s	
5359	uint	_FFT_UL1_MIN_T[36]	s	
5361	uint	_FFT_UL1_MIN_T[37]	s	
5363	uint	_FFT_UL1_MIN_T[38]	s	
5365	uint	_FFT_UL1_MIN_T[39]	s	
5367	uint	_FFT_UL2_MIN_T[0]	s	
5369	uint	_FFT_UL2_MIN_T[1]	s	
5371	uint	_FFT_UL2_MIN_T[2]	s	
5373	uint	_FFT_UL2_MIN_T[3]	s	
5375	uint	_FFT_UL2_MIN_T[4]	s	
5377	uint	_FFT_UL2_MIN_T[5]	s	
5379	uint	_FFT_UL2_MIN_T[6]	s	
5381	uint	_FFT_UL2_MIN_T[7]	s	
5383	uint	_FFT_UL2_MIN_T[8]	s	
5385	uint	_FFT_UL2_MIN_T[9]	s	
5387	uint	_FFT_UL2_MIN_T[10]	s	
5389	uint	_FFT_UL2_MIN_T[11]	s	
5391	uint	_FFT_UL2_MIN_T[12]	s	
5393	uint	_FFT_UL2_MIN_T[13]	s	
5395	uint	_FFT_UL2_MIN_T[14]	s	
5397	uint	_FFT_UL2_MIN_T[15]	s	
5399	uint	_FFT_UL2_MIN_T[16]	s	
5401	uint	_FFT_UL2_MIN_T[17]	s	
5403	uint	_FFT_UL2_MIN_T[18]	s	
5405	uint	_FFT_UL2_MIN_T[19]	s	
5407	uint	_FFT_UL2_MIN_T[20]	s	
5409	uint	_FFT_UL2_MIN_T[21]	s	
5411	uint	_FFT_UL2_MIN_T[22]	s	
5413	uint	_FFT_UL2_MIN_T[23]	s	Time stamp (UTC-time)
5415	uint	_FFT_UL2_MIN_T[24]	s	
5417	uint	_FFT_UL2_MIN_T[25]	s	
5419	uint	_FFT_UL2_MIN_T[26]	s	
5421	uint	_FFT_UL2_MIN_T[27]	s	
5423	uint	_FFT_UL2_MIN_T[28]	s	
5425	uint	_FFT_UL2_MIN_T[29]	s	
5427	uint	_FFT_UL2_MIN_T[30]	s	
5429	uint	_FFT_UL2_MIN_T[31]	s	

Address	Format	Designation	Unit	Remarks
5431	uint	_FFT_UL2_MIN_T[32]	s	
5433	uint	_FFT_UL2_MIN_T[33]	s	
5435	uint	_FFT_UL2_MIN_T[34]	s	
5437	uint	_FFT_UL2_MIN_T[35]	s	
5439	uint	_FFT_UL2_MIN_T[36]	s	
5441	uint	_FFT_UL2_MIN_T[37]	s	
5443	uint	_FFT_UL2_MIN_T[38]	s	
5445	uint	_FFT_UL2_MIN_T[39]	s	
5447	uint	_FFT_UL3_MIN_T[0]	s	
5449	uint	_FFT_UL3_MIN_T[1]	s	
5451	uint	_FFT_UL3_MIN_T[2]	s	
5453	uint	_FFT_UL3_MIN_T[3]	s	
5455	uint	_FFT_UL3_MIN_T[4]	s	
5457	uint	_FFT_UL3_MIN_T[5]	s	
5459	uint	_FFT_UL3_MIN_T[6]	s	
5461	uint	_FFT_UL3_MIN_T[7]	s	
5463	uint	_FFT_UL3_MIN_T[8]	s	
5465	uint	_FFT_UL3_MIN_T[9]	s	
5467	uint	_FFT_UL3_MIN_T[10]	s	
5469	uint	_FFT_UL3_MIN_T[11]	s	
5471	uint	_FFT_UL3_MIN_T[12]	s	
5473	uint	_FFT_UL3_MIN_T[13]	s	
5475	uint	_FFT_UL3_MIN_T[14]	s	
5477	uint	_FFT_UL3_MIN_T[15]	s	
5479	uint	_FFT_UL3_MIN_T[16]	s	
5481	uint	_FFT_UL3_MIN_T[17]	s	
5483	uint	_FFT_UL3_MIN_T[18]	s	
5485	uint	_FFT_UL3_MIN_T[19]	s	
5487	uint	_FFT_UL3_MIN_T[20]	s	
5489	uint	_FFT_UL3_MIN_T[21]	s	
5491	uint	_FFT_UL3_MIN_T[22]	s	
5493	uint	_FFT_UL3_MIN_T[23]	s	
5495	uint	_FFT_UL3_MIN_T[24]	s	
5497	uint	_FFT_UL3_MIN_T[25]	s	
5499	uint	_FFT_UL3_MIN_T[26]	s	
5501	uint	_FFT_UL3_MIN_T[27]	s	
5503	uint	_FFT_UL3_MIN_T[28]	s	
5505	uint	_FFT_UL3_MIN_T[29]	s	
5507	uint	_FFT_UL3_MIN_T[30]	s	
5509	uint	_FFT_UL3_MIN_T[31]	s	
5511	uint	_FFT_UL3_MIN_T[32]	s	
5513	uint	_FFT_UL3_MIN_T[33]	s	
5515	uint	_FFT_UL3_MIN_T[34]	s	
5517	uint	_FFT_UL3_MIN_T[35]	s	
5519	uint	_FFT_UL3_MIN_T[36]	s	
5521	uint	_FFT_UL3_MIN_T[37]	s	
5523	uint	_FFT_UL3_MIN_T[38]	s	
5525	uint	_FFT_UL3_MIN_T[39]	s	
5527	uint	_FFT_UL4_MIN_T[0]	s	
5529	uint	_FFT_UL4_MIN_T[1]	s	
5531	uint	_FFT_UL4_MIN_T[2]	s	
5533	uint	_FFT_UL4_MIN_T[3]	s	Time stamp (UTC-time)
5535	uint	_FFT_UL4_MIN_T[4]	s	
5537	uint	_FFT_UL4_MIN_T[5]	s	
5539	uint	_FFT_UL4_MIN_T[6]	s	
5541	uint	_FFT_UL4_MIN_T[7]	s	
5543	uint	_FFT_UL4_MIN_T[8]	s	
5545	uint	_FFT_UL4_MIN_T[9]	s	
5547	uint	_FFT_UL4_MIN_T[10]	s	

Address	Format	Designation	Unit	Remarks
5549	uint	_FFT_UL4_MIN_T[11]	s	
5551	uint	_FFT_UL4_MIN_T[12]	s	
5553	uint	_FFT_UL4_MIN_T[13]	s	
5555	uint	_FFT_UL4_MIN_T[14]	s	
5557	uint	_FFT_UL4_MIN_T[15]	s	
5559	uint	_FFT_UL4_MIN_T[16]	s	
5561	uint	_FFT_UL4_MIN_T[17]	s	
5563	uint	_FFT_UL4_MIN_T[18]	s	
5565	uint	_FFT_UL4_MIN_T[19]	s	
5567	uint	_FFT_UL4_MIN_T[20]	s	
5569	uint	_FFT_UL4_MIN_T[21]	s	
5571	uint	_FFT_UL4_MIN_T[22]	s	
5573	uint	_FFT_UL4_MIN_T[23]	s	
5575	uint	_FFT_UL4_MIN_T[24]	s	
5577	uint	_FFT_UL4_MIN_T[25]	s	
5579	uint	_FFT_UL4_MIN_T[26]	s	
5581	uint	_FFT_UL4_MIN_T[27]	s	
5583	uint	_FFT_UL4_MIN_T[28]	s	
5585	uint	_FFT_UL4_MIN_T[29]	s	
5587	uint	_FFT_UL4_MIN_T[30]	s	
5589	uint	_FFT_UL4_MIN_T[31]	s	
5591	uint	_FFT_UL4_MIN_T[32]	s	
5593	uint	_FFT_UL4_MIN_T[33]	s	
5595	uint	_FFT_UL4_MIN_T[34]	s	
5597	uint	_FFT_UL4_MIN_T[35]	s	
5599	uint	_FFT_UL4_MIN_T[36]	s	
5601	uint	_FFT_UL4_MIN_T[37]	s	
5603	uint	_FFT_UL4_MIN_T[38]	s	
5605	uint	_FFT_UL4_MIN_T[39]	s	
5607	uint	_THD_ULN_MIN_T[0]	s	
5609	uint	_THD_ULN_MIN_T[1]	s	
5611	uint	_THD_ULN_MIN_T[2]	s	
5613	uint	_THD_ULN_MIN_T[3]	s	
5615	uint	_ULN_MIN_T[0]	s	
5617	uint	_ULN_MIN_T[1]	s	
5619	uint	_ULN_MIN_T[2]	s	
5621	uint	_ULN_MIN_T[3]	s	
5623	uint	_ULL_MIN_T[0]	s	
5625	uint	_ULL_MIN_T[1]	s	
5627	uint	_ULL_MIN_T[2]	s	
5629	uint	_FREQ_MIN_T	s	
5631	uint	_N_MIN_T	s	
5633	uint	_M_MIN_T	s	
5635	uint	_G_MIN_T	s	
5637	uint	_SYM_MIN_T	s	
5639	uint	_EXT_TEMPERATUR_MIN_T	s	
5641	uint	_FFT_UL1_MAX_T[0]	s	
5643	uint	_FFT_UL1_MAX_T[1]	s	
5645	uint	_FFT_UL1_MAX_T[2]	s	
5647	uint	_FFT_UL1_MAX_T[3]	s	
5649	uint	_FFT_UL1_MAX_T[4]	s	
5651	uint	_FFT_UL1_MAX_T[5]	s	
5653	uint	_FFT_UL1_MAX_T[6]	s	Time stamp (UTC-time)
5655	uint	_FFT_UL1_MAX_T[7]	s	
5657	uint	_FFT_UL1_MAX_T[8]	s	
5659	uint	_FFT_UL1_MAX_T[9]	s	
5661	uint	_FFT_UL1_MAX_T[10]	s	
5663	uint	_FFT_UL1_MAX_T[11]	s	
5665	uint	_FFT_UL1_MAX_T[12]	s	

Address	Format	Designation	Unit	Remarks
5667	uint	_FFT_UL1_MAX_T[13]	s	
5669	uint	_FFT_UL1_MAX_T[14]	s	
5671	uint	_FFT_UL1_MAX_T[15]	s	
5673	uint	_FFT_UL1_MAX_T[16]	s	
5675	uint	_FFT_UL1_MAX_T[17]	s	
5677	uint	_FFT_UL1_MAX_T[18]	s	
5679	uint	_FFT_UL1_MAX_T[19]	s	
5681	uint	_FFT_UL1_MAX_T[20]	s	
5683	uint	_FFT_UL1_MAX_T[21]	s	
5685	uint	_FFT_UL1_MAX_T[22]	s	
5687	uint	_FFT_UL1_MAX_T[23]	s	
5689	uint	_FFT_UL1_MAX_T[24]	s	
5691	uint	_FFT_UL1_MAX_T[25]	s	
5693	uint	_FFT_UL1_MAX_T[26]	s	
5695	uint	_FFT_UL1_MAX_T[27]	s	
5697	uint	_FFT_UL1_MAX_T[28]	s	
5699	uint	_FFT_UL1_MAX_T[29]	s	
5701	uint	_FFT_UL1_MAX_T[30]	s	
5703	uint	_FFT_UL1_MAX_T[31]	s	
5705	uint	_FFT_UL1_MAX_T[32]	s	
5707	uint	_FFT_UL1_MAX_T[33]	s	
5709	uint	_FFT_UL1_MAX_T[34]	s	
5711	uint	_FFT_UL1_MAX_T[35]	s	
5713	uint	_FFT_UL1_MAX_T[36]	s	
5715	uint	_FFT_UL1_MAX_T[37]	s	
5717	uint	_FFT_UL1_MAX_T[38]	s	
5719	uint	_FFT_UL1_MAX_T[39]	s	
5721	uint	_FFT_UL2_MAX_T[0]	s	
5723	uint	_FFT_UL2_MAX_T[1]	s	
5725	uint	_FFT_UL2_MAX_T[2]	s	
5727	uint	_FFT_UL2_MAX_T[3]	s	
5729	uint	_FFT_UL2_MAX_T[4]	s	
5731	uint	_FFT_UL2_MAX_T[5]	s	
5733	uint	_FFT_UL2_MAX_T[6]	s	
5735	uint	_FFT_UL2_MAX_T[7]	s	
5737	uint	_FFT_UL2_MAX_T[8]	s	
5739	uint	_FFT_UL2_MAX_T[9]	s	
5741	uint	_FFT_UL2_MAX_T[10]	s	
5743	uint	_FFT_UL2_MAX_T[11]	s	
5745	uint	_FFT_UL2_MAX_T[12]	s	
5747	uint	_FFT_UL2_MAX_T[13]	s	
5749	uint	_FFT_UL2_MAX_T[14]	s	
5751	uint	_FFT_UL2_MAX_T[15]	s	
5753	uint	_FFT_UL2_MAX_T[16]	s	
5755	uint	_FFT_UL2_MAX_T[17]	s	
5757	uint	_FFT_UL2_MAX_T[18]	s	
5759	uint	_FFT_UL2_MAX_T[19]	s	
5761	uint	_FFT_UL2_MAX_T[20]	s	
5763	uint	_FFT_UL2_MAX_T[21]	s	
5765	uint	_FFT_UL2_MAX_T[22]	s	
5767	uint	_FFT_UL2_MAX_T[23]	s	
5769	uint	_FFT_UL2_MAX_T[24]	s	
5771	uint	_FFT_UL2_MAX_T[25]	s	
5773	uint	_FFT_UL2_MAX_T[26]	s	Time stamp (UTC-time)
5775	uint	_FFT_UL2_MAX_T[27]	s	
5777	uint	_FFT_UL2_MAX_T[28]	s	
5779	uint	_FFT_UL2_MAX_T[29]	s	
5781	uint	_FFT_UL2_MAX_T[30]	s	
5783	uint	_FFT_UL2_MAX_T[31]	s	

Address	Format	Designation	Unit	Remarks
5785	uint	_FFT_UL2_MAX_T[32]	s	
5787	uint	_FFT_UL2_MAX_T[33]	s	
5789	uint	_FFT_UL2_MAX_T[34]	s	
5791	uint	_FFT_UL2_MAX_T[35]	s	
5793	uint	_FFT_UL2_MAX_T[36]	s	
5795	uint	_FFT_UL2_MAX_T[37]	s	
5797	uint	_FFT_UL2_MAX_T[38]	s	
5799	uint	_FFT_UL2_MAX_T[39]	s	
5801	uint	_FFT_UL3_MAX_T[0]	s	
5803	uint	_FFT_UL3_MAX_T[1]	s	
5805	uint	_FFT_UL3_MAX_T[2]	s	
5807	uint	_FFT_UL3_MAX_T[3]	s	
5809	uint	_FFT_UL3_MAX_T[4]	s	
5811	uint	_FFT_UL3_MAX_T[5]	s	
5813	uint	_FFT_UL3_MAX_T[6]	s	
5815	uint	_FFT_UL3_MAX_T[7]	s	
5817	uint	_FFT_UL3_MAX_T[8]	s	
5819	uint	_FFT_UL3_MAX_T[9]	s	
5821	uint	_FFT_UL3_MAX_T[10]	s	
5823	uint	_FFT_UL3_MAX_T[11]	s	
5825	uint	_FFT_UL3_MAX_T[12]	s	
5827	uint	_FFT_UL3_MAX_T[13]	s	
5829	uint	_FFT_UL3_MAX_T[14]	s	
5831	uint	_FFT_UL3_MAX_T[15]	s	
5833	uint	_FFT_UL3_MAX_T[16]	s	
5835	uint	_FFT_UL3_MAX_T[17]	s	
5837	uint	_FFT_UL3_MAX_T[18]	s	
5839	uint	_FFT_UL3_MAX_T[19]	s	
5841	uint	_FFT_UL3_MAX_T[20]	s	
5843	uint	_FFT_UL3_MAX_T[21]	s	
5845	uint	_FFT_UL3_MAX_T[22]	s	
5847	uint	_FFT_UL3_MAX_T[23]	s	
5849	uint	_FFT_UL3_MAX_T[24]	s	
5851	uint	_FFT_UL3_MAX_T[25]	s	
5853	uint	_FFT_UL3_MAX_T[26]	s	
5855	uint	_FFT_UL3_MAX_T[27]	s	
5857	uint	_FFT_UL3_MAX_T[28]	s	
5859	uint	_FFT_UL3_MAX_T[29]	s	
5861	uint	_FFT_UL3_MAX_T[30]	s	
5863	uint	_FFT_UL3_MAX_T[31]	s	
5865	uint	_FFT_UL3_MAX_T[32]	s	
5867	uint	_FFT_UL3_MAX_T[33]	s	
5869	uint	_FFT_UL3_MAX_T[34]	s	
5871	uint	_FFT_UL3_MAX_T[35]	s	
5873	uint	_FFT_UL3_MAX_T[36]	s	
5875	uint	_FFT_UL3_MAX_T[37]	s	
5877	uint	_FFT_UL3_MAX_T[38]	s	
5879	uint	_FFT_UL3_MAX_T[39]	s	
5881	uint	_FFT_UL4_MAX_T[0]	s	
5883	uint	_FFT_UL4_MAX_T[1]	s	
5885	uint	_FFT_UL4_MAX_T[2]	s	
5887	uint	_FFT_UL4_MAX_T[3]	s	
5889	uint	_FFT_UL4_MAX_T[4]	s	
5891	uint	_FFT_UL4_MAX_T[5]	s	
5893	uint	_FFT_UL4_MAX_T[6]	s	Time stamp (UTC-time)
5895	uint	_FFT_UL4_MAX_T[7]	s	
5897	uint	_FFT_UL4_MAX_T[8]	s	
5899	uint	_FFT_UL4_MAX_T[9]	s	
5901	uint	_FFT_UL4_MAX_T[10]	s	

Address	Format	Designation	Unit	Remarks
5903	uint	_FFT_UL4_MAX_T[11]	s	
5905	uint	_FFT_UL4_MAX_T[12]	s	
5907	uint	_FFT_UL4_MAX_T[13]	s	
5909	uint	_FFT_UL4_MAX_T[14]	s	
5911	uint	_FFT_UL4_MAX_T[15]	s	
5913	uint	_FFT_UL4_MAX_T[16]	s	
5915	uint	_FFT_UL4_MAX_T[17]	s	
5917	uint	_FFT_UL4_MAX_T[18]	s	
5919	uint	_FFT_UL4_MAX_T[19]	s	
5921	uint	_FFT_UL4_MAX_T[20]	s	
5923	uint	_FFT_UL4_MAX_T[21]	s	
5925	uint	_FFT_UL4_MAX_T[22]	s	
5927	uint	_FFT_UL4_MAX_T[23]	s	
5929	uint	_FFT_UL4_MAX_T[24]	s	
5931	uint	_FFT_UL4_MAX_T[25]	s	
5933	uint	_FFT_UL4_MAX_T[26]	s	
5935	uint	_FFT_UL4_MAX_T[27]	s	
5937	uint	_FFT_UL4_MAX_T[28]	s	
5939	uint	_FFT_UL4_MAX_T[29]	s	
5941	uint	_FFT_UL4_MAX_T[30]	s	
5943	uint	_FFT_UL4_MAX_T[31]	s	
5945	uint	_FFT_UL4_MAX_T[32]	s	
5947	uint	_FFT_UL4_MAX_T[33]	s	
5949	uint	_FFT_UL4_MAX_T[34]	s	
5951	uint	_FFT_UL4_MAX_T[35]	s	
5953	uint	_FFT_UL4_MAX_T[36]	s	
5955	uint	_FFT_UL4_MAX_T[37]	s	
5957	uint	_FFT_UL4_MAX_T[38]	s	
5959	uint	_FFT_UL4_MAX_T[39]	s	
5961	uint	_FFT_IL1_MAX_T[0]	s	
5963	uint	_FFT_IL1_MAX_T[1]	s	
5965	uint	_FFT_IL1_MAX_T[2]	s	
5967	uint	_FFT_IL1_MAX_T[3]	s	
5969	uint	_FFT_IL1_MAX_T[4]	s	
5971	uint	_FFT_IL1_MAX_T[5]	s	
5973	uint	_FFT_IL1_MAX_T[6]	s	
5975	uint	_FFT_IL1_MAX_T[7]	s	
5977	uint	_FFT_IL1_MAX_T[8]	s	
5979	uint	_FFT_IL1_MAX_T[9]	s	
5981	uint	_FFT_IL1_MAX_T[10]	s	
5983	uint	_FFT_IL1_MAX_T[11]	s	
5985	uint	_FFT_IL1_MAX_T[12]	s	
5987	uint	_FFT_IL1_MAX_T[13]	s	
5989	uint	_FFT_IL1_MAX_T[14]	s	
5991	uint	_FFT_IL1_MAX_T[15]	s	
5993	uint	_FFT_IL1_MAX_T[16]	s	
5995	uint	_FFT_IL1_MAX_T[17]	s	
5997	uint	_FFT_IL1_MAX_T[18]	s	
5999	uint	_FFT_IL1_MAX_T[19]	s	
6001	uint	_FFT_IL1_MAX_T[20]	s	
6003	uint	_FFT_IL1_MAX_T[21]	s	
6005	uint	_FFT_IL1_MAX_T[22]	s	
6007	uint	_FFT_IL1_MAX_T[23]	s	
6009	uint	_FFT_IL1_MAX_T[24]	s	
6011	uint	_FFT_IL1_MAX_T[25]	s	
6013	uint	_FFT_IL1_MAX_T[26]	s	Time stamp (UTC-time)
6015	uint	_FFT_IL1_MAX_T[27]	s	
6017	uint	_FFT_IL1_MAX_T[28]	s	
6019	uint	_FFT_IL1_MAX_T[29]	s	

Address	Format	Designation	Unit	Remarks
6021	uint	_FFT_IL1_MAX_T[30]	s	
6023	uint	_FFT_IL1_MAX_T[31]	s	
6025	uint	_FFT_IL1_MAX_T[32]	s	
6027	uint	_FFT_IL1_MAX_T[33]	s	
6029	uint	_FFT_IL1_MAX_T[34]	s	
6031	uint	_FFT_IL1_MAX_T[35]	s	
6033	uint	_FFT_IL1_MAX_T[36]	s	
6035	uint	_FFT_IL1_MAX_T[37]	s	
6037	uint	_FFT_IL1_MAX_T[38]	s	
6039	uint	_FFT_IL1_MAX_T[39]	s	
6041	uint	_FFT_IL2_MAX_T[0]	s	
6043	uint	_FFT_IL2_MAX_T[1]	s	
6045	uint	_FFT_IL2_MAX_T[2]	s	
6047	uint	_FFT_IL2_MAX_T[3]	s	
6049	uint	_FFT_IL2_MAX_T[4]	s	
6051	uint	_FFT_IL2_MAX_T[5]	s	
6053	uint	_FFT_IL2_MAX_T[6]	s	
6055	uint	_FFT_IL2_MAX_T[7]	s	
6057	uint	_FFT_IL2_MAX_T[8]	s	
6059	uint	_FFT_IL2_MAX_T[9]	s	
6061	uint	_FFT_IL2_MAX_T[10]	s	
6063	uint	_FFT_IL2_MAX_T[11]	s	
6065	uint	_FFT_IL2_MAX_T[12]	s	
6067	uint	_FFT_IL2_MAX_T[13]	s	
6069	uint	_FFT_IL2_MAX_T[14]	s	
6071	uint	_FFT_IL2_MAX_T[15]	s	
6073	uint	_FFT_IL2_MAX_T[16]	s	
6075	uint	_FFT_IL2_MAX_T[17]	s	
6077	uint	_FFT_IL2_MAX_T[18]	s	
6079	uint	_FFT_IL2_MAX_T[19]	s	
6081	uint	_FFT_IL2_MAX_T[20]	s	
6083	uint	_FFT_IL2_MAX_T[21]	s	
6085	uint	_FFT_IL2_MAX_T[22]	s	
6087	uint	_FFT_IL2_MAX_T[23]	s	
6089	uint	_FFT_IL2_MAX_T[24]	s	
6091	uint	_FFT_IL2_MAX_T[25]	s	
6093	uint	_FFT_IL2_MAX_T[26]	s	
6095	uint	_FFT_IL2_MAX_T[27]	s	
6097	uint	_FFT_IL2_MAX_T[28]	s	
6099	uint	_FFT_IL2_MAX_T[29]	s	
6101	uint	_FFT_IL2_MAX_T[30]	s	
6103	uint	_FFT_IL2_MAX_T[31]	s	
6105	uint	_FFT_IL2_MAX_T[32]	s	
6107	uint	_FFT_IL2_MAX_T[33]	s	
6109	uint	_FFT_IL2_MAX_T[34]	s	
6111	uint	_FFT_IL2_MAX_T[35]	s	
6113	uint	_FFT_IL2_MAX_T[36]	s	
6115	uint	_FFT_IL2_MAX_T[37]	s	
6117	uint	_FFT_IL2_MAX_T[38]	s	
6119	uint	_FFT_IL2_MAX_T[39]	s	
6121	uint	_FFT_IL3_MAX_T[0]	s	
6123	uint	_FFT_IL3_MAX_T[1]	s	
6125	uint	_FFT_IL3_MAX_T[2]	s	
6127	uint	_FFT_IL3_MAX_T[3]	s	
6129	uint	_FFT_IL3_MAX_T[4]	s	
6131	uint	_FFT_IL3_MAX_T[5]	s	
6133	uint	_FFT_IL3_MAX_T[6]	s	Time stamp (UTC-time)
6135	uint	_FFT_IL3_MAX_T[7]	s	
6137	uint	_FFT_IL3_MAX_T[8]	s	

Address	Format	Designation	Unit	Remarks
6139	uint	_FFT_IL3_MAX_T[9]	s	
6141	uint	_FFT_IL3_MAX_T[10]	s	
6143	uint	_FFT_IL3_MAX_T[11]	s	
6145	uint	_FFT_IL3_MAX_T[12]	s	
6147	uint	_FFT_IL3_MAX_T[13]	s	
6149	uint	_FFT_IL3_MAX_T[14]	s	
6151	uint	_FFT_IL3_MAX_T[15]	s	
6153	uint	_FFT_IL3_MAX_T[16]	s	
6155	uint	_FFT_IL3_MAX_T[17]	s	
6157	uint	_FFT_IL3_MAX_T[18]	s	
6159	uint	_FFT_IL3_MAX_T[19]	s	
6161	uint	_FFT_IL3_MAX_T[20]	s	
6163	uint	_FFT_IL3_MAX_T[21]	s	
6165	uint	_FFT_IL3_MAX_T[22]	s	
6167	uint	_FFT_IL3_MAX_T[23]	s	
6169	uint	_FFT_IL3_MAX_T[24]	s	
6171	uint	_FFT_IL3_MAX_T[25]	s	
6173	uint	_FFT_IL3_MAX_T[26]	s	
6175	uint	_FFT_IL3_MAX_T[27]	s	
6177	uint	_FFT_IL3_MAX_T[28]	s	
6179	uint	_FFT_IL3_MAX_T[29]	s	
6181	uint	_FFT_IL3_MAX_T[30]	s	
6183	uint	_FFT_IL3_MAX_T[31]	s	
6185	uint	_FFT_IL3_MAX_T[32]	s	
6187	uint	_FFT_IL3_MAX_T[33]	s	
6189	uint	_FFT_IL3_MAX_T[34]	s	
6191	uint	_FFT_IL3_MAX_T[35]	s	
6193	uint	_FFT_IL3_MAX_T[36]	s	
6195	uint	_FFT_IL3_MAX_T[37]	s	
6197	uint	_FFT_IL3_MAX_T[38]	s	
6199	uint	_FFT_IL3_MAX_T[39]	s	
6201	uint	_FFT_IL4_MAX_T[0]	s	
6203	uint	_FFT_IL4_MAX_T[1]	s	
6205	uint	_FFT_IL4_MAX_T[2]	s	
6207	uint	_FFT_IL4_MAX_T[3]	s	
6209	uint	_FFT_IL4_MAX_T[4]	s	
6211	uint	_FFT_IL4_MAX_T[5]	s	
6213	uint	_FFT_IL4_MAX_T[6]	s	
6215	uint	_FFT_IL4_MAX_T[7]	s	
6217	uint	_FFT_IL4_MAX_T[8]	s	
6219	uint	_FFT_IL4_MAX_T[9]	s	
6221	uint	_FFT_IL4_MAX_T[10]	s	
6223	uint	_FFT_IL4_MAX_T[11]	s	
6225	uint	_FFT_IL4_MAX_T[12]	s	
6227	uint	_FFT_IL4_MAX_T[13]	s	
6229	uint	_FFT_IL4_MAX_T[14]	s	
6231	uint	_FFT_IL4_MAX_T[15]	s	
6233	uint	_FFT_IL4_MAX_T[16]	s	
6235	uint	_FFT_IL4_MAX_T[17]	s	
6237	uint	_FFT_IL4_MAX_T[18]	s	
6239	uint	_FFT_IL4_MAX_T[19]	s	
6241	uint	_FFT_IL4_MAX_T[20]	s	
6243	uint	_FFT_IL4_MAX_T[21]	s	
6245	uint	_FFT_IL4_MAX_T[22]	s	
6247	uint	_FFT_IL4_MAX_T[23]	s	
6249	uint	_FFT_IL4_MAX_T[24]	s	
6251	uint	_FFT_IL4_MAX_T[25]	s	
6253	uint	_FFT_IL4_MAX_T[26]	s	Time stamp (UTC-time)
6255	uint	_FFT_IL4_MAX_T[27]	s	



Address	Format	Designation	Unit	Remarks
6257	uint	_FFT_IL4_MAX_T[28]	s	
6259	uint	_FFT_IL4_MAX_T[29]	s	
6261	uint	_FFT_IL4_MAX_T[30]	s	
6263	uint	_FFT_IL4_MAX_T[31]	s	
6265	uint	_FFT_IL4_MAX_T[32]	s	
6267	uint	_FFT_IL4_MAX_T[33]	s	
6269	uint	_FFT_IL4_MAX_T[34]	s	
6271	uint	_FFT_IL4_MAX_T[35]	s	
6273	uint	_FFT_IL4_MAX_T[36]	s	
6275	uint	_FFT_IL4_MAX_T[37]	s	
6277	uint	_FFT_IL4_MAX_T[38]	s	
6279	uint	_FFT_IL4_MAX_T[39]	s	
6281	uint	_FFT_PL1_MAX_T[0]	s	
6283	uint	_FFT_PL1_MAX_T[1]	s	
6285	uint	_FFT_PL1_MAX_T[2]	s	
6287	uint	_FFT_PL1_MAX_T[3]	s	
6289	uint	_FFT_PL1_MAX_T[4]	s	
6291	uint	_FFT_PL1_MAX_T[5]	s	
6293	uint	_FFT_PL1_MAX_T[6]	s	
6295	uint	_FFT_PL1_MAX_T[7]	s	
6297	uint	_FFT_PL1_MAX_T[8]	s	
6299	uint	_FFT_PL1_MAX_T[9]	s	
6301	uint	_FFT_PL1_MAX_T[10]	s	
6303	uint	_FFT_PL1_MAX_T[11]	s	
6305	uint	_FFT_PL1_MAX_T[12]	s	
6307	uint	_FFT_PL1_MAX_T[13]	s	
6309	uint	_FFT_PL1_MAX_T[14]	s	
6311	uint	_FFT_PL1_MAX_T[15]	s	
6313	uint	_FFT_PL1_MAX_T[16]	s	
6315	uint	_FFT_PL1_MAX_T[17]	s	
6317	uint	_FFT_PL1_MAX_T[18]	s	
6319	uint	_FFT_PL1_MAX_T[19]	s	
6321	uint	_FFT_PL1_MAX_T[20]	s	
6323	uint	_FFT_PL1_MAX_T[21]	s	
6325	uint	_FFT_PL1_MAX_T[22]	s	
6327	uint	_FFT_PL1_MAX_T[23]	s	
6329	uint	_FFT_PL1_MAX_T[24]	s	
6331	uint	_FFT_PL1_MAX_T[25]	s	
6333	uint	_FFT_PL1_MAX_T[26]	s	
6335	uint	_FFT_PL1_MAX_T[27]	s	
6337	uint	_FFT_PL1_MAX_T[28]	s	
6339	uint	_FFT_PL1_MAX_T[29]	s	
6341	uint	_FFT_PL1_MAX_T[30]	s	
6343	uint	_FFT_PL1_MAX_T[31]	s	
6345	uint	_FFT_PL1_MAX_T[32]	s	
6347	uint	_FFT_PL1_MAX_T[33]	s	
6349	uint	_FFT_PL1_MAX_T[34]	s	
6351	uint	_FFT_PL1_MAX_T[35]	s	
6353	uint	_FFT_PL1_MAX_T[36]	s	
6355	uint	_FFT_PL1_MAX_T[37]	s	
6357	uint	_FFT_PL1_MAX_T[38]	s	
6359	uint	_FFT_PL1_MAX_T[39]	s	
6361	uint	_FFT_PL2_MAX_T[0]	s	
6363	uint	_FFT_PL2_MAX_T[1]	s	
6365	uint	_FFT_PL2_MAX_T[2]	s	
6367	uint	_FFT_PL2_MAX_T[3]	s	
6369	uint	_FFT_PL2_MAX_T[4]	s	
6371	uint	_FFT_PL2_MAX_T[5]	s	
6373	uint	_FFT_PL2_MAX_T[6]	s	Time stamp (UTC-time)

Address	Format	Designation	Unit	Remarks
6375	uint	_FFT_PL2_MAX_T[7]	s	
6377	uint	_FFT_PL2_MAX_T[8]	s	
6379	uint	_FFT_PL2_MAX_T[9]	s	
6381	uint	_FFT_PL2_MAX_T[10]	s	
6383	uint	_FFT_PL2_MAX_T[11]	s	
6385	uint	_FFT_PL2_MAX_T[12]	s	
6387	uint	_FFT_PL2_MAX_T[13]	s	
6389	uint	_FFT_PL2_MAX_T[14]	s	
6391	uint	_FFT_PL2_MAX_T[15]	s	
6393	uint	_FFT_PL2_MAX_T[16]	s	
6395	uint	_FFT_PL2_MAX_T[17]	s	
6397	uint	_FFT_PL2_MAX_T[18]	s	
6399	uint	_FFT_PL2_MAX_T[19]	s	
6401	uint	_FFT_PL2_MAX_T[20]	s	
6403	uint	_FFT_PL2_MAX_T[21]	s	
6405	uint	_FFT_PL2_MAX_T[22]	s	
6407	uint	_FFT_PL2_MAX_T[23]	s	
6409	uint	_FFT_PL2_MAX_T[24]	s	
6411	uint	_FFT_PL2_MAX_T[25]	s	
6413	uint	_FFT_PL2_MAX_T[26]	s	
6415	uint	_FFT_PL2_MAX_T[27]	s	
6417	uint	_FFT_PL2_MAX_T[28]	s	
6419	uint	_FFT_PL2_MAX_T[29]	s	
6421	uint	_FFT_PL2_MAX_T[30]	s	
6423	uint	_FFT_PL2_MAX_T[31]	s	
6425	uint	_FFT_PL2_MAX_T[32]	s	
6427	uint	_FFT_PL2_MAX_T[33]	s	
6429	uint	_FFT_PL2_MAX_T[34]	s	
6431	uint	_FFT_PL2_MAX_T[35]	s	
6433	uint	_FFT_PL2_MAX_T[36]	s	
6435	uint	_FFT_PL2_MAX_T[37]	s	
6437	uint	_FFT_PL2_MAX_T[38]	s	
6439	uint	_FFT_PL2_MAX_T[39]	s	
6441	uint	_FFT_PL3_MAX_T[0]	s	
6443	uint	_FFT_PL3_MAX_T[1]	s	
6445	uint	_FFT_PL3_MAX_T[2]	s	
6447	uint	_FFT_PL3_MAX_T[3]	s	
6449	uint	_FFT_PL3_MAX_T[4]	s	
6451	uint	_FFT_PL3_MAX_T[5]	s	
6453	uint	_FFT_PL3_MAX_T[6]	s	
6455	uint	_FFT_PL3_MAX_T[7]	s	
6457	uint	_FFT_PL3_MAX_T[8]	s	
6459	uint	_FFT_PL3_MAX_T[9]	s	
6461	uint	_FFT_PL3_MAX_T[10]	s	
6463	uint	_FFT_PL3_MAX_T[11]	s	
6465	uint	_FFT_PL3_MAX_T[12]	s	
6467	uint	_FFT_PL3_MAX_T[13]	s	
6469	uint	_FFT_PL3_MAX_T[14]	s	
6471	uint	_FFT_PL3_MAX_T[15]	s	
6473	uint	_FFT_PL3_MAX_T[16]	s	
6475	uint	_FFT_PL3_MAX_T[17]	s	
6477	uint	_FFT_PL3_MAX_T[18]	s	
6479	uint	_FFT_PL3_MAX_T[19]	s	
6481	uint	_FFT_PL3_MAX_T[20]	s	
6483	uint	_FFT_PL3_MAX_T[21]	s	
6485	uint	_FFT_PL3_MAX_T[22]	s	
6487	uint	_FFT_PL3_MAX_T[23]	s	
6489	uint	_FFT_PL3_MAX_T[24]	s	
6491	uint	_FFT_PL3_MAX_T[25]	s	

Address	Format	Designation	Unit	Remarks
6493	uint	_FFT_PL3_MAX_T[26]	s	Time stamp (UTC-time)
6495	uint	_FFT_PL3_MAX_T[27]	s	
6497	uint	_FFT_PL3_MAX_T[28]	s	
6499	uint	_FFT_PL3_MAX_T[29]	s	
6501	uint	_FFT_PL3_MAX_T[30]	s	
6503	uint	_FFT_PL3_MAX_T[31]	s	
6505	uint	_FFT_PL3_MAX_T[32]	s	
6507	uint	_FFT_PL3_MAX_T[33]	s	
6509	uint	_FFT_PL3_MAX_T[34]	s	
6511	uint	_FFT_PL3_MAX_T[35]	s	
6513	uint	_FFT_PL3_MAX_T[36]	s	
6515	uint	_FFT_PL3_MAX_T[37]	s	
6517	uint	_FFT_PL3_MAX_T[38]	s	
6519	uint	_FFT_PL3_MAX_T[39]	s	
6521	uint	_FFT_PL4_MAX_T[0]	s	
6523	uint	_FFT_PL4_MAX_T[1]	s	
6525	uint	_FFT_PL4_MAX_T[2]	s	
6527	uint	_FFT_PL4_MAX_T[3]	s	
6529	uint	_FFT_PL4_MAX_T[4]	s	
6531	uint	_FFT_PL4_MAX_T[5]	s	
6533	uint	_FFT_PL4_MAX_T[6]	s	
6535	uint	_FFT_PL4_MAX_T[7]	s	
6537	uint	_FFT_PL4_MAX_T[8]	s	
6539	uint	_FFT_PL4_MAX_T[9]	s	
6541	uint	_FFT_PL4_MAX_T[10]	s	
6543	uint	_FFT_PL4_MAX_T[11]	s	
6545	uint	_FFT_PL4_MAX_T[12]	s	
6547	uint	_FFT_PL4_MAX_T[13]	s	
6549	uint	_FFT_PL4_MAX_T[14]	s	
6551	uint	_FFT_PL4_MAX_T[15]	s	
6553	uint	_FFT_PL4_MAX_T[16]	s	
6555	uint	_FFT_PL4_MAX_T[17]	s	
6557	uint	_FFT_PL4_MAX_T[18]	s	
6559	uint	_FFT_PL4_MAX_T[19]	s	
6561	uint	_FFT_PL4_MAX_T[20]	s	
6563	uint	_FFT_PL4_MAX_T[21]	s	
6565	uint	_FFT_PL4_MAX_T[22]	s	
6567	uint	_FFT_PL4_MAX_T[23]	s	
6569	uint	_FFT_PL4_MAX_T[24]	s	
6571	uint	_FFT_PL4_MAX_T[25]	s	
6573	uint	_FFT_PL4_MAX_T[26]	s	
6575	uint	_FFT_PL4_MAX_T[27]	s	
6577	uint	_FFT_PL4_MAX_T[28]	s	
6579	uint	_FFT_PL4_MAX_T[29]	s	
6581	uint	_FFT_PL4_MAX_T[30]	s	
6583	uint	_FFT_PL4_MAX_T[31]	s	
6585	uint	_FFT_PL4_MAX_T[32]	s	
6587	uint	_FFT_PL4_MAX_T[33]	s	
6589	uint	_FFT_PL4_MAX_T[34]	s	
6591	uint	_FFT_PL4_MAX_T[35]	s	
6593	uint	_FFT_PL4_MAX_T[36]	s	
6595	uint	_FFT_PL4_MAX_T[37]	s	
6597	uint	_FFT_PL4_MAX_T[38]	s	
6599	uint	_FFT_PL4_MAX_T[39]	s	
6601	uint	_FFT_QL1_MAX_T[0]	s	
6603	uint	_FFT_QL1_MAX_T[1]	s	
6605	uint	_FFT_QL1_MAX_T[2]	s	
6607	uint	_FFT_QL1_MAX_T[3]	s	
6609	uint	_FFT_QL1_MAX_T[4]	s	

Address	Format	Designation	Unit	Remarks
6611	uint	_FFT_QL1_MAX_T[5]	s	
6613	uint	_FFT_QL1_MAX_T[6]	s	Time stamp (UTC-time)
6615	uint	_FFT_QL1_MAX_T[7]	s	
6617	uint	_FFT_QL1_MAX_T[8]	s	
6619	uint	_FFT_QL1_MAX_T[9]	s	
6621	uint	_FFT_QL1_MAX_T[10]	s	
6623	uint	_FFT_QL1_MAX_T[11]	s	
6625	uint	_FFT_QL1_MAX_T[12]	s	
6627	uint	_FFT_QL1_MAX_T[13]	s	
6629	uint	_FFT_QL1_MAX_T[14]	s	
6631	uint	_FFT_QL1_MAX_T[15]	s	
6633	uint	_FFT_QL1_MAX_T[16]	s	
6635	uint	_FFT_QL1_MAX_T[17]	s	
6637	uint	_FFT_QL1_MAX_T[18]	s	
6639	uint	_FFT_QL1_MAX_T[19]	s	
6641	uint	_FFT_QL1_MAX_T[20]	s	
6643	uint	_FFT_QL1_MAX_T[21]	s	
6645	uint	_FFT_QL1_MAX_T[22]	s	
6647	uint	_FFT_QL1_MAX_T[23]	s	
6649	uint	_FFT_QL1_MAX_T[24]	s	
6651	uint	_FFT_QL1_MAX_T[25]	s	
6653	uint	_FFT_QL1_MAX_T[26]	s	
6655	uint	_FFT_QL1_MAX_T[27]	s	
6657	uint	_FFT_QL1_MAX_T[28]	s	
6659	uint	_FFT_QL1_MAX_T[29]	s	
6661	uint	_FFT_QL1_MAX_T[30]	s	
6663	uint	_FFT_QL1_MAX_T[31]	s	
6665	uint	_FFT_QL1_MAX_T[32]	s	
6667	uint	_FFT_QL1_MAX_T[33]	s	
6669	uint	_FFT_QL1_MAX_T[34]	s	
6671	uint	_FFT_QL1_MAX_T[35]	s	
6673	uint	_FFT_QL1_MAX_T[36]	s	
6675	uint	_FFT_QL1_MAX_T[37]	s	
6677	uint	_FFT_QL1_MAX_T[38]	s	
6679	uint	_FFT_QL1_MAX_T[39]	s	
6681	uint	_FFT_QL2_MAX_T[0]	s	
6683	uint	_FFT_QL2_MAX_T[1]	s	
6685	uint	_FFT_QL2_MAX_T[2]	s	
6687	uint	_FFT_QL2_MAX_T[3]	s	
6689	uint	_FFT_QL2_MAX_T[4]	s	
6691	uint	_FFT_QL2_MAX_T[5]	s	
6693	uint	_FFT_QL2_MAX_T[6]	s	
6695	uint	_FFT_QL2_MAX_T[7]	s	
6697	uint	_FFT_QL2_MAX_T[8]	s	
6699	uint	_FFT_QL2_MAX_T[9]	s	
6701	uint	_FFT_QL2_MAX_T[10]	s	
6703	uint	_FFT_QL2_MAX_T[11]	s	
6705	uint	_FFT_QL2_MAX_T[12]	s	
6707	uint	_FFT_QL2_MAX_T[13]	s	
6709	uint	_FFT_QL2_MAX_T[14]	s	
6711	uint	_FFT_QL2_MAX_T[15]	s	
6713	uint	_FFT_QL2_MAX_T[16]	s	
6715	uint	_FFT_QL2_MAX_T[17]	s	
6717	uint	_FFT_QL2_MAX_T[18]	s	
6719	uint	_FFT_QL2_MAX_T[19]	s	
6721	uint	_FFT_QL2_MAX_T[20]	s	
6723	uint	_FFT_QL2_MAX_T[21]	s	
6725	uint	_FFT_QL2_MAX_T[22]	s	
6727	uint	_FFT_QL2_MAX_T[23]	s	

Address	Format	Designation	Unit	Remarks
6729	uint	_FFT_QL2_MAX_T[24]	s	
6731	uint	_FFT_QL2_MAX_T[25]	s	
6733	uint	_FFT_QL2_MAX_T[26]	s	Time stamp (UTC-time)
6735	uint	_FFT_QL2_MAX_T[27]	s	
6737	uint	_FFT_QL2_MAX_T[28]	s	
6739	uint	_FFT_QL2_MAX_T[29]	s	
6741	uint	_FFT_QL2_MAX_T[30]	s	
6743	uint	_FFT_QL2_MAX_T[31]	s	
6745	uint	_FFT_QL2_MAX_T[32]	s	
6747	uint	_FFT_QL2_MAX_T[33]	s	
6749	uint	_FFT_QL2_MAX_T[34]	s	
6751	uint	_FFT_QL2_MAX_T[35]	s	
6753	uint	_FFT_QL2_MAX_T[36]	s	
6755	uint	_FFT_QL2_MAX_T[37]	s	
6757	uint	_FFT_QL2_MAX_T[38]	s	
6759	uint	_FFT_QL2_MAX_T[39]	s	
6761	uint	_FFT_QL3_MAX_T[0]	s	
6763	uint	_FFT_QL3_MAX_T[1]	s	
6765	uint	_FFT_QL3_MAX_T[2]	s	
6767	uint	_FFT_QL3_MAX_T[3]	s	
6769	uint	_FFT_QL3_MAX_T[4]	s	
6771	uint	_FFT_QL3_MAX_T[5]	s	
6773	uint	_FFT_QL3_MAX_T[6]	s	
6775	uint	_FFT_QL3_MAX_T[7]	s	
6777	uint	_FFT_QL3_MAX_T[8]	s	
6779	uint	_FFT_QL3_MAX_T[9]	s	
6781	uint	_FFT_QL3_MAX_T[10]	s	
6783	uint	_FFT_QL3_MAX_T[11]	s	
6785	uint	_FFT_QL3_MAX_T[12]	s	
6787	uint	_FFT_QL3_MAX_T[13]	s	
6789	uint	_FFT_QL3_MAX_T[14]	s	
6791	uint	_FFT_QL3_MAX_T[15]	s	
6793	uint	_FFT_QL3_MAX_T[16]	s	
6795	uint	_FFT_QL3_MAX_T[17]	s	
6797	uint	_FFT_QL3_MAX_T[18]	s	
6799	uint	_FFT_QL3_MAX_T[19]	s	
6801	uint	_FFT_QL3_MAX_T[20]	s	
6803	uint	_FFT_QL3_MAX_T[21]	s	
6805	uint	_FFT_QL3_MAX_T[22]	s	
6807	uint	_FFT_QL3_MAX_T[23]	s	
6809	uint	_FFT_QL3_MAX_T[24]	s	
6811	uint	_FFT_QL3_MAX_T[25]	s	
6813	uint	_FFT_QL3_MAX_T[26]	s	
6815	uint	_FFT_QL3_MAX_T[27]	s	
6817	uint	_FFT_QL3_MAX_T[28]	s	
6819	uint	_FFT_QL3_MAX_T[29]	s	
6821	uint	_FFT_QL3_MAX_T[30]	s	
6823	uint	_FFT_QL3_MAX_T[31]	s	
6825	uint	_FFT_QL3_MAX_T[32]	s	
6827	uint	_FFT_QL3_MAX_T[33]	s	
6829	uint	_FFT_QL3_MAX_T[34]	s	
6831	uint	_FFT_QL3_MAX_T[35]	s	
6833	uint	_FFT_QL3_MAX_T[36]	s	
6835	uint	_FFT_QL3_MAX_T[37]	s	
6837	uint	_FFT_QL3_MAX_T[38]	s	
6839	uint	_FFT_QL3_MAX_T[39]	s	
6841	uint	_FFT_QL4_MAX_T[0]	s	
6843	uint	_FFT_QL4_MAX_T[1]	s	
6845	uint	_FFT_QL4_MAX_T[2]	s	

Address	Format	Designation	Unit	Remarks
6847	uint	_FFT_QL4_MAX_T[3]	s	
6849	uint	_FFT_QL4_MAX_T[4]	s	
6851	uint	_FFT_QL4_MAX_T[5]	s	
6853	uint	_FFT_QL4_MAX_T[6]	s	Time stamp (UTC-time)
6855	uint	_FFT_QL4_MAX_T[7]	s	
6857	uint	_FFT_QL4_MAX_T[8]	s	
6859	uint	_FFT_QL4_MAX_T[9]	s	
6861	uint	_FFT_QL4_MAX_T[10]	s	
6863	uint	_FFT_QL4_MAX_T[11]	s	
6865	uint	_FFT_QL4_MAX_T[12]	s	
6867	uint	_FFT_QL4_MAX_T[13]	s	
6869	uint	_FFT_QL4_MAX_T[14]	s	
6871	uint	_FFT_QL4_MAX_T[15]	s	
6873	uint	_FFT_QL4_MAX_T[16]	s	
6875	uint	_FFT_QL4_MAX_T[17]	s	
6877	uint	_FFT_QL4_MAX_T[18]	s	
6879	uint	_FFT_QL4_MAX_T[19]	s	
6881	uint	_FFT_QL4_MAX_T[20]	s	
6883	uint	_FFT_QL4_MAX_T[21]	s	
6885	uint	_FFT_QL4_MAX_T[22]	s	
6887	uint	_FFT_QL4_MAX_T[23]	s	
6889	uint	_FFT_QL4_MAX_T[24]	s	
6891	uint	_FFT_QL4_MAX_T[25]	s	
6893	uint	_FFT_QL4_MAX_T[26]	s	
6895	uint	_FFT_QL4_MAX_T[27]	s	
6897	uint	_FFT_QL4_MAX_T[28]	s	
6899	uint	_FFT_QL4_MAX_T[29]	s	
6901	uint	_FFT_QL4_MAX_T[30]	s	
6903	uint	_FFT_QL4_MAX_T[31]	s	
6905	uint	_FFT_QL4_MAX_T[32]	s	
6907	uint	_FFT_QL4_MAX_T[33]	s	
6909	uint	_FFT_QL4_MAX_T[34]	s	
6911	uint	_FFT_QL4_MAX_T[35]	s	
6913	uint	_FFT_QL4_MAX_T[36]	s	
6915	uint	_FFT_QL4_MAX_T[37]	s	
6917	uint	_FFT_QL4_MAX_T[38]	s	
6919	uint	_FFT_QL4_MAX_T[39]	s	
6921	uint	_THD_ULN_MAX_T[0]	s	
6923	uint	_THD_ULN_MAX_T[1]	s	
6925	uint	_THD_ULN_MAX_T[2]	s	
6927	uint	_THD_ULN_MAX_T[3]	s	
6929	uint	_THD_ILN_MAX_T[0]	s	
6931	uint	_THD_ILN_MAX_T[1]	s	
6933	uint	_THD_ILN_MAX_T[2]	s	
6935	uint	_THD_ILN_MAX_T[3]	s	
6937	uint	_KFACT_MAX_T[0]	s	
6939	uint	_KFACT_MAX_T[1]	s	
6941	uint	_KFACT_MAX_T[2]	s	
6943	uint	_KFACT_MAX_T[3]	s	
6945	uint	_ULN_MAX_T[0]	s	
6947	uint	_ULN_MAX_T[1]	s	
6949	uint	_ULN_MAX_T[2]	s	
6951	uint	_ULN_MAX_T[3]	s	
6953	uint	_ILN_MAX_T[0]	s	
6955	uint	_ILN_MAX_T[1]	s	
6957	uint	_ILN_MAX_T[2]	s	
6959	uint	_ILN_MAX_T[3]	s	
6961	uint	_PLN_MAX_T[0]	s	
6963	uint	_PLN_MAX_T[1]	s	

Address	Format	Designation	Unit	Remarks
6965	uint	_PLN_MAX_T[2]	s	
6967	uint	_PLN_MAX_T[3]	s	
6969	uint	_QLN_MAX_T[0]	s	
6971	uint	_QLN_MAX_T[1]	s	
6973	uint	_QLN_MAX_T[2]	s	Time stamp (UTC-time)
6975	uint	_QLN_MAX_T[3]	s	
6977	uint	_SLN_MAX_T[0]	s	
6979	uint	_SLN_MAX_T[1]	s	
6981	uint	_SLN_MAX_T[2]	s	
6983	uint	_SLN_MAX_T[3]	s	
6985	uint	_ULL_MAX_T[0]	s	
6987	uint	_ULL_MAX_T[1]	s	
6989	uint	_ULL_MAX_T[2]	s	
6991	uint	_I_SUM3_MAX_T	s	
6993	uint	_I_SUM_MAX_T	s	
6995	uint	_S_SUM3_MAX_T	s	
6997	uint	_P_SUM3_MAX_T	s	
6999	uint	_Q_SUM3_MAX_T	s	
7001	uint	_S_SUM_MAX_T	s	
7003	uint	_P_SUM_MAX_T	s	
7005	uint	_Q_SUM_MAX_T	s	
7007	uint	_FREQ_MAX_T	s	
7009	uint	_N_MAX_T	s	
7011	uint	_M_MAX_T	s	
7013	uint	_G_MAX_T	s	
7015	uint	_SYM_MAX_T	s	
7017	uint	_IN_MAX_T	s	
7019	uint	_IM_MAX_T	s	
7021	uint	_IG_MAX_T	s	
7023	uint	_S0_POWER_MAX_T[0]	s	
7025	uint	_S0_POWER_MAX_T[1]	s	
7027	uint	_EXT_TEMPERATUR_MAX_T	s	
7029	float	_FFT_UL1_AVG_MAX[0]	V	
7031	float	_FFT_UL1_AVG_MAX[1]	V	
7033	float	_FFT_UL1_AVG_MAX[2]	V	
7035	float	_FFT_UL1_AVG_MAX[3]	V	
7037	float	_FFT_UL1_AVG_MAX[4]	V	
7039	float	_FFT_UL1_AVG_MAX[5]	V	
7041	float	_FFT_UL1_AVG_MAX[6]	V	
7043	float	_FFT_UL1_AVG_MAX[7]	V	
7045	float	_FFT_UL1_AVG_MAX[8]	V	
7047	float	_FFT_UL1_AVG_MAX[9]	V	
7049	float	_FFT_UL1_AVG_MAX[10]	V	
7051	float	_FFT_UL1_AVG_MAX[11]	V	
7053	float	_FFT_UL1_AVG_MAX[12]	V	
7055	float	_FFT_UL1_AVG_MAX[13]	V	
7057	float	_FFT_UL1_AVG_MAX[14]	V	
7059	float	_FFT_UL1_AVG_MAX[15]	V	
7061	float	_FFT_UL1_AVG_MAX[16]	V	
7063	float	_FFT_UL1_AVG_MAX[17]	V	
7065	float	_FFT_UL1_AVG_MAX[18]	V	
7067	float	_FFT_UL1_AVG_MAX[19]	V	
7069	float	_FFT_UL1_AVG_MAX[20]	V	
7071	float	_FFT_UL1_AVG_MAX[21]	V	
7073	float	_FFT_UL1_AVG_MAX[22]	V	
7075	float	_FFT_UL1_AVG_MAX[23]	V	
7077	float	_FFT_UL1_AVG_MAX[24]	V	
7079	float	_FFT_UL1_AVG_MAX[25]	V	
7081	float	_FFT_UL1_AVG_MAX[26]	V	



Address	Format	Designation	Unit	Remarks
7083	float	_FFT_UL1_AVG_MAX[27]	V	
7085	float	_FFT_UL1_AVG_MAX[28]	V	
7087	float	_FFT_UL1_AVG_MAX[29]	V	
7089	float	_FFT_UL1_AVG_MAX[30]	V	
7091	float	_FFT_UL1_AVG_MAX[31]	V	
7093	float	_FFT_UL1_AVG_MAX[32]	V	
7095	float	_FFT_UL1_AVG_MAX[33]	V	
7097	float	_FFT_UL1_AVG_MAX[34]	V	
7099	float	_FFT_UL1_AVG_MAX[35]	V	
7101	float	_FFT_UL1_AVG_MAX[36]	V	
7103	float	_FFT_UL1_AVG_MAX[37]	V	
7105	float	_FFT_UL1_AVG_MAX[38]	V	
7107	float	_FFT_UL1_AVG_MAX[39]	V	
7109	float	_FFT_UL2_AVG_MAX[0]	V	
7111	float	_FFT_UL2_AVG_MAX[1]	V	
7113	float	_FFT_UL2_AVG_MAX[2]	V	
7115	float	_FFT_UL2_AVG_MAX[3]	V	
7117	float	_FFT_UL2_AVG_MAX[4]	V	
7119	float	_FFT_UL2_AVG_MAX[5]	V	
7121	float	_FFT_UL2_AVG_MAX[6]	V	
7123	float	_FFT_UL2_AVG_MAX[7]	V	
7125	float	_FFT_UL2_AVG_MAX[8]	V	
7127	float	_FFT_UL2_AVG_MAX[9]	V	
7129	float	_FFT_UL2_AVG_MAX[10]	V	
7131	float	_FFT_UL2_AVG_MAX[11]	V	
7133	float	_FFT_UL2_AVG_MAX[12]	V	
7135	float	_FFT_UL2_AVG_MAX[13]	V	
7137	float	_FFT_UL2_AVG_MAX[14]	V	
7139	float	_FFT_UL2_AVG_MAX[15]	V	
7141	float	_FFT_UL2_AVG_MAX[16]	V	
7143	float	_FFT_UL2_AVG_MAX[17]	V	
7145	float	_FFT_UL2_AVG_MAX[18]	V	
7147	float	_FFT_UL2_AVG_MAX[19]	V	
7149	float	_FFT_UL2_AVG_MAX[20]	V	
7151	float	_FFT_UL2_AVG_MAX[21]	V	
7153	float	_FFT_UL2_AVG_MAX[22]	V	
7155	float	_FFT_UL2_AVG_MAX[23]	V	
7157	float	_FFT_UL2_AVG_MAX[24]	V	
7159	float	_FFT_UL2_AVG_MAX[25]	V	
7161	float	_FFT_UL2_AVG_MAX[26]	V	
7163	float	_FFT_UL2_AVG_MAX[27]	V	
7165	float	_FFT_UL2_AVG_MAX[28]	V	
7167	float	_FFT_UL2_AVG_MAX[29]	V	
7169	float	_FFT_UL2_AVG_MAX[30]	V	
7171	float	_FFT_UL2_AVG_MAX[31]	V	
7173	float	_FFT_UL2_AVG_MAX[32]	V	
7175	float	_FFT_UL2_AVG_MAX[33]	V	
7177	float	_FFT_UL2_AVG_MAX[34]	V	
7179	float	_FFT_UL2_AVG_MAX[35]	V	
7181	float	_FFT_UL2_AVG_MAX[36]	V	
7183	float	_FFT_UL2_AVG_MAX[37]	V	
7185	float	_FFT_UL2_AVG_MAX[38]	V	
7187	float	_FFT_UL2_AVG_MAX[39]	V	
7189	float	_FFT_UL3_AVG_MAX[0]	V	
7191	float	_FFT_UL3_AVG_MAX[1]	V	
7193	float	_FFT_UL3_AVG_MAX[2]	V	
7195	float	_FFT_UL3_AVG_MAX[3]	V	
7197	float	_FFT_UL3_AVG_MAX[4]	V	
7199	float	_FFT_UL3_AVG_MAX[5]	V	



Address	Format	Designation	Unit	Remarks
7201	float	_FFT_UL3_AVG_MAX[6]	V	
7203	float	_FFT_UL3_AVG_MAX[7]	V	
7205	float	_FFT_UL3_AVG_MAX[8]	V	
7207	float	_FFT_UL3_AVG_MAX[9]	V	
7209	float	_FFT_UL3_AVG_MAX[10]	V	
7211	float	_FFT_UL3_AVG_MAX[11]	V	
7213	float	_FFT_UL3_AVG_MAX[12]	V	
7215	float	_FFT_UL3_AVG_MAX[13]	V	
7217	float	_FFT_UL3_AVG_MAX[14]	V	
7219	float	_FFT_UL3_AVG_MAX[15]	V	
7221	float	_FFT_UL3_AVG_MAX[16]	V	
7223	float	_FFT_UL3_AVG_MAX[17]	V	
7225	float	_FFT_UL3_AVG_MAX[18]	V	
7227	float	_FFT_UL3_AVG_MAX[19]	V	
7229	float	_FFT_UL3_AVG_MAX[20]	V	
7231	float	_FFT_UL3_AVG_MAX[21]	V	
7233	float	_FFT_UL3_AVG_MAX[22]	V	
7235	float	_FFT_UL3_AVG_MAX[23]	V	
7237	float	_FFT_UL3_AVG_MAX[24]	V	
7239	float	_FFT_UL3_AVG_MAX[25]	V	
7241	float	_FFT_UL3_AVG_MAX[26]	V	
7243	float	_FFT_UL3_AVG_MAX[27]	V	
7245	float	_FFT_UL3_AVG_MAX[28]	V	
7247	float	_FFT_UL3_AVG_MAX[29]	V	
7249	float	_FFT_UL3_AVG_MAX[30]	V	
7251	float	_FFT_UL3_AVG_MAX[31]	V	
7253	float	_FFT_UL3_AVG_MAX[32]	V	
7255	float	_FFT_UL3_AVG_MAX[33]	V	
7257	float	_FFT_UL3_AVG_MAX[34]	V	
7259	float	_FFT_UL3_AVG_MAX[35]	V	
7261	float	_FFT_UL3_AVG_MAX[36]	V	
7263	float	_FFT_UL3_AVG_MAX[37]	V	
7265	float	_FFT_UL3_AVG_MAX[38]	V	
7267	float	_FFT_UL3_AVG_MAX[39]	V	
7269	float	_FFT_UL4_AVG_MAX[0]	V	
7271	float	_FFT_UL4_AVG_MAX[1]	V	
7273	float	_FFT_UL4_AVG_MAX[2]	V	
7275	float	_FFT_UL4_AVG_MAX[3]	V	
7277	float	_FFT_UL4_AVG_MAX[4]	V	
7279	float	_FFT_UL4_AVG_MAX[5]	V	
7281	float	_FFT_UL4_AVG_MAX[6]	V	
7283	float	_FFT_UL4_AVG_MAX[7]	V	
7285	float	_FFT_UL4_AVG_MAX[8]	V	
7287	float	_FFT_UL4_AVG_MAX[9]	V	
7289	float	_FFT_UL4_AVG_MAX[10]	V	
7291	float	_FFT_UL4_AVG_MAX[11]	V	
7293	float	_FFT_UL4_AVG_MAX[12]	V	
7295	float	_FFT_UL4_AVG_MAX[13]	V	
7297	float	_FFT_UL4_AVG_MAX[14]	V	
7299	float	_FFT_UL4_AVG_MAX[15]	V	
7301	float	_FFT_UL4_AVG_MAX[16]	V	
7303	float	_FFT_UL4_AVG_MAX[17]	V	
7305	float	_FFT_UL4_AVG_MAX[18]	V	
7307	float	_FFT_UL4_AVG_MAX[19]	V	
7309	float	_FFT_UL4_AVG_MAX[20]	V	
7311	float	_FFT_UL4_AVG_MAX[21]	V	
7313	float	_FFT_UL4_AVG_MAX[22]	V	
7315	float	_FFT_UL4_AVG_MAX[23]	V	
7317	float	_FFT_UL4_AVG_MAX[24]	V	

Address	Format	Designation	Unit	Remarks
7319	float	_FFT_UL4_AVG_MAX[25]	V	
7321	float	_FFT_UL4_AVG_MAX[26]	V	
7323	float	_FFT_UL4_AVG_MAX[27]	V	
7325	float	_FFT_UL4_AVG_MAX[28]	V	
7327	float	_FFT_UL4_AVG_MAX[29]	V	
7329	float	_FFT_UL4_AVG_MAX[30]	V	
7331	float	_FFT_UL4_AVG_MAX[31]	V	
7333	float	_FFT_UL4_AVG_MAX[32]	V	
7335	float	_FFT_UL4_AVG_MAX[33]	V	
7337	float	_FFT_UL4_AVG_MAX[34]	V	
7339	float	_FFT_UL4_AVG_MAX[35]	V	
7341	float	_FFT_UL4_AVG_MAX[36]	V	
7343	float	_FFT_UL4_AVG_MAX[37]	V	
7345	float	_FFT_UL4_AVG_MAX[38]	V	
7347	float	_FFT_UL4_AVG_MAX[39]	V	
7349	float	_FFT_IL1_AVG_MAX[0]	A	
7351	float	_FFT_IL1_AVG_MAX[1]	A	
7353	float	_FFT_IL1_AVG_MAX[2]	A	
7355	float	_FFT_IL1_AVG_MAX[3]	A	
7357	float	_FFT_IL1_AVG_MAX[4]	A	
7359	float	_FFT_IL1_AVG_MAX[5]	A	
7361	float	_FFT_IL1_AVG_MAX[6]	A	
7363	float	_FFT_IL1_AVG_MAX[7]	A	
7365	float	_FFT_IL1_AVG_MAX[8]	A	
7367	float	_FFT_IL1_AVG_MAX[9]	A	
7369	float	_FFT_IL1_AVG_MAX[10]	A	
7371	float	_FFT_IL1_AVG_MAX[11]	A	
7373	float	_FFT_IL1_AVG_MAX[12]	A	
7375	float	_FFT_IL1_AVG_MAX[13]	A	
7377	float	_FFT_IL1_AVG_MAX[14]	A	
7379	float	_FFT_IL1_AVG_MAX[15]	A	
7381	float	_FFT_IL1_AVG_MAX[16]	A	
7383	float	_FFT_IL1_AVG_MAX[17]	A	
7385	float	_FFT_IL1_AVG_MAX[18]	A	
7387	float	_FFT_IL1_AVG_MAX[19]	A	
7389	float	_FFT_IL1_AVG_MAX[20]	A	
7391	float	_FFT_IL1_AVG_MAX[21]	A	
7393	float	_FFT_IL1_AVG_MAX[22]	A	
7395	float	_FFT_IL1_AVG_MAX[23]	A	
7397	float	_FFT_IL1_AVG_MAX[24]	A	
7399	float	_FFT_IL1_AVG_MAX[25]	A	
7401	float	_FFT_IL1_AVG_MAX[26]	A	
7403	float	_FFT_IL1_AVG_MAX[27]	A	
7405	float	_FFT_IL1_AVG_MAX[28]	A	
7407	float	_FFT_IL1_AVG_MAX[29]	A	
7409	float	_FFT_IL1_AVG_MAX[30]	A	
7411	float	_FFT_IL1_AVG_MAX[31]	A	
7413	float	_FFT_IL1_AVG_MAX[32]	A	
7415	float	_FFT_IL1_AVG_MAX[33]	A	
7417	float	_FFT_IL1_AVG_MAX[34]	A	
7419	float	_FFT_IL1_AVG_MAX[35]	A	
7421	float	_FFT_IL1_AVG_MAX[36]	A	
7423	float	_FFT_IL1_AVG_MAX[37]	A	
7425	float	_FFT_IL1_AVG_MAX[38]	A	
7427	float	_FFT_IL1_AVG_MAX[39]	A	
7429	float	_FFT_IL2_AVG_MAX[0]	A	
7431	float	_FFT_IL2_AVG_MAX[1]	A	
7433	float	_FFT_IL2_AVG_MAX[2]	A	
7435	float	_FFT_IL2_AVG_MAX[3]	A	

Address	Format	Designation	Unit	Remarks
7437	float	_FFT_IL2_AVG_MAX[4]	A	
7439	float	_FFT_IL2_AVG_MAX[5]	A	
7441	float	_FFT_IL2_AVG_MAX[6]	A	
7443	float	_FFT_IL2_AVG_MAX[7]	A	
7445	float	_FFT_IL2_AVG_MAX[8]	A	
7447	float	_FFT_IL2_AVG_MAX[9]	A	
7449	float	_FFT_IL2_AVG_MAX[10]	A	
7451	float	_FFT_IL2_AVG_MAX[11]	A	
7453	float	_FFT_IL2_AVG_MAX[12]	A	
7455	float	_FFT_IL2_AVG_MAX[13]	A	
7457	float	_FFT_IL2_AVG_MAX[14]	A	
7459	float	_FFT_IL2_AVG_MAX[15]	A	
7461	float	_FFT_IL2_AVG_MAX[16]	A	
7463	float	_FFT_IL2_AVG_MAX[17]	A	
7465	float	_FFT_IL2_AVG_MAX[18]	A	
7467	float	_FFT_IL2_AVG_MAX[19]	A	
7469	float	_FFT_IL2_AVG_MAX[20]	A	
7471	float	_FFT_IL2_AVG_MAX[21]	A	
7473	float	_FFT_IL2_AVG_MAX[22]	A	
7475	float	_FFT_IL2_AVG_MAX[23]	A	
7477	float	_FFT_IL2_AVG_MAX[24]	A	
7479	float	_FFT_IL2_AVG_MAX[25]	A	
7481	float	_FFT_IL2_AVG_MAX[26]	A	
7483	float	_FFT_IL2_AVG_MAX[27]	A	
7485	float	_FFT_IL2_AVG_MAX[28]	A	
7487	float	_FFT_IL2_AVG_MAX[29]	A	
7489	float	_FFT_IL2_AVG_MAX[30]	A	
7491	float	_FFT_IL2_AVG_MAX[31]	A	
7493	float	_FFT_IL2_AVG_MAX[32]	A	
7495	float	_FFT_IL2_AVG_MAX[33]	A	
7497	float	_FFT_IL2_AVG_MAX[34]	A	
7499	float	_FFT_IL2_AVG_MAX[35]	A	
7501	float	_FFT_IL2_AVG_MAX[36]	A	
7503	float	_FFT_IL2_AVG_MAX[37]	A	
7505	float	_FFT_IL2_AVG_MAX[38]	A	
7507	float	_FFT_IL2_AVG_MAX[39]	A	
7509	float	_FFT_IL3_AVG_MAX[0]	A	
7511	float	_FFT_IL3_AVG_MAX[1]	A	
7513	float	_FFT_IL3_AVG_MAX[2]	A	
7515	float	_FFT_IL3_AVG_MAX[3]	A	
7517	float	_FFT_IL3_AVG_MAX[4]	A	
7519	float	_FFT_IL3_AVG_MAX[5]	A	
7521	float	_FFT_IL3_AVG_MAX[6]	A	
7523	float	_FFT_IL3_AVG_MAX[7]	A	
7525	float	_FFT_IL3_AVG_MAX[8]	A	
7527	float	_FFT_IL3_AVG_MAX[9]	A	
7529	float	_FFT_IL3_AVG_MAX[10]	A	
7531	float	_FFT_IL3_AVG_MAX[11]	A	
7533	float	_FFT_IL3_AVG_MAX[12]	A	
7535	float	_FFT_IL3_AVG_MAX[13]	A	
7537	float	_FFT_IL3_AVG_MAX[14]	A	
7539	float	_FFT_IL3_AVG_MAX[15]	A	
7541	float	_FFT_IL3_AVG_MAX[16]	A	
7543	float	_FFT_IL3_AVG_MAX[17]	A	
7545	float	_FFT_IL3_AVG_MAX[18]	A	
7547	float	_FFT_IL3_AVG_MAX[19]	A	
7549	float	_FFT_IL3_AVG_MAX[20]	A	
7551	float	_FFT_IL3_AVG_MAX[21]	A	
7553	float	_FFT_IL3_AVG_MAX[22]	A	

Address	Format	Designation	Unit	Remarks
7555	float	_FFT_IL3_AVG_MAX[23]	A	
7557	float	_FFT_IL3_AVG_MAX[24]	A	
7559	float	_FFT_IL3_AVG_MAX[25]	A	
7561	float	_FFT_IL3_AVG_MAX[26]	A	
7563	float	_FFT_IL3_AVG_MAX[27]	A	
7565	float	_FFT_IL3_AVG_MAX[28]	A	
7567	float	_FFT_IL3_AVG_MAX[29]	A	
7569	float	_FFT_IL3_AVG_MAX[30]	A	
7571	float	_FFT_IL3_AVG_MAX[31]	A	
7573	float	_FFT_IL3_AVG_MAX[32]	A	
7575	float	_FFT_IL3_AVG_MAX[33]	A	
7577	float	_FFT_IL3_AVG_MAX[34]	A	
7579	float	_FFT_IL3_AVG_MAX[35]	A	
7581	float	_FFT_IL3_AVG_MAX[36]	A	
7583	float	_FFT_IL3_AVG_MAX[37]	A	
7585	float	_FFT_IL3_AVG_MAX[38]	A	
7587	float	_FFT_IL3_AVG_MAX[39]	A	
7589	float	_FFT_IL4_AVG_MAX[0]	A	
7591	float	_FFT_IL4_AVG_MAX[1]	A	
7593	float	_FFT_IL4_AVG_MAX[2]	A	
7595	float	_FFT_IL4_AVG_MAX[3]	A	
7597	float	_FFT_IL4_AVG_MAX[4]	A	
7599	float	_FFT_IL4_AVG_MAX[5]	A	
7601	float	_FFT_IL4_AVG_MAX[6]	A	
7603	float	_FFT_IL4_AVG_MAX[7]	A	
7605	float	_FFT_IL4_AVG_MAX[8]	A	
7607	float	_FFT_IL4_AVG_MAX[9]	A	
7609	float	_FFT_IL4_AVG_MAX[10]	A	
7611	float	_FFT_IL4_AVG_MAX[11]	A	
7613	float	_FFT_IL4_AVG_MAX[12]	A	
7615	float	_FFT_IL4_AVG_MAX[13]	A	
7617	float	_FFT_IL4_AVG_MAX[14]	A	
7619	float	_FFT_IL4_AVG_MAX[15]	A	
7621	float	_FFT_IL4_AVG_MAX[16]	A	
7623	float	_FFT_IL4_AVG_MAX[17]	A	
7625	float	_FFT_IL4_AVG_MAX[18]	A	
7627	float	_FFT_IL4_AVG_MAX[19]	A	
7629	float	_FFT_IL4_AVG_MAX[20]	A	
7631	float	_FFT_IL4_AVG_MAX[21]	A	
7633	float	_FFT_IL4_AVG_MAX[22]	A	
7635	float	_FFT_IL4_AVG_MAX[23]	A	
7637	float	_FFT_IL4_AVG_MAX[24]	A	
7639	float	_FFT_IL4_AVG_MAX[25]	A	
7641	float	_FFT_IL4_AVG_MAX[26]	A	
7643	float	_FFT_IL4_AVG_MAX[27]	A	
7645	float	_FFT_IL4_AVG_MAX[28]	A	
7647	float	_FFT_IL4_AVG_MAX[29]	A	
7649	float	_FFT_IL4_AVG_MAX[30]	A	
7651	float	_FFT_IL4_AVG_MAX[31]	A	
7653	float	_FFT_IL4_AVG_MAX[32]	A	
7655	float	_FFT_IL4_AVG_MAX[33]	A	
7657	float	_FFT_IL4_AVG_MAX[34]	A	
7659	float	_FFT_IL4_AVG_MAX[35]	A	
7661	float	_FFT_IL4_AVG_MAX[36]	A	
7663	float	_FFT_IL4_AVG_MAX[37]	A	
7665	float	_FFT_IL4_AVG_MAX[38]	A	
7667	float	_FFT_IL4_AVG_MAX[39]	A	
7669	float	_FFT_PL1_AVG_MAX[0]	W	
7671	float	_FFT_PL1_AVG_MAX[1]	W	

Address	Format	Designation	Unit	Remarks
7673	float	_FFT_PL1_AVG_MAX[2]	W	
7675	float	_FFT_PL1_AVG_MAX[3]	W	
7677	float	_FFT_PL1_AVG_MAX[4]	W	
7679	float	_FFT_PL1_AVG_MAX[5]	W	
7681	float	_FFT_PL1_AVG_MAX[6]	W	
7683	float	_FFT_PL1_AVG_MAX[7]	W	
7685	float	_FFT_PL1_AVG_MAX[8]	W	
7687	float	_FFT_PL1_AVG_MAX[9]	W	
7689	float	_FFT_PL1_AVG_MAX[10]	W	
7691	float	_FFT_PL1_AVG_MAX[11]	W	
7693	float	_FFT_PL1_AVG_MAX[12]	W	
7695	float	_FFT_PL1_AVG_MAX[13]	W	
7697	float	_FFT_PL1_AVG_MAX[14]	W	
7699	float	_FFT_PL1_AVG_MAX[15]	W	
7701	float	_FFT_PL1_AVG_MAX[16]	W	
7703	float	_FFT_PL1_AVG_MAX[17]	W	
7705	float	_FFT_PL1_AVG_MAX[18]	W	
7707	float	_FFT_PL1_AVG_MAX[19]	W	
7709	float	_FFT_PL1_AVG_MAX[20]	W	
7711	float	_FFT_PL1_AVG_MAX[21]	W	
7713	float	_FFT_PL1_AVG_MAX[22]	W	
7715	float	_FFT_PL1_AVG_MAX[23]	W	
7717	float	_FFT_PL1_AVG_MAX[24]	W	
7719	float	_FFT_PL1_AVG_MAX[25]	W	
7721	float	_FFT_PL1_AVG_MAX[26]	W	
7723	float	_FFT_PL1_AVG_MAX[27]	W	
7725	float	_FFT_PL1_AVG_MAX[28]	W	
7727	float	_FFT_PL1_AVG_MAX[29]	W	
7729	float	_FFT_PL1_AVG_MAX[30]	W	
7731	float	_FFT_PL1_AVG_MAX[31]	W	
7733	float	_FFT_PL1_AVG_MAX[32]	W	
7735	float	_FFT_PL1_AVG_MAX[33]	W	
7737	float	_FFT_PL1_AVG_MAX[34]	W	
7739	float	_FFT_PL1_AVG_MAX[35]	W	
7741	float	_FFT_PL1_AVG_MAX[36]	W	
7743	float	_FFT_PL1_AVG_MAX[37]	W	
7745	float	_FFT_PL1_AVG_MAX[38]	W	
7747	float	_FFT_PL1_AVG_MAX[39]	W	
7749	float	_FFT_PL2_AVG_MAX[0]	W	
7751	float	_FFT_PL2_AVG_MAX[1]	W	
7753	float	_FFT_PL2_AVG_MAX[2]	W	
7755	float	_FFT_PL2_AVG_MAX[3]	W	
7757	float	_FFT_PL2_AVG_MAX[4]	W	
7759	float	_FFT_PL2_AVG_MAX[5]	W	
7761	float	_FFT_PL2_AVG_MAX[6]	W	
7763	float	_FFT_PL2_AVG_MAX[7]	W	
7765	float	_FFT_PL2_AVG_MAX[8]	W	
7767	float	_FFT_PL2_AVG_MAX[9]	W	
7769	float	_FFT_PL2_AVG_MAX[10]	W	
7771	float	_FFT_PL2_AVG_MAX[11]	W	
7773	float	_FFT_PL2_AVG_MAX[12]	W	
7775	float	_FFT_PL2_AVG_MAX[13]	W	
7777	float	_FFT_PL2_AVG_MAX[14]	W	
7779	float	_FFT_PL2_AVG_MAX[15]	W	
7781	float	_FFT_PL2_AVG_MAX[16]	W	
7783	float	_FFT_PL2_AVG_MAX[17]	W	
7785	float	_FFT_PL2_AVG_MAX[18]	W	
7787	float	_FFT_PL2_AVG_MAX[19]	W	
7789	float	_FFT_PL2_AVG_MAX[20]	W	

Address	Format	Designation	Unit	Remarks
7791	float	_FFT_PL2_AVG_MAX[21]	W	
7793	float	_FFT_PL2_AVG_MAX[22]	W	
7795	float	_FFT_PL2_AVG_MAX[23]	W	
7797	float	_FFT_PL2_AVG_MAX[24]	W	
7799	float	_FFT_PL2_AVG_MAX[25]	W	
7801	float	_FFT_PL2_AVG_MAX[26]	W	
7803	float	_FFT_PL2_AVG_MAX[27]	W	
7805	float	_FFT_PL2_AVG_MAX[28]	W	
7807	float	_FFT_PL2_AVG_MAX[29]	W	
7809	float	_FFT_PL2_AVG_MAX[30]	W	
7811	float	_FFT_PL2_AVG_MAX[31]	W	
7813	float	_FFT_PL2_AVG_MAX[32]	W	
7815	float	_FFT_PL2_AVG_MAX[33]	W	
7817	float	_FFT_PL2_AVG_MAX[34]	W	
7819	float	_FFT_PL2_AVG_MAX[35]	W	
7821	float	_FFT_PL2_AVG_MAX[36]	W	
7823	float	_FFT_PL2_AVG_MAX[37]	W	
7825	float	_FFT_PL2_AVG_MAX[38]	W	
7827	float	_FFT_PL2_AVG_MAX[39]	W	
7829	float	_FFT_PL3_AVG_MAX[0]	W	
7831	float	_FFT_PL3_AVG_MAX[1]	W	
7833	float	_FFT_PL3_AVG_MAX[2]	W	
7835	float	_FFT_PL3_AVG_MAX[3]	W	
7837	float	_FFT_PL3_AVG_MAX[4]	W	
7839	float	_FFT_PL3_AVG_MAX[5]	W	
7841	float	_FFT_PL3_AVG_MAX[6]	W	
7843	float	_FFT_PL3_AVG_MAX[7]	W	
7845	float	_FFT_PL3_AVG_MAX[8]	W	
7847	float	_FFT_PL3_AVG_MAX[9]	W	
7849	float	_FFT_PL3_AVG_MAX[10]	W	
7851	float	_FFT_PL3_AVG_MAX[11]	W	
7853	float	_FFT_PL3_AVG_MAX[12]	W	
7855	float	_FFT_PL3_AVG_MAX[13]	W	
7857	float	_FFT_PL3_AVG_MAX[14]	W	
7859	float	_FFT_PL3_AVG_MAX[15]	W	
7861	float	_FFT_PL3_AVG_MAX[16]	W	
7863	float	_FFT_PL3_AVG_MAX[17]	W	
7865	float	_FFT_PL3_AVG_MAX[18]	W	
7867	float	_FFT_PL3_AVG_MAX[19]	W	
7869	float	_FFT_PL3_AVG_MAX[20]	W	
7871	float	_FFT_PL3_AVG_MAX[21]	W	
7873	float	_FFT_PL3_AVG_MAX[22]	W	
7875	float	_FFT_PL3_AVG_MAX[23]	W	
7877	float	_FFT_PL3_AVG_MAX[24]	W	
7879	float	_FFT_PL3_AVG_MAX[25]	W	
7881	float	_FFT_PL3_AVG_MAX[26]	W	
7883	float	_FFT_PL3_AVG_MAX[27]	W	
7885	float	_FFT_PL3_AVG_MAX[28]	W	
7887	float	_FFT_PL3_AVG_MAX[29]	W	
7889	float	_FFT_PL3_AVG_MAX[30]	W	
7891	float	_FFT_PL3_AVG_MAX[31]	W	
7893	float	_FFT_PL3_AVG_MAX[32]	W	
7895	float	_FFT_PL3_AVG_MAX[33]	W	
7897	float	_FFT_PL3_AVG_MAX[34]	W	
7899	float	_FFT_PL3_AVG_MAX[35]	W	
7901	float	_FFT_PL3_AVG_MAX[36]	W	
7903	float	_FFT_PL3_AVG_MAX[37]	W	
7905	float	_FFT_PL3_AVG_MAX[38]	W	
7907	float	_FFT_PL3_AVG_MAX[39]	W	



Address	Format	Designation	Unit	Remarks
7909	float	_FFT_PL4_AVG_MAX[0]	W	
7911	float	_FFT_PL4_AVG_MAX[1]	W	
7913	float	_FFT_PL4_AVG_MAX[2]	W	
7915	float	_FFT_PL4_AVG_MAX[3]	W	
7917	float	_FFT_PL4_AVG_MAX[4]	W	
7919	float	_FFT_PL4_AVG_MAX[5]	W	
7921	float	_FFT_PL4_AVG_MAX[6]	W	
7923	float	_FFT_PL4_AVG_MAX[7]	W	
7925	float	_FFT_PL4_AVG_MAX[8]	W	
7927	float	_FFT_PL4_AVG_MAX[9]	W	
7929	float	_FFT_PL4_AVG_MAX[10]	W	
7931	float	_FFT_PL4_AVG_MAX[11]	W	
7933	float	_FFT_PL4_AVG_MAX[12]	W	
7935	float	_FFT_PL4_AVG_MAX[13]	W	
7937	float	_FFT_PL4_AVG_MAX[14]	W	
7939	float	_FFT_PL4_AVG_MAX[15]	W	
7941	float	_FFT_PL4_AVG_MAX[16]	W	
7943	float	_FFT_PL4_AVG_MAX[17]	W	
7945	float	_FFT_PL4_AVG_MAX[18]	W	
7947	float	_FFT_PL4_AVG_MAX[19]	W	
7949	float	_FFT_PL4_AVG_MAX[20]	W	
7951	float	_FFT_PL4_AVG_MAX[21]	W	
7953	float	_FFT_PL4_AVG_MAX[22]	W	
7955	float	_FFT_PL4_AVG_MAX[23]	W	
7957	float	_FFT_PL4_AVG_MAX[24]	W	
7959	float	_FFT_PL4_AVG_MAX[25]	W	
7961	float	_FFT_PL4_AVG_MAX[26]	W	
7963	float	_FFT_PL4_AVG_MAX[27]	W	
7965	float	_FFT_PL4_AVG_MAX[28]	W	
7967	float	_FFT_PL4_AVG_MAX[29]	W	
7969	float	_FFT_PL4_AVG_MAX[30]	W	
7971	float	_FFT_PL4_AVG_MAX[31]	W	
7973	float	_FFT_PL4_AVG_MAX[32]	W	
7975	float	_FFT_PL4_AVG_MAX[33]	W	
7977	float	_FFT_PL4_AVG_MAX[34]	W	
7979	float	_FFT_PL4_AVG_MAX[35]	W	
7981	float	_FFT_PL4_AVG_MAX[36]	W	
7983	float	_FFT_PL4_AVG_MAX[37]	W	
7985	float	_FFT_PL4_AVG_MAX[38]	W	
7987	float	_FFT_PL4_AVG_MAX[39]	W	
7989	float	_FFT_QL1_AVG_MAX[0]	VAr	
7991	float	_FFT_QL1_AVG_MAX[1]	VAr	
7993	float	_FFT_QL1_AVG_MAX[2]	VAr	
7995	float	_FFT_QL1_AVG_MAX[3]	VAr	
7997	float	_FFT_QL1_AVG_MAX[4]	VAr	
7999	float	_FFT_QL1_AVG_MAX[5]	VAr	
8001	float	_FFT_QL1_AVG_MAX[6]	VAr	
8003	float	_FFT_QL1_AVG_MAX[7]	VAr	
8005	float	_FFT_QL1_AVG_MAX[8]	VAr	
8007	float	_FFT_QL1_AVG_MAX[9]	VAr	
8009	float	_FFT_QL1_AVG_MAX[10]	VAr	
8011	float	_FFT_QL1_AVG_MAX[11]	VAr	
8013	float	_FFT_QL1_AVG_MAX[12]	VAr	
8015	float	_FFT_QL1_AVG_MAX[13]	VAr	
8017	float	_FFT_QL1_AVG_MAX[14]	VAr	
8019	float	_FFT_QL1_AVG_MAX[15]	VAr	
8021	float	_FFT_QL1_AVG_MAX[16]	VAr	
8023	float	_FFT_QL1_AVG_MAX[17]	VAr	
8025	float	_FFT_QL1_AVG_MAX[18]	VAr	

Address	Format	Designation	Unit	Remarks
8027	float	_FFT_QL1_AVG_MAX[19]	VAr	
8029	float	_FFT_QL1_AVG_MAX[20]	VAr	
8031	float	_FFT_QL1_AVG_MAX[21]	VAr	
8033	float	_FFT_QL1_AVG_MAX[22]	VAr	
8035	float	_FFT_QL1_AVG_MAX[23]	VAr	
8037	float	_FFT_QL1_AVG_MAX[24]	VAr	
8039	float	_FFT_QL1_AVG_MAX[25]	VAr	
8041	float	_FFT_QL1_AVG_MAX[26]	VAr	
8043	float	_FFT_QL1_AVG_MAX[27]	VAr	
8045	float	_FFT_QL1_AVG_MAX[28]	VAr	
8047	float	_FFT_QL1_AVG_MAX[29]	VAr	
8049	float	_FFT_QL1_AVG_MAX[30]	VAr	
8051	float	_FFT_QL1_AVG_MAX[31]	VAr	
8053	float	_FFT_QL1_AVG_MAX[32]	VAr	
8055	float	_FFT_QL1_AVG_MAX[33]	VAr	
8057	float	_FFT_QL1_AVG_MAX[34]	VAr	
8059	float	_FFT_QL1_AVG_MAX[35]	VAr	
8061	float	_FFT_QL1_AVG_MAX[36]	VAr	
8063	float	_FFT_QL1_AVG_MAX[37]	VAr	
8065	float	_FFT_QL1_AVG_MAX[38]	VAr	
8067	float	_FFT_QL1_AVG_MAX[39]	VAr	
8069	float	_FFT_QL2_AVG_MAX[0]	VAr	
8071	float	_FFT_QL2_AVG_MAX[1]	VAr	
8073	float	_FFT_QL2_AVG_MAX[2]	VAr	
8075	float	_FFT_QL2_AVG_MAX[3]	VAr	
8077	float	_FFT_QL2_AVG_MAX[4]	VAr	
8079	float	_FFT_QL2_AVG_MAX[5]	VAr	
8081	float	_FFT_QL2_AVG_MAX[6]	VAr	
8083	float	_FFT_QL2_AVG_MAX[7]	VAr	
8085	float	_FFT_QL2_AVG_MAX[8]	VAr	
8087	float	_FFT_QL2_AVG_MAX[9]	VAr	
8089	float	_FFT_QL2_AVG_MAX[10]	VAr	
8091	float	_FFT_QL2_AVG_MAX[11]	VAr	
8093	float	_FFT_QL2_AVG_MAX[12]	VAr	
8095	float	_FFT_QL2_AVG_MAX[13]	VAr	
8097	float	_FFT_QL2_AVG_MAX[14]	VAr	
8099	float	_FFT_QL2_AVG_MAX[15]	VAr	
8101	float	_FFT_QL2_AVG_MAX[16]	VAr	
8103	float	_FFT_QL2_AVG_MAX[17]	VAr	
8105	float	_FFT_QL2_AVG_MAX[18]	VAr	
8107	float	_FFT_QL2_AVG_MAX[19]	VAr	
8109	float	_FFT_QL2_AVG_MAX[20]	VAr	
8111	float	_FFT_QL2_AVG_MAX[21]	VAr	
8113	float	_FFT_QL2_AVG_MAX[22]	VAr	
8115	float	_FFT_QL2_AVG_MAX[23]	VAr	
8117	float	_FFT_QL2_AVG_MAX[24]	VAr	
8119	float	_FFT_QL2_AVG_MAX[25]	VAr	
8121	float	_FFT_QL2_AVG_MAX[26]	VAr	
8123	float	_FFT_QL2_AVG_MAX[27]	VAr	
8125	float	_FFT_QL2_AVG_MAX[28]	VAr	
8127	float	_FFT_QL2_AVG_MAX[29]	VAr	
8129	float	_FFT_QL2_AVG_MAX[30]	VAr	
8131	float	_FFT_QL2_AVG_MAX[31]	VAr	
8133	float	_FFT_QL2_AVG_MAX[32]	VAr	
8135	float	_FFT_QL2_AVG_MAX[33]	VAr	
8137	float	_FFT_QL2_AVG_MAX[34]	VAr	
8139	float	_FFT_QL2_AVG_MAX[35]	VAr	
8141	float	_FFT_QL2_AVG_MAX[36]	VAr	
8143	float	_FFT_QL2_AVG_MAX[37]	VAr	



Address	Format	Designation	Unit	Remarks
8145	float	_FFT_QL2_AVG_MAX[38]	VAr	
8147	float	_FFT_QL2_AVG_MAX[39]	VAr	
8149	float	_FFT_QL3_AVG_MAX[0]	VAr	
8151	float	_FFT_QL3_AVG_MAX[1]	VAr	
8153	float	_FFT_QL3_AVG_MAX[2]	VAr	
8155	float	_FFT_QL3_AVG_MAX[3]	VAr	
8157	float	_FFT_QL3_AVG_MAX[4]	VAr	
8159	float	_FFT_QL3_AVG_MAX[5]	VAr	
8161	float	_FFT_QL3_AVG_MAX[6]	VAr	
8163	float	_FFT_QL3_AVG_MAX[7]	VAr	
8165	float	_FFT_QL3_AVG_MAX[8]	VAr	
8167	float	_FFT_QL3_AVG_MAX[9]	VAr	
8169	float	_FFT_QL3_AVG_MAX[10]	VAr	
8171	float	_FFT_QL3_AVG_MAX[11]	VAr	
8173	float	_FFT_QL3_AVG_MAX[12]	VAr	
8175	float	_FFT_QL3_AVG_MAX[13]	VAr	
8177	float	_FFT_QL3_AVG_MAX[14]	VAr	
8179	float	_FFT_QL3_AVG_MAX[15]	VAr	
8181	float	_FFT_QL3_AVG_MAX[16]	VAr	
8183	float	_FFT_QL3_AVG_MAX[17]	VAr	
8185	float	_FFT_QL3_AVG_MAX[18]	VAr	
8187	float	_FFT_QL3_AVG_MAX[19]	VAr	
8189	float	_FFT_QL3_AVG_MAX[20]	VAr	
8191	float	_FFT_QL3_AVG_MAX[21]	VAr	
8193	float	_FFT_QL3_AVG_MAX[22]	VAr	
8195	float	_FFT_QL3_AVG_MAX[23]	VAr	
8197	float	_FFT_QL3_AVG_MAX[24]	VAr	
8199	float	_FFT_QL3_AVG_MAX[25]	VAr	
8201	float	_FFT_QL3_AVG_MAX[26]	VAr	
8203	float	_FFT_QL3_AVG_MAX[27]	VAr	
8205	float	_FFT_QL3_AVG_MAX[28]	VAr	
8207	float	_FFT_QL3_AVG_MAX[29]	VAr	
8209	float	_FFT_QL3_AVG_MAX[30]	VAr	
8211	float	_FFT_QL3_AVG_MAX[31]	VAr	
8213	float	_FFT_QL3_AVG_MAX[32]	VAr	
8215	float	_FFT_QL3_AVG_MAX[33]	VAr	
8217	float	_FFT_QL3_AVG_MAX[34]	VAr	
8219	float	_FFT_QL3_AVG_MAX[35]	VAr	
8221	float	_FFT_QL3_AVG_MAX[36]	VAr	
8223	float	_FFT_QL3_AVG_MAX[37]	VAr	
8225	float	_FFT_QL3_AVG_MAX[38]	VAr	
8227	float	_FFT_QL3_AVG_MAX[39]	VAr	
8229	float	_FFT_QL4_AVG_MAX[0]	VAr	
8231	float	_FFT_QL4_AVG_MAX[1]	VAr	
8233	float	_FFT_QL4_AVG_MAX[2]	VAr	
8235	float	_FFT_QL4_AVG_MAX[3]	VAr	
8237	float	_FFT_QL4_AVG_MAX[4]	VAr	
8239	float	_FFT_QL4_AVG_MAX[5]	VAr	
8241	float	_FFT_QL4_AVG_MAX[6]	VAr	
8243	float	_FFT_QL4_AVG_MAX[7]	VAr	
8245	float	_FFT_QL4_AVG_MAX[8]	VAr	
8247	float	_FFT_QL4_AVG_MAX[9]	VAr	
8249	float	_FFT_QL4_AVG_MAX[10]	VAr	
8251	float	_FFT_QL4_AVG_MAX[11]	VAr	
8253	float	_FFT_QL4_AVG_MAX[12]	VAr	
8255	float	_FFT_QL4_AVG_MAX[13]	VAr	
8257	float	_FFT_QL4_AVG_MAX[14]	VAr	
8259	float	_FFT_QL4_AVG_MAX[15]	VAr	
8261	float	_FFT_QL4_AVG_MAX[16]	VAr	

Address	Format	Designation	Unit	Remarks
8263	float	_FFT_QL4_AVG_MAX[17]	VAr	
8265	float	_FFT_QL4_AVG_MAX[18]	VAr	
8267	float	_FFT_QL4_AVG_MAX[19]	VAr	
8269	float	_FFT_QL4_AVG_MAX[20]	VAr	
8271	float	_FFT_QL4_AVG_MAX[21]	VAr	
8273	float	_FFT_QL4_AVG_MAX[22]	VAr	
8275	float	_FFT_QL4_AVG_MAX[23]	VAr	
8277	float	_FFT_QL4_AVG_MAX[24]	VAr	
8279	float	_FFT_QL4_AVG_MAX[25]	VAr	
8281	float	_FFT_QL4_AVG_MAX[26]	VAr	
8283	float	_FFT_QL4_AVG_MAX[27]	VAr	
8285	float	_FFT_QL4_AVG_MAX[28]	VAr	
8287	float	_FFT_QL4_AVG_MAX[29]	VAr	
8289	float	_FFT_QL4_AVG_MAX[30]	VAr	
8291	float	_FFT_QL4_AVG_MAX[31]	VAr	
8293	float	_FFT_QL4_AVG_MAX[32]	VAr	
8295	float	_FFT_QL4_AVG_MAX[33]	VAr	
8297	float	_FFT_QL4_AVG_MAX[34]	VAr	
8299	float	_FFT_QL4_AVG_MAX[35]	VAr	
8301	float	_FFT_QL4_AVG_MAX[36]	VAr	
8303	float	_FFT_QL4_AVG_MAX[37]	VAr	
8305	float	_FFT_QL4_AVG_MAX[38]	VAr	
8307	float	_FFT_QL4_AVG_MAX[39]	VAr	
8309	float	_THD_ULN_AVG_MAX[0]	%	
8311	float	_THD_ULN_AVG_MAX[1]	%	
8313	float	_THD_ULN_AVG_MAX[2]	%	
8315	float	_THD_ULN_AVG_MAX[3]	%	
8317	float	_THD_ILN_AVG_MAX[0]	%	
8319	float	_THD_ILN_AVG_MAX[1]	%	
8321	float	_THD_ILN_AVG_MAX[2]	%	
8323	float	_THD_ILN_AVG_MAX[3]	%	
8325	float	_KFACT_AVG_MAX[0]		
8327	float	_KFACT_AVG_MAX[1]		
8329	float	_KFACT_AVG_MAX[2]		
8331	float	_KFACT_AVG_MAX[3]		
8333	float	_ULN_AVG_MAX[0]	V	
8335	float	_ULN_AVG_MAX[1]	V	
8337	float	_ULN_AVG_MAX[2]	V	
8339	float	_ULN_AVG_MAX[3]	V	
8341	float	_ILN_AVG_MAX[0]	A	
8343	float	_ILN_AVG_MAX[1]	A	
8345	float	_ILN_AVG_MAX[2]	A	
8347	float	_ILN_AVG_MAX[3]	A	
8349	float	_PLN_AVG_MAX[0]	W	
8351	float	_PLN_AVG_MAX[1]	W	
8353	float	_PLN_AVG_MAX[2]	W	
8355	float	_PLN_AVG_MAX[3]	W	
8357	float	_QLN_AVG_MAX[0]	VAr	
8359	float	_QLN_AVG_MAX[1]	VAr	
8361	float	_QLN_AVG_MAX[2]	VAr	
8363	float	_QLN_AVG_MAX[3]	VAr	
8365	float	_SLN_AVG_MAX[0]	VA	
8367	float	_SLN_AVG_MAX[1]	VA	
8369	float	_SLN_AVG_MAX[2]	VA	
8371	float	_SLN_AVG_MAX[3]	VA	
8373	float	_ULL_AVG_MAX[0]	V	
8375	float	_ULL_AVG_MAX[1]	V	
8377	float	_ULL_AVG_MAX[2]	V	
8379	float	_I_SUM3_AVG_MAX	A	

Address	Format	Designation	Unit	Remarks
8381	float	_I_SUM_AVG_MAX	A	
8383	float	_S_SUM3_AVG_MAX	VA	
8385	float	_P_SUM3_AVG_MAX	W	
8387	float	_Q_SUM3_AVG_MAX	VAr	
8389	float	_S_SUM_AVG_MAX	VA	
8391	float	_P_SUM_AVG_MAX	W	
8393	float	_Q_SUM_AVG_MAX	VAr	
8395	float	_FREQ_AVG_MAX	Hz	
8397	float	_N_AVG_MAX	V	
8399	float	_M_AVG_MAX	V	
8401	float	_G_AVG_MAX	V	
8403	float	_SYM_AVG_MAX	%	
8405	float	_IN_AVG_MAX	A	
8407	float	_IM_AVG_MAX	A	
8409	float	_IG_AVG_MAX	A	
8411	float	_S0_POWER_AVG_MAX[0]	W	
8413	float	_S0_POWER_AVG_MAX[1]	W	
8415	float	_EXT_TEMPERATUR_AVG_MAX	°C	
8417	uint	_FFT_UL1_AVG_MAX_T[0]	s	
8419	uint	_FFT_UL1_AVG_MAX_T[1]	s	
8421	uint	_FFT_UL1_AVG_MAX_T[2]	s	
8423	uint	_FFT_UL1_AVG_MAX_T[3]	s	
8425	uint	_FFT_UL1_AVG_MAX_T[4]	s	
8427	uint	_FFT_UL1_AVG_MAX_T[5]	s	
8429	uint	_FFT_UL1_AVG_MAX_T[6]	s	
8431	uint	_FFT_UL1_AVG_MAX_T[7]	s	
8433	uint	_FFT_UL1_AVG_MAX_T[8]	s	
8435	uint	_FFT_UL1_AVG_MAX_T[9]	s	
8437	uint	_FFT_UL1_AVG_MAX_T[10]	s	
8439	uint	_FFT_UL1_AVG_MAX_T[11]	s	
8441	uint	_FFT_UL1_AVG_MAX_T[12]	s	
8443	uint	_FFT_UL1_AVG_MAX_T[13]	s	
8445	uint	_FFT_UL1_AVG_MAX_T[14]	s	
8447	uint	_FFT_UL1_AVG_MAX_T[15]	s	
8449	uint	_FFT_UL1_AVG_MAX_T[16]	s	
8451	uint	_FFT_UL1_AVG_MAX_T[17]	s	
8453	uint	_FFT_UL1_AVG_MAX_T[18]	s	
8455	uint	_FFT_UL1_AVG_MAX_T[19]	s	
8457	uint	_FFT_UL1_AVG_MAX_T[20]	s	
8459	uint	_FFT_UL1_AVG_MAX_T[21]	s	
8461	uint	_FFT_UL1_AVG_MAX_T[22]	s	
8463	uint	_FFT_UL1_AVG_MAX_T[23]	s	
8465	uint	_FFT_UL1_AVG_MAX_T[24]	s	
8467	uint	_FFT_UL1_AVG_MAX_T[25]	s	
8469	uint	_FFT_UL1_AVG_MAX_T[26]	s	
8471	uint	_FFT_UL1_AVG_MAX_T[27]	s	
8473	uint	_FFT_UL1_AVG_MAX_T[28]	s	
8475	uint	_FFT_UL1_AVG_MAX_T[29]	s	
8477	uint	_FFT_UL1_AVG_MAX_T[30]	s	
8479	uint	_FFT_UL1_AVG_MAX_T[31]	s	
8481	uint	_FFT_UL1_AVG_MAX_T[32]	s	
8483	uint	_FFT_UL1_AVG_MAX_T[33]	s	
8485	uint	_FFT_UL1_AVG_MAX_T[34]	s	
8487	uint	_FFT_UL1_AVG_MAX_T[35]	s	
8489	uint	_FFT_UL1_AVG_MAX_T[36]	s	
8491	uint	_FFT_UL1_AVG_MAX_T[37]	s	
8493	uint	_FFT_UL1_AVG_MAX_T[38]	s	
8495	uint	_FFT_UL1_AVG_MAX_T[39]	s	
8497	uint	_FFT_UL2_AVG_MAX_T[0]	s	

Address	Format	Designation	Unit	Remarks
8499	uint	_FFT_UL2_AVG_MAX_T[1]	s	
8501	uint	_FFT_UL2_AVG_MAX_T[2]	s	
8503	uint	_FFT_UL2_AVG_MAX_T[3]	s	
8505	uint	_FFT_UL2_AVG_MAX_T[4]	s	
8507	uint	_FFT_UL2_AVG_MAX_T[5]	s	
8509	uint	_FFT_UL2_AVG_MAX_T[6]	s	
8511	uint	_FFT_UL2_AVG_MAX_T[7]	s	
8513	uint	_FFT_UL2_AVG_MAX_T[8]	s	
8515	uint	_FFT_UL2_AVG_MAX_T[9]	s	
8517	uint	_FFT_UL2_AVG_MAX_T[10]	s	
8519	uint	_FFT_UL2_AVG_MAX_T[11]	s	
8521	uint	_FFT_UL2_AVG_MAX_T[12]	s	
8523	uint	_FFT_UL2_AVG_MAX_T[13]	s	
8525	uint	_FFT_UL2_AVG_MAX_T[14]	s	
8527	uint	_FFT_UL2_AVG_MAX_T[15]	s	
8529	uint	_FFT_UL2_AVG_MAX_T[16]	s	
8531	uint	_FFT_UL2_AVG_MAX_T[17]	s	
8533	uint	_FFT_UL2_AVG_MAX_T[18]	s	
8535	uint	_FFT_UL2_AVG_MAX_T[19]	s	
8537	uint	_FFT_UL2_AVG_MAX_T[20]	s	
8539	uint	_FFT_UL2_AVG_MAX_T[21]	s	
8541	uint	_FFT_UL2_AVG_MAX_T[22]	s	
8543	uint	_FFT_UL2_AVG_MAX_T[23]	s	
8545	uint	_FFT_UL2_AVG_MAX_T[24]	s	
8547	uint	_FFT_UL2_AVG_MAX_T[25]	s	
8549	uint	_FFT_UL2_AVG_MAX_T[26]	s	
8551	uint	_FFT_UL2_AVG_MAX_T[27]	s	
8553	uint	_FFT_UL2_AVG_MAX_T[28]	s	
8555	uint	_FFT_UL2_AVG_MAX_T[29]	s	
8557	uint	_FFT_UL2_AVG_MAX_T[30]	s	
8559	uint	_FFT_UL2_AVG_MAX_T[31]	s	
8561	uint	_FFT_UL2_AVG_MAX_T[32]	s	
8563	uint	_FFT_UL2_AVG_MAX_T[33]	s	
8565	uint	_FFT_UL2_AVG_MAX_T[34]	s	
8567	uint	_FFT_UL2_AVG_MAX_T[35]	s	
8569	uint	_FFT_UL2_AVG_MAX_T[36]	s	
8571	uint	_FFT_UL2_AVG_MAX_T[37]	s	
8573	uint	_FFT_UL2_AVG_MAX_T[38]	s	
8575	uint	_FFT_UL2_AVG_MAX_T[39]	s	
8577	uint	_FFT_UL3_AVG_MAX_T[0]	s	
8579	uint	_FFT_UL3_AVG_MAX_T[1]	s	
8581	uint	_FFT_UL3_AVG_MAX_T[2]	s	
8583	uint	_FFT_UL3_AVG_MAX_T[3]	s	
8585	uint	_FFT_UL3_AVG_MAX_T[4]	s	
8587	uint	_FFT_UL3_AVG_MAX_T[5]	s	
8589	uint	_FFT_UL3_AVG_MAX_T[6]	s	
8591	uint	_FFT_UL3_AVG_MAX_T[7]	s	
8593	uint	_FFT_UL3_AVG_MAX_T[8]	s	
8595	uint	_FFT_UL3_AVG_MAX_T[9]	s	
8597	uint	_FFT_UL3_AVG_MAX_T[10]	s	
8599	uint	_FFT_UL3_AVG_MAX_T[11]	s	
8601	uint	_FFT_UL3_AVG_MAX_T[12]	s	
8603	uint	_FFT_UL3_AVG_MAX_T[13]	s	
8605	uint	_FFT_UL3_AVG_MAX_T[14]	s	
8607	uint	_FFT_UL3_AVG_MAX_T[15]	s	
8609	uint	_FFT_UL3_AVG_MAX_T[16]	s	
8611	uint	_FFT_UL3_AVG_MAX_T[17]	s	
8613	uint	_FFT_UL3_AVG_MAX_T[18]	s	
8615	uint	_FFT_UL3_AVG_MAX_T[19]	s	

Address	Format	Designation	Unit	Remarks
8617	uint	_FFT_UL3_AVG_MAX_T[20]	s	
8619	uint	_FFT_UL3_AVG_MAX_T[21]	s	
8621	uint	_FFT_UL3_AVG_MAX_T[22]	s	
8623	uint	_FFT_UL3_AVG_MAX_T[23]	s	
8625	uint	_FFT_UL3_AVG_MAX_T[24]	s	
8627	uint	_FFT_UL3_AVG_MAX_T[25]	s	
8629	uint	_FFT_UL3_AVG_MAX_T[26]	s	
8631	uint	_FFT_UL3_AVG_MAX_T[27]	s	
8633	uint	_FFT_UL3_AVG_MAX_T[28]	s	
8635	uint	_FFT_UL3_AVG_MAX_T[29]	s	
8637	uint	_FFT_UL3_AVG_MAX_T[30]	s	
8639	uint	_FFT_UL3_AVG_MAX_T[31]	s	
8641	uint	_FFT_UL3_AVG_MAX_T[32]	s	
8643	uint	_FFT_UL3_AVG_MAX_T[33]	s	
8645	uint	_FFT_UL3_AVG_MAX_T[34]	s	
8647	uint	_FFT_UL3_AVG_MAX_T[35]	s	
8649	uint	_FFT_UL3_AVG_MAX_T[36]	s	
8651	uint	_FFT_UL3_AVG_MAX_T[37]	s	
8653	uint	_FFT_UL3_AVG_MAX_T[38]	s	
8655	uint	_FFT_UL3_AVG_MAX_T[39]	s	
8657	uint	_FFT_UL4_AVG_MAX_T[0]	s	
8659	uint	_FFT_UL4_AVG_MAX_T[1]	s	
8661	uint	_FFT_UL4_AVG_MAX_T[2]	s	
8663	uint	_FFT_UL4_AVG_MAX_T[3]	s	
8665	uint	_FFT_UL4_AVG_MAX_T[4]	s	
8667	uint	_FFT_UL4_AVG_MAX_T[5]	s	
8669	uint	_FFT_UL4_AVG_MAX_T[6]	s	
8671	uint	_FFT_UL4_AVG_MAX_T[7]	s	
8673	uint	_FFT_UL4_AVG_MAX_T[8]	s	
8675	uint	_FFT_UL4_AVG_MAX_T[9]	s	
8677	uint	_FFT_UL4_AVG_MAX_T[10]	s	
8679	uint	_FFT_UL4_AVG_MAX_T[11]	s	
8681	uint	_FFT_UL4_AVG_MAX_T[12]	s	
8683	uint	_FFT_UL4_AVG_MAX_T[13]	s	
8685	uint	_FFT_UL4_AVG_MAX_T[14]	s	
8687	uint	_FFT_UL4_AVG_MAX_T[15]	s	
8689	uint	_FFT_UL4_AVG_MAX_T[16]	s	
8691	uint	_FFT_UL4_AVG_MAX_T[17]	s	
8693	uint	_FFT_UL4_AVG_MAX_T[18]	s	
8695	uint	_FFT_UL4_AVG_MAX_T[19]	s	
8697	uint	_FFT_UL4_AVG_MAX_T[20]	s	
8699	uint	_FFT_UL4_AVG_MAX_T[21]	s	
8701	uint	_FFT_UL4_AVG_MAX_T[22]	s	
8703	uint	_FFT_UL4_AVG_MAX_T[23]	s	
8705	uint	_FFT_UL4_AVG_MAX_T[24]	s	
8707	uint	_FFT_UL4_AVG_MAX_T[25]	s	
8709	uint	_FFT_UL4_AVG_MAX_T[26]	s	
8711	uint	_FFT_UL4_AVG_MAX_T[27]	s	
8713	uint	_FFT_UL4_AVG_MAX_T[28]	s	
8715	uint	_FFT_UL4_AVG_MAX_T[29]	s	
8717	uint	_FFT_UL4_AVG_MAX_T[30]	s	
8719	uint	_FFT_UL4_AVG_MAX_T[31]	s	
8721	uint	_FFT_UL4_AVG_MAX_T[32]	s	
8723	uint	_FFT_UL4_AVG_MAX_T[33]	s	
8725	uint	_FFT_UL4_AVG_MAX_T[34]	s	
8727	uint	_FFT_UL4_AVG_MAX_T[35]	s	
8729	uint	_FFT_UL4_AVG_MAX_T[36]	s	
8731	uint	_FFT_UL4_AVG_MAX_T[37]	s	
8733	uint	_FFT_UL4_AVG_MAX_T[38]	s	

Address	Format	Designation	Unit	Remarks
8735	uint	_FFT_UL4_AVG_MAX_T[39]	s	
8737	uint	_FFT_IL1_AVG_MAX_T[0]	s	
8739	uint	_FFT_IL1_AVG_MAX_T[1]	s	
8741	uint	_FFT_IL1_AVG_MAX_T[2]	s	
8743	uint	_FFT_IL1_AVG_MAX_T[3]	s	
8745	uint	_FFT_IL1_AVG_MAX_T[4]	s	
8747	uint	_FFT_IL1_AVG_MAX_T[5]	s	
8749	uint	_FFT_IL1_AVG_MAX_T[6]	s	
8751	uint	_FFT_IL1_AVG_MAX_T[7]	s	
8753	uint	_FFT_IL1_AVG_MAX_T[8]	s	
8755	uint	_FFT_IL1_AVG_MAX_T[9]	s	
8757	uint	_FFT_IL1_AVG_MAX_T[10]	s	
8759	uint	_FFT_IL1_AVG_MAX_T[11]	s	
8761	uint	_FFT_IL1_AVG_MAX_T[12]	s	
8763	uint	_FFT_IL1_AVG_MAX_T[13]	s	
8765	uint	_FFT_IL1_AVG_MAX_T[14]	s	
8767	uint	_FFT_IL1_AVG_MAX_T[15]	s	
8769	uint	_FFT_IL1_AVG_MAX_T[16]	s	
8771	uint	_FFT_IL1_AVG_MAX_T[17]	s	
8773	uint	_FFT_IL1_AVG_MAX_T[18]	s	
8775	uint	_FFT_IL1_AVG_MAX_T[19]	s	
8777	uint	_FFT_IL1_AVG_MAX_T[20]	s	
8779	uint	_FFT_IL1_AVG_MAX_T[21]	s	
8781	uint	_FFT_IL1_AVG_MAX_T[22]	s	
8783	uint	_FFT_IL1_AVG_MAX_T[23]	s	
8785	uint	_FFT_IL1_AVG_MAX_T[24]	s	
8787	uint	_FFT_IL1_AVG_MAX_T[25]	s	
8789	uint	_FFT_IL1_AVG_MAX_T[26]	s	
8791	uint	_FFT_IL1_AVG_MAX_T[27]	s	
8793	uint	_FFT_IL1_AVG_MAX_T[28]	s	
8795	uint	_FFT_IL1_AVG_MAX_T[29]	s	
8797	uint	_FFT_IL1_AVG_MAX_T[30]	s	
8799	uint	_FFT_IL1_AVG_MAX_T[31]	s	
8801	uint	_FFT_IL1_AVG_MAX_T[32]	s	
8803	uint	_FFT_IL1_AVG_MAX_T[33]	s	
8805	uint	_FFT_IL1_AVG_MAX_T[34]	s	
8807	uint	_FFT_IL1_AVG_MAX_T[35]	s	
8809	uint	_FFT_IL1_AVG_MAX_T[36]	s	
8811	uint	_FFT_IL1_AVG_MAX_T[37]	s	
8813	uint	_FFT_IL1_AVG_MAX_T[38]	s	
8815	uint	_FFT_IL1_AVG_MAX_T[39]	s	
8817	uint	_FFT_IL2_AVG_MAX_T[0]	s	
8819	uint	_FFT_IL2_AVG_MAX_T[1]	s	
8821	uint	_FFT_IL2_AVG_MAX_T[2]	s	
8823	uint	_FFT_IL2_AVG_MAX_T[3]	s	
8825	uint	_FFT_IL2_AVG_MAX_T[4]	s	
8827	uint	_FFT_IL2_AVG_MAX_T[5]	s	
8829	uint	_FFT_IL2_AVG_MAX_T[6]	s	
8831	uint	_FFT_IL2_AVG_MAX_T[7]	s	
8833	uint	_FFT_IL2_AVG_MAX_T[8]	s	
8835	uint	_FFT_IL2_AVG_MAX_T[9]	s	
8837	uint	_FFT_IL2_AVG_MAX_T[10]	s	
8839	uint	_FFT_IL2_AVG_MAX_T[11]	s	
8841	uint	_FFT_IL2_AVG_MAX_T[12]	s	
8843	uint	_FFT_IL2_AVG_MAX_T[13]	s	
8845	uint	_FFT_IL2_AVG_MAX_T[14]	s	
8847	uint	_FFT_IL2_AVG_MAX_T[15]	s	
8849	uint	_FFT_IL2_AVG_MAX_T[16]	s	
8851	uint	_FFT_IL2_AVG_MAX_T[17]	s	



Address	Format	Designation	Unit	Remarks
8853	uint	_FFT_IL2_AVG_MAX_T[18]	s	
8855	uint	_FFT_IL2_AVG_MAX_T[19]	s	
8857	uint	_FFT_IL2_AVG_MAX_T[20]	s	
8859	uint	_FFT_IL2_AVG_MAX_T[21]	s	
8861	uint	_FFT_IL2_AVG_MAX_T[22]	s	
8863	uint	_FFT_IL2_AVG_MAX_T[23]	s	
8865	uint	_FFT_IL2_AVG_MAX_T[24]	s	
8867	uint	_FFT_IL2_AVG_MAX_T[25]	s	
8869	uint	_FFT_IL2_AVG_MAX_T[26]	s	
8871	uint	_FFT_IL2_AVG_MAX_T[27]	s	
8873	uint	_FFT_IL2_AVG_MAX_T[28]	s	
8875	uint	_FFT_IL2_AVG_MAX_T[29]	s	
8877	uint	_FFT_IL2_AVG_MAX_T[30]	s	
8879	uint	_FFT_IL2_AVG_MAX_T[31]	s	
8881	uint	_FFT_IL2_AVG_MAX_T[32]	s	
8883	uint	_FFT_IL2_AVG_MAX_T[33]	s	
8885	uint	_FFT_IL2_AVG_MAX_T[34]	s	
8887	uint	_FFT_IL2_AVG_MAX_T[35]	s	
8889	uint	_FFT_IL2_AVG_MAX_T[36]	s	
8891	uint	_FFT_IL2_AVG_MAX_T[37]	s	
8893	uint	_FFT_IL2_AVG_MAX_T[38]	s	
8895	uint	_FFT_IL2_AVG_MAX_T[39]	s	
8897	uint	_FFT_IL3_AVG_MAX_T[0]	s	
8899	uint	_FFT_IL3_AVG_MAX_T[1]	s	
8901	uint	_FFT_IL3_AVG_MAX_T[2]	s	
8903	uint	_FFT_IL3_AVG_MAX_T[3]	s	
8905	uint	_FFT_IL3_AVG_MAX_T[4]	s	
8907	uint	_FFT_IL3_AVG_MAX_T[5]	s	
8909	uint	_FFT_IL3_AVG_MAX_T[6]	s	
8911	uint	_FFT_IL3_AVG_MAX_T[7]	s	
8913	uint	_FFT_IL3_AVG_MAX_T[8]	s	
8915	uint	_FFT_IL3_AVG_MAX_T[9]	s	
8917	uint	_FFT_IL3_AVG_MAX_T[10]	s	
8919	uint	_FFT_IL3_AVG_MAX_T[11]	s	
8921	uint	_FFT_IL3_AVG_MAX_T[12]	s	
8923	uint	_FFT_IL3_AVG_MAX_T[13]	s	
8925	uint	_FFT_IL3_AVG_MAX_T[14]	s	
8927	uint	_FFT_IL3_AVG_MAX_T[15]	s	
8929	uint	_FFT_IL3_AVG_MAX_T[16]	s	
8931	uint	_FFT_IL3_AVG_MAX_T[17]	s	
8933	uint	_FFT_IL3_AVG_MAX_T[18]	s	
8935	uint	_FFT_IL3_AVG_MAX_T[19]	s	
8937	uint	_FFT_IL3_AVG_MAX_T[20]	s	
8939	uint	_FFT_IL3_AVG_MAX_T[21]	s	
8941	uint	_FFT_IL3_AVG_MAX_T[22]	s	
8943	uint	_FFT_IL3_AVG_MAX_T[23]	s	
8945	uint	_FFT_IL3_AVG_MAX_T[24]	s	
8947	uint	_FFT_IL3_AVG_MAX_T[25]	s	
8949	uint	_FFT_IL3_AVG_MAX_T[26]	s	
8951	uint	_FFT_IL3_AVG_MAX_T[27]	s	
8953	uint	_FFT_IL3_AVG_MAX_T[28]	s	
8955	uint	_FFT_IL3_AVG_MAX_T[29]	s	
8957	uint	_FFT_IL3_AVG_MAX_T[30]	s	
8959	uint	_FFT_IL3_AVG_MAX_T[31]	s	
8961	uint	_FFT_IL3_AVG_MAX_T[32]	s	
8963	uint	_FFT_IL3_AVG_MAX_T[33]	s	
8965	uint	_FFT_IL3_AVG_MAX_T[34]	s	
8967	uint	_FFT_IL3_AVG_MAX_T[35]	s	
8969	uint	_FFT_IL3_AVG_MAX_T[36]	s	

Address	Format	Designation	Unit	Remarks
8971	uint	_FFT_IL3_AVG_MAX_T[37]	s	
8973	uint	_FFT_IL3_AVG_MAX_T[38]	s	
8975	uint	_FFT_IL3_AVG_MAX_T[39]	s	
8977	uint	_FFT_IL4_AVG_MAX_T[0]	s	
8979	uint	_FFT_IL4_AVG_MAX_T[1]	s	
8981	uint	_FFT_IL4_AVG_MAX_T[2]	s	
8983	uint	_FFT_IL4_AVG_MAX_T[3]	s	
8985	uint	_FFT_IL4_AVG_MAX_T[4]	s	
8987	uint	_FFT_IL4_AVG_MAX_T[5]	s	
8989	uint	_FFT_IL4_AVG_MAX_T[6]	s	
8991	uint	_FFT_IL4_AVG_MAX_T[7]	s	
8993	uint	_FFT_IL4_AVG_MAX_T[8]	s	
8995	uint	_FFT_IL4_AVG_MAX_T[9]	s	
8997	uint	_FFT_IL4_AVG_MAX_T[10]	s	
8999	uint	_FFT_IL4_AVG_MAX_T[11]	s	
9001	uint	_FFT_IL4_AVG_MAX_T[12]	s	
9003	uint	_FFT_IL4_AVG_MAX_T[13]	s	
9005	uint	_FFT_IL4_AVG_MAX_T[14]	s	
9007	uint	_FFT_IL4_AVG_MAX_T[15]	s	
9009	uint	_FFT_IL4_AVG_MAX_T[16]	s	
9011	uint	_FFT_IL4_AVG_MAX_T[17]	s	
9013	uint	_FFT_IL4_AVG_MAX_T[18]	s	
9015	uint	_FFT_IL4_AVG_MAX_T[19]	s	
9017	uint	_FFT_IL4_AVG_MAX_T[20]	s	
9019	uint	_FFT_IL4_AVG_MAX_T[21]	s	
9021	uint	_FFT_IL4_AVG_MAX_T[22]	s	
9023	uint	_FFT_IL4_AVG_MAX_T[23]	s	
9025	uint	_FFT_IL4_AVG_MAX_T[24]	s	
9027	uint	_FFT_IL4_AVG_MAX_T[25]	s	
9029	uint	_FFT_IL4_AVG_MAX_T[26]	s	
9031	uint	_FFT_IL4_AVG_MAX_T[27]	s	
9033	uint	_FFT_IL4_AVG_MAX_T[28]	s	
9035	uint	_FFT_IL4_AVG_MAX_T[29]	s	
9037	uint	_FFT_IL4_AVG_MAX_T[30]	s	
9039	uint	_FFT_IL4_AVG_MAX_T[31]	s	
9041	uint	_FFT_IL4_AVG_MAX_T[32]	s	
9043	uint	_FFT_IL4_AVG_MAX_T[33]	s	
9045	uint	_FFT_IL4_AVG_MAX_T[34]	s	
9047	uint	_FFT_IL4_AVG_MAX_T[35]	s	
9049	uint	_FFT_IL4_AVG_MAX_T[36]	s	
9051	uint	_FFT_IL4_AVG_MAX_T[37]	s	
9053	uint	_FFT_IL4_AVG_MAX_T[38]	s	
9055	uint	_FFT_IL4_AVG_MAX_T[39]	s	
9057	uint	_FFT_PL1_AVG_MAX_T[0]	s	
9059	uint	_FFT_PL1_AVG_MAX_T[1]	s	
9061	uint	_FFT_PL1_AVG_MAX_T[2]	s	
9063	uint	_FFT_PL1_AVG_MAX_T[3]	s	
9065	uint	_FFT_PL1_AVG_MAX_T[4]	s	
9067	uint	_FFT_PL1_AVG_MAX_T[5]	s	
9069	uint	_FFT_PL1_AVG_MAX_T[6]	s	
9071	uint	_FFT_PL1_AVG_MAX_T[7]	s	
9073	uint	_FFT_PL1_AVG_MAX_T[8]	s	
9075	uint	_FFT_PL1_AVG_MAX_T[9]	s	
9077	uint	_FFT_PL1_AVG_MAX_T[10]	s	
9079	uint	_FFT_PL1_AVG_MAX_T[11]	s	
9081	uint	_FFT_PL1_AVG_MAX_T[12]	s	
9083	uint	_FFT_PL1_AVG_MAX_T[13]	s	
9085	uint	_FFT_PL1_AVG_MAX_T[14]	s	
9087	uint	_FFT_PL1_AVG_MAX_T[15]	s	



Address	Format	Designation	Unit	Remarks
9089	uint	_FFT_PL1_AVG_MAX_T[16]	s	
9091	uint	_FFT_PL1_AVG_MAX_T[17]	s	
9093	uint	_FFT_PL1_AVG_MAX_T[18]	s	
9095	uint	_FFT_PL1_AVG_MAX_T[19]	s	
9097	uint	_FFT_PL1_AVG_MAX_T[20]	s	
9099	uint	_FFT_PL1_AVG_MAX_T[21]	s	
9101	uint	_FFT_PL1_AVG_MAX_T[22]	s	
9103	uint	_FFT_PL1_AVG_MAX_T[23]	s	
9105	uint	_FFT_PL1_AVG_MAX_T[24]	s	
9107	uint	_FFT_PL1_AVG_MAX_T[25]	s	
9109	uint	_FFT_PL1_AVG_MAX_T[26]	s	
9111	uint	_FFT_PL1_AVG_MAX_T[27]	s	
9113	uint	_FFT_PL1_AVG_MAX_T[28]	s	
9115	uint	_FFT_PL1_AVG_MAX_T[29]	s	
9117	uint	_FFT_PL1_AVG_MAX_T[30]	s	
9119	uint	_FFT_PL1_AVG_MAX_T[31]	s	
9121	uint	_FFT_PL1_AVG_MAX_T[32]	s	
9123	uint	_FFT_PL1_AVG_MAX_T[33]	s	
9125	uint	_FFT_PL1_AVG_MAX_T[34]	s	
9127	uint	_FFT_PL1_AVG_MAX_T[35]	s	
9129	uint	_FFT_PL1_AVG_MAX_T[36]	s	
9131	uint	_FFT_PL1_AVG_MAX_T[37]	s	
9133	uint	_FFT_PL1_AVG_MAX_T[38]	s	
9135	uint	_FFT_PL1_AVG_MAX_T[39]	s	
9137	uint	_FFT_PL2_AVG_MAX_T[0]	s	
9139	uint	_FFT_PL2_AVG_MAX_T[1]	s	
9141	uint	_FFT_PL2_AVG_MAX_T[2]	s	
9143	uint	_FFT_PL2_AVG_MAX_T[3]	s	
9145	uint	_FFT_PL2_AVG_MAX_T[4]	s	
9147	uint	_FFT_PL2_AVG_MAX_T[5]	s	
9149	uint	_FFT_PL2_AVG_MAX_T[6]	s	
9151	uint	_FFT_PL2_AVG_MAX_T[7]	s	
9153	uint	_FFT_PL2_AVG_MAX_T[8]	s	
9155	uint	_FFT_PL2_AVG_MAX_T[9]	s	
9157	uint	_FFT_PL2_AVG_MAX_T[10]	s	
9159	uint	_FFT_PL2_AVG_MAX_T[11]	s	
9161	uint	_FFT_PL2_AVG_MAX_T[12]	s	
9163	uint	_FFT_PL2_AVG_MAX_T[13]	s	
9165	uint	_FFT_PL2_AVG_MAX_T[14]	s	
9167	uint	_FFT_PL2_AVG_MAX_T[15]	s	
9169	uint	_FFT_PL2_AVG_MAX_T[16]	s	
9171	uint	_FFT_PL2_AVG_MAX_T[17]	s	
9173	uint	_FFT_PL2_AVG_MAX_T[18]	s	
9175	uint	_FFT_PL2_AVG_MAX_T[19]	s	
9177	uint	_FFT_PL2_AVG_MAX_T[20]	s	
9179	uint	_FFT_PL2_AVG_MAX_T[21]	s	
9181	uint	_FFT_PL2_AVG_MAX_T[22]	s	
9183	uint	_FFT_PL2_AVG_MAX_T[23]	s	
9185	uint	_FFT_PL2_AVG_MAX_T[24]	s	
9187	uint	_FFT_PL2_AVG_MAX_T[25]	s	
9189	uint	_FFT_PL2_AVG_MAX_T[26]	s	
9191	uint	_FFT_PL2_AVG_MAX_T[27]	s	
9193	uint	_FFT_PL2_AVG_MAX_T[28]	s	
9195	uint	_FFT_PL2_AVG_MAX_T[29]	s	
9197	uint	_FFT_PL2_AVG_MAX_T[30]	s	
9199	uint	_FFT_PL2_AVG_MAX_T[31]	s	
9201	uint	_FFT_PL2_AVG_MAX_T[32]	s	
9203	uint	_FFT_PL2_AVG_MAX_T[33]	s	
9205	uint	_FFT_PL2_AVG_MAX_T[34]	s	

Address	Format	Designation	Unit	Remarks
9207	uint	_FFT_PL2_AVG_MAX_T[35]	s	
9209	uint	_FFT_PL2_AVG_MAX_T[36]	s	
9211	uint	_FFT_PL2_AVG_MAX_T[37]	s	
9213	uint	_FFT_PL2_AVG_MAX_T[38]	s	
9215	uint	_FFT_PL2_AVG_MAX_T[39]	s	
9217	uint	_FFT_PL3_AVG_MAX_T[0]	s	
9219	uint	_FFT_PL3_AVG_MAX_T[1]	s	
9221	uint	_FFT_PL3_AVG_MAX_T[2]	s	
9223	uint	_FFT_PL3_AVG_MAX_T[3]	s	
9225	uint	_FFT_PL3_AVG_MAX_T[4]	s	
9227	uint	_FFT_PL3_AVG_MAX_T[5]	s	
9229	uint	_FFT_PL3_AVG_MAX_T[6]	s	
9231	uint	_FFT_PL3_AVG_MAX_T[7]	s	
9233	uint	_FFT_PL3_AVG_MAX_T[8]	s	
9235	uint	_FFT_PL3_AVG_MAX_T[9]	s	
9237	uint	_FFT_PL3_AVG_MAX_T[10]	s	
9239	uint	_FFT_PL3_AVG_MAX_T[11]	s	
9241	uint	_FFT_PL3_AVG_MAX_T[12]	s	
9243	uint	_FFT_PL3_AVG_MAX_T[13]	s	
9245	uint	_FFT_PL3_AVG_MAX_T[14]	s	
9247	uint	_FFT_PL3_AVG_MAX_T[15]	s	
9249	uint	_FFT_PL3_AVG_MAX_T[16]	s	
9251	uint	_FFT_PL3_AVG_MAX_T[17]	s	
9253	uint	_FFT_PL3_AVG_MAX_T[18]	s	
9255	uint	_FFT_PL3_AVG_MAX_T[19]	s	
9257	uint	_FFT_PL3_AVG_MAX_T[20]	s	
9259	uint	_FFT_PL3_AVG_MAX_T[21]	s	
9261	uint	_FFT_PL3_AVG_MAX_T[22]	s	
9263	uint	_FFT_PL3_AVG_MAX_T[23]	s	
9265	uint	_FFT_PL3_AVG_MAX_T[24]	s	
9267	uint	_FFT_PL3_AVG_MAX_T[25]	s	
9269	uint	_FFT_PL3_AVG_MAX_T[26]	s	
9271	uint	_FFT_PL3_AVG_MAX_T[27]	s	
9273	uint	_FFT_PL3_AVG_MAX_T[28]	s	
9275	uint	_FFT_PL3_AVG_MAX_T[29]	s	
9277	uint	_FFT_PL3_AVG_MAX_T[30]	s	
9279	uint	_FFT_PL3_AVG_MAX_T[31]	s	
9281	uint	_FFT_PL3_AVG_MAX_T[32]	s	
9283	uint	_FFT_PL3_AVG_MAX_T[33]	s	
9285	uint	_FFT_PL3_AVG_MAX_T[34]	s	
9287	uint	_FFT_PL3_AVG_MAX_T[35]	s	
9289	uint	_FFT_PL3_AVG_MAX_T[36]	s	
9291	uint	_FFT_PL3_AVG_MAX_T[37]	s	
9293	uint	_FFT_PL3_AVG_MAX_T[38]	s	
9295	uint	_FFT_PL3_AVG_MAX_T[39]	s	
9297	uint	_FFT_PL4_AVG_MAX_T[0]	s	
9299	uint	_FFT_PL4_AVG_MAX_T[1]	s	
9301	uint	_FFT_PL4_AVG_MAX_T[2]	s	
9303	uint	_FFT_PL4_AVG_MAX_T[3]	s	
9305	uint	_FFT_PL4_AVG_MAX_T[4]	s	
9307	uint	_FFT_PL4_AVG_MAX_T[5]	s	
9309	uint	_FFT_PL4_AVG_MAX_T[6]	s	
9311	uint	_FFT_PL4_AVG_MAX_T[7]	s	
9313	uint	_FFT_PL4_AVG_MAX_T[8]	s	
9315	uint	_FFT_PL4_AVG_MAX_T[9]	s	
9317	uint	_FFT_PL4_AVG_MAX_T[10]	s	
9319	uint	_FFT_PL4_AVG_MAX_T[11]	s	
9321	uint	_FFT_PL4_AVG_MAX_T[12]	s	
9323	uint	_FFT_PL4_AVG_MAX_T[13]	s	

Address	Format	Designation	Unit	Remarks
9325	uint	_FFT_PL4_AVG_MAX_T[14]	s	
9327	uint	_FFT_PL4_AVG_MAX_T[15]	s	
9329	uint	_FFT_PL4_AVG_MAX_T[16]	s	
9331	uint	_FFT_PL4_AVG_MAX_T[17]	s	
9333	uint	_FFT_PL4_AVG_MAX_T[18]	s	
9335	uint	_FFT_PL4_AVG_MAX_T[19]	s	
9337	uint	_FFT_PL4_AVG_MAX_T[20]	s	
9339	uint	_FFT_PL4_AVG_MAX_T[21]	s	
9341	uint	_FFT_PL4_AVG_MAX_T[22]	s	
9343	uint	_FFT_PL4_AVG_MAX_T[23]	s	
9345	uint	_FFT_PL4_AVG_MAX_T[24]	s	
9347	uint	_FFT_PL4_AVG_MAX_T[25]	s	
9349	uint	_FFT_PL4_AVG_MAX_T[26]	s	
9351	uint	_FFT_PL4_AVG_MAX_T[27]	s	
9353	uint	_FFT_PL4_AVG_MAX_T[28]	s	
9355	uint	_FFT_PL4_AVG_MAX_T[29]	s	
9357	uint	_FFT_PL4_AVG_MAX_T[30]	s	
9359	uint	_FFT_PL4_AVG_MAX_T[31]	s	
9361	uint	_FFT_PL4_AVG_MAX_T[32]	s	
9363	uint	_FFT_PL4_AVG_MAX_T[33]	s	
9365	uint	_FFT_PL4_AVG_MAX_T[34]	s	
9367	uint	_FFT_PL4_AVG_MAX_T[35]	s	
9369	uint	_FFT_PL4_AVG_MAX_T[36]	s	
9371	uint	_FFT_PL4_AVG_MAX_T[37]	s	
9373	uint	_FFT_PL4_AVG_MAX_T[38]	s	
9375	uint	_FFT_PL4_AVG_MAX_T[39]	s	
9377	uint	_FFT_QL1_AVG_MAX_T[0]	s	
9379	uint	_FFT_QL1_AVG_MAX_T[1]	s	
9381	uint	_FFT_QL1_AVG_MAX_T[2]	s	
9383	uint	_FFT_QL1_AVG_MAX_T[3]	s	
9385	uint	_FFT_QL1_AVG_MAX_T[4]	s	
9387	uint	_FFT_QL1_AVG_MAX_T[5]	s	
9389	uint	_FFT_QL1_AVG_MAX_T[6]	s	
9391	uint	_FFT_QL1_AVG_MAX_T[7]	s	
9393	uint	_FFT_QL1_AVG_MAX_T[8]	s	
9395	uint	_FFT_QL1_AVG_MAX_T[9]	s	
9397	uint	_FFT_QL1_AVG_MAX_T[10]	s	
9399	uint	_FFT_QL1_AVG_MAX_T[11]	s	
9401	uint	_FFT_QL1_AVG_MAX_T[12]	s	
9403	uint	_FFT_QL1_AVG_MAX_T[13]	s	
9405	uint	_FFT_QL1_AVG_MAX_T[14]	s	
9407	uint	_FFT_QL1_AVG_MAX_T[15]	s	
9409	uint	_FFT_QL1_AVG_MAX_T[16]	s	
9411	uint	_FFT_QL1_AVG_MAX_T[17]	s	
9413	uint	_FFT_QL1_AVG_MAX_T[18]	s	
9415	uint	_FFT_QL1_AVG_MAX_T[19]	s	
9417	uint	_FFT_QL1_AVG_MAX_T[20]	s	
9419	uint	_FFT_QL1_AVG_MAX_T[21]	s	
9421	uint	_FFT_QL1_AVG_MAX_T[22]	s	
9423	uint	_FFT_QL1_AVG_MAX_T[23]	s	
9425	uint	_FFT_QL1_AVG_MAX_T[24]	s	
9427	uint	_FFT_QL1_AVG_MAX_T[25]	s	
9429	uint	_FFT_QL1_AVG_MAX_T[26]	s	
9431	uint	_FFT_QL1_AVG_MAX_T[27]	s	
9433	uint	_FFT_QL1_AVG_MAX_T[28]	s	
9435	uint	_FFT_QL1_AVG_MAX_T[29]	s	
9437	uint	_FFT_QL1_AVG_MAX_T[30]	s	
9439	uint	_FFT_QL1_AVG_MAX_T[31]	s	
9441	uint	_FFT_QL1_AVG_MAX_T[32]	s	

Address	Format	Designation	Unit	Remarks
9443	uint	_FFT_QL1_AVG_MAX_T[33]	s	
9445	uint	_FFT_QL1_AVG_MAX_T[34]	s	
9447	uint	_FFT_QL1_AVG_MAX_T[35]	s	
9449	uint	_FFT_QL1_AVG_MAX_T[36]	s	
9451	uint	_FFT_QL1_AVG_MAX_T[37]	s	
9453	uint	_FFT_QL1_AVG_MAX_T[38]	s	
9455	uint	_FFT_QL1_AVG_MAX_T[39]	s	
9457	uint	_FFT_QL2_AVG_MAX_T[0]	s	
9459	uint	_FFT_QL2_AVG_MAX_T[1]	s	
9461	uint	_FFT_QL2_AVG_MAX_T[2]	s	
9463	uint	_FFT_QL2_AVG_MAX_T[3]	s	
9465	uint	_FFT_QL2_AVG_MAX_T[4]	s	
9467	uint	_FFT_QL2_AVG_MAX_T[5]	s	
9469	uint	_FFT_QL2_AVG_MAX_T[6]	s	
9471	uint	_FFT_QL2_AVG_MAX_T[7]	s	
9473	uint	_FFT_QL2_AVG_MAX_T[8]	s	
9475	uint	_FFT_QL2_AVG_MAX_T[9]	s	
9477	uint	_FFT_QL2_AVG_MAX_T[10]	s	
9479	uint	_FFT_QL2_AVG_MAX_T[11]	s	
9481	uint	_FFT_QL2_AVG_MAX_T[12]	s	
9483	uint	_FFT_QL2_AVG_MAX_T[13]	s	
9485	uint	_FFT_QL2_AVG_MAX_T[14]	s	
9487	uint	_FFT_QL2_AVG_MAX_T[15]	s	
9489	uint	_FFT_QL2_AVG_MAX_T[16]	s	
9491	uint	_FFT_QL2_AVG_MAX_T[17]	s	
9493	uint	_FFT_QL2_AVG_MAX_T[18]	s	
9495	uint	_FFT_QL2_AVG_MAX_T[19]	s	
9497	uint	_FFT_QL2_AVG_MAX_T[20]	s	
9499	uint	_FFT_QL2_AVG_MAX_T[21]	s	
9501	uint	_FFT_QL2_AVG_MAX_T[22]	s	
9503	uint	_FFT_QL2_AVG_MAX_T[23]	s	
9505	uint	_FFT_QL2_AVG_MAX_T[24]	s	
9507	uint	_FFT_QL2_AVG_MAX_T[25]	s	
9509	uint	_FFT_QL2_AVG_MAX_T[26]	s	
9511	uint	_FFT_QL2_AVG_MAX_T[27]	s	
9513	uint	_FFT_QL2_AVG_MAX_T[28]	s	
9515	uint	_FFT_QL2_AVG_MAX_T[29]	s	
9517	uint	_FFT_QL2_AVG_MAX_T[30]	s	
9519	uint	_FFT_QL2_AVG_MAX_T[31]	s	
9521	uint	_FFT_QL2_AVG_MAX_T[32]	s	
9523	uint	_FFT_QL2_AVG_MAX_T[33]	s	
9525	uint	_FFT_QL2_AVG_MAX_T[34]	s	
9527	uint	_FFT_QL2_AVG_MAX_T[35]	s	
9529	uint	_FFT_QL2_AVG_MAX_T[36]	s	
9531	uint	_FFT_QL2_AVG_MAX_T[37]	s	
9533	uint	_FFT_QL2_AVG_MAX_T[38]	s	
9535	uint	_FFT_QL2_AVG_MAX_T[39]	s	
9537	uint	_FFT_QL3_AVG_MAX_T[0]	s	
9539	uint	_FFT_QL3_AVG_MAX_T[1]	s	
9541	uint	_FFT_QL3_AVG_MAX_T[2]	s	
9543	uint	_FFT_QL3_AVG_MAX_T[3]	s	
9545	uint	_FFT_QL3_AVG_MAX_T[4]	s	
9547	uint	_FFT_QL3_AVG_MAX_T[5]	s	
9549	uint	_FFT_QL3_AVG_MAX_T[6]	s	
9551	uint	_FFT_QL3_AVG_MAX_T[7]	s	
9553	uint	_FFT_QL3_AVG_MAX_T[8]	s	
9555	uint	_FFT_QL3_AVG_MAX_T[9]	s	
9557	uint	_FFT_QL3_AVG_MAX_T[10]	s	
9559	uint	_FFT_QL3_AVG_MAX_T[11]	s	

Address	Format	Designation	Unit	Remarks
9561	uint	_FFT_QL3_AVG_MAX_T[12]	s	
9563	uint	_FFT_QL3_AVG_MAX_T[13]	s	
9565	uint	_FFT_QL3_AVG_MAX_T[14]	s	
9567	uint	_FFT_QL3_AVG_MAX_T[15]	s	
9569	uint	_FFT_QL3_AVG_MAX_T[16]	s	
9571	uint	_FFT_QL3_AVG_MAX_T[17]	s	
9573	uint	_FFT_QL3_AVG_MAX_T[18]	s	
9575	uint	_FFT_QL3_AVG_MAX_T[19]	s	
9577	uint	_FFT_QL3_AVG_MAX_T[20]	s	
9579	uint	_FFT_QL3_AVG_MAX_T[21]	s	
9581	uint	_FFT_QL3_AVG_MAX_T[22]	s	
9583	uint	_FFT_QL3_AVG_MAX_T[23]	s	
9585	uint	_FFT_QL3_AVG_MAX_T[24]	s	
9587	uint	_FFT_QL3_AVG_MAX_T[25]	s	
9589	uint	_FFT_QL3_AVG_MAX_T[26]	s	
9591	uint	_FFT_QL3_AVG_MAX_T[27]	s	
9593	uint	_FFT_QL3_AVG_MAX_T[28]	s	
9595	uint	_FFT_QL3_AVG_MAX_T[29]	s	
9597	uint	_FFT_QL3_AVG_MAX_T[30]	s	
9599	uint	_FFT_QL3_AVG_MAX_T[31]	s	
9601	uint	_FFT_QL3_AVG_MAX_T[32]	s	
9603	uint	_FFT_QL3_AVG_MAX_T[33]	s	
9605	uint	_FFT_QL3_AVG_MAX_T[34]	s	
9607	uint	_FFT_QL3_AVG_MAX_T[35]	s	
9609	uint	_FFT_QL3_AVG_MAX_T[36]	s	
9611	uint	_FFT_QL3_AVG_MAX_T[37]	s	
9613	uint	_FFT_QL3_AVG_MAX_T[38]	s	
9615	uint	_FFT_QL3_AVG_MAX_T[39]	s	
9617	uint	_FFT_QL4_AVG_MAX_T[0]	s	
9619	uint	_FFT_QL4_AVG_MAX_T[1]	s	
9621	uint	_FFT_QL4_AVG_MAX_T[2]	s	
9623	uint	_FFT_QL4_AVG_MAX_T[3]	s	
9625	uint	_FFT_QL4_AVG_MAX_T[4]	s	
9627	uint	_FFT_QL4_AVG_MAX_T[5]	s	
9629	uint	_FFT_QL4_AVG_MAX_T[6]	s	
9631	uint	_FFT_QL4_AVG_MAX_T[7]	s	
9633	uint	_FFT_QL4_AVG_MAX_T[8]	s	
9635	uint	_FFT_QL4_AVG_MAX_T[9]	s	
9637	uint	_FFT_QL4_AVG_MAX_T[10]	s	
9639	uint	_FFT_QL4_AVG_MAX_T[11]	s	
9641	uint	_FFT_QL4_AVG_MAX_T[12]	s	
9643	uint	_FFT_QL4_AVG_MAX_T[13]	s	
9645	uint	_FFT_QL4_AVG_MAX_T[14]	s	
9647	uint	_FFT_QL4_AVG_MAX_T[15]	s	
9649	uint	_FFT_QL4_AVG_MAX_T[16]	s	
9651	uint	_FFT_QL4_AVG_MAX_T[17]	s	
9653	uint	_FFT_QL4_AVG_MAX_T[18]	s	
9655	uint	_FFT_QL4_AVG_MAX_T[19]	s	
9657	uint	_FFT_QL4_AVG_MAX_T[20]	s	
9659	uint	_FFT_QL4_AVG_MAX_T[21]	s	
9661	uint	_FFT_QL4_AVG_MAX_T[22]	s	
9663	uint	_FFT_QL4_AVG_MAX_T[23]	s	
9665	uint	_FFT_QL4_AVG_MAX_T[24]	s	
9667	uint	_FFT_QL4_AVG_MAX_T[25]	s	
9669	uint	_FFT_QL4_AVG_MAX_T[26]	s	
9671	uint	_FFT_QL4_AVG_MAX_T[27]	s	
9673	uint	_FFT_QL4_AVG_MAX_T[28]	s	
9675	uint	_FFT_QL4_AVG_MAX_T[29]	s	
9677	uint	_FFT_QL4_AVG_MAX_T[30]	s	

Address	Format	Designation	Unit	Remarks
9679	uint	_FFT_QL4_AVG_MAX_T[31]	s	
9681	uint	_FFT_QL4_AVG_MAX_T[32]	s	
9683	uint	_FFT_QL4_AVG_MAX_T[33]	s	
9685	uint	_FFT_QL4_AVG_MAX_T[34]	s	
9687	uint	_FFT_QL4_AVG_MAX_T[35]	s	
9689	uint	_FFT_QL4_AVG_MAX_T[36]	s	
9691	uint	_FFT_QL4_AVG_MAX_T[37]	s	
9693	uint	_FFT_QL4_AVG_MAX_T[38]	s	
9695	uint	_FFT_QL4_AVG_MAX_T[39]	s	
9697	uint	_THD_ULN_AVG_MAX_T[0]	s	THD, Mean Value, Maximum, UL1
9699	uint	_THD_ULN_AVG_MAX_T[1]	s	THD, Mean Value, Maximum, UL2
9701	uint	_THD_ULN_AVG_MAX_T[2]	s	THD, Mean Value, Maximum, UL3
9703	uint	_THD_ULN_AVG_MAX_T[3]	s	THD, Mean Value, Maximum, UL4
9705	uint	_THD_ILN_AVG_MAX_T[0]	s	THD, Mean Value, Maximum, IL1
9707	uint	_THD_ILN_AVG_MAX_T[1]	s	THD, Mean Value, Maximum, IL2
9709	uint	_THD_ILN_AVG_MAX_T[2]	s	THD, Mean Value, Maximum, IL3
9711	uint	_THD_ILN_AVG_MAX_T[3]	s	THD, Mean Value, Maximum, IL4
9713	uint	_KFACT_AVG_MAX_T[0]	s	K-Factor, Mean Value, Maximum, L1
9715	uint	_KFACT_AVG_MAX_T[1]	s	K-Factor, Mean Value, Maximum, L2
9717	uint	_KFACT_AVG_MAX_T[2]	s	K-Factor, Mean Value, Maximum, L3
9719	uint	_KFACT_AVG_MAX_T[3]	s	K-Factor, Mean Value, Maximum, L4
9721	uint	_ULN_AVG_MAX_T[0]	s	Voltage, Mean Value, Maximum, L1-N
9723	uint	_ULN_AVG_MAX_T[1]	s	Voltage, Mean Value, Maximum, L2-N
9725	uint	_ULN_AVG_MAX_T[2]	s	Voltage, Mean Value, Maximum, L3-N
9727	uint	_ULN_AVG_MAX_T[3]	s	Voltage, Mean Value, Maximum, L4-N
9729	uint	_ILN_AVG_MAX_T[0]	s	Current, Mean Value, Maximum, L1-N
9731	uint	_ILN_AVG_MAX_T[1]	s	Current, Mean Value, Maximum, L2-N
9733	uint	_ILN_AVG_MAX_T[2]	s	Current, Mean Value, Maximum, L3-N
9735	uint	_ILN_AVG_MAX_T[3]	s	Current, Mean Value, Maximum, L4-N
9737	uint	_PLN_AVG_MAX_T[0]	s	Real Power, Mean Value, Maximum, L1
9739	uint	_PLN_AVG_MAX_T[1]	s	Real Power, Mean Value, Maximum, L2
9741	uint	_PLN_AVG_MAX_T[2]	s	Real Power, Mean Value, Maximum, L3
9743	uint	_PLN_AVG_MAX_T[3]	s	Real Power, Mean Value, Maximum, L4
9745	uint	_QLN_AVG_MAX_T[0]	s	Reactive Power, Mean Value, Max., L1
9747	uint	_QLN_AVG_MAX_T[1]	s	Reactive Power, Mean Value, Max., L2
9749	uint	_QLN_AVG_MAX_T[2]	s	Reactive Power, Mean Value, Max., L3
9751	uint	_QLN_AVG_MAX_T[3]	s	Reactive Power, Mean Value, Max., L4
9753	uint	_SLN_AVG_MAX_T[0]	s	Apparent Power, Mean Value, Max., L1
9755	uint	_SLN_AVG_MAX_T[1]	s	Apparent Power, Mean Value, Max., L2
9757	uint	_SLN_AVG_MAX_T[2]	s	Apparent Power, Mean Value, Max., L3
9759	uint	_SLN_AVG_MAX_T[3]	s	Apparent Power, Mean Value, Max., L4
9761	uint	_ULL_AVG_MAX_T[0]	s	Voltage, Mean Value, Maximum, L1-L2
9763	uint	_ULL_AVG_MAX_T[1]	s	Voltage, Mean Value, Maximum, L2-L3
9765	uint	_ULL_AVG_MAX_T[2]	s	Voltage, Mean Value, Maximum, L3-L1
9767	uint	_I_SUM3_AVG_MAX_T	s	Sum, Mean Value, Max., IL1+IL2+IL3
9769	uint	_I_SUM_AVG_MAX_T	s	Sum., Mean Val., Max., IL1+IL2+IL3+IL4
9771	uint	_S_SUM3_AVG_MAX_T	s	Sum, Mean Value, Max., SL1+SL2+SL3
9773	uint	_P_SUM3_AVG_MAX_T	s	Sum, Mean Value, Max., PL1+PL2+PL3
9775	uint	_Q_SUM3_AVG_MAX_T	s	Sum, Mittelw., Max., QL1+QL2+QL3
9777	uint	_S_SUM_AVG_MAX_T	s	Sum, Mean Value, Max, SL1+SL2+SL3+SL4
9779	uint	_P_SUM_AVG_MAX_T	s	Sum, Mean Value, Max., PL1+PL2+PL3+PL4
9781	uint	_Q_SUM_AVG_MAX_T	s	Sum, Mean Value, Max. QL1+QL2+QL3+QL4
9783	uint	_FREQ_AVG_MAX_T	s	Frequency, Mean Value, Max
9785	uint	_N_AVG_MAX_T	s	Zero Sequence System, Voltage, Mean Value, Max.
9787	uint	_M_AVG_MAX_T	s	Positive Sequence System, Voltage,



Address	Format	Designation	Unit	Remarks
				Mean Value, Max.
9789	uint	_G_AVG_MAX_T	s	Negative Sequence System, Voltage
9791	uint	_SYM_AVG_MAX_T	s	Unbalance, Voltage
9793	uint	_IN_AVG_MAX_T	s	Zero Sequence System, Current, Mean Value, Max.
9795	uint	_IM_AVG_MAX_T	s	Positive Sequence System, Current, Mean Value, Max.
9797	uint	_IG_AVG_MAX_T	s	Negative Sequence System, Current, Mean value, Max.
9799	uint	_S0_POWER_AVG_MAX_T[0]	s	Pulse Input 1, Mean Value, Max
9801	uint	_S0_POWER_AVG_MAX_T[1]	s	Pulse Input 1, Mean Value, Max.
9803	uint	_EXT_TEMPERATUR_AVG_MAX_T	s	Temperatur, Mean Value, Max
9805	short	_W_Tariff		Real Energy, Tariff
9806	short	_Q_Tariff		Reactiv energy, Tariff
9807	float	_WH_S[0]	VAh	Apparent energy, L1
9809	float	_WH_S[1]	VAh	Apparent energy, L2
9811	float	_WH_S[2]	VAh	Apparent energy, L3
9813	float	_WH_S[3]	VAh	Apparent energy, L4
9815	float	_WH_S[4]	VAh	Apparent energy, L1+L2+L3
9817	float	_WH_S[5]	VAh	Apparent energy, L1+L2+L3+L4
9819	float	_WH[0]	Wh	Real Energy, L1
9821	float	_WH[1]	Wh	Real Energy, L2
9823	float	_WH[2]	Wh	Real Energy, L3
9825	float	_WH[3]	Wh	Real Energy, L4
9827	float	_WH[4]	Wh	Real Energy, L1+L2+L3
9829	float	_WH[5]	Wh	Real Energy, L1+L2+L3+L4
9831	float	_QH[0]	VArh	Reactiv energy, L1
9833	float	_QH[1]	VArh	Reactiv energy, L2
9835	float	_QH[2]	VArh	Reactiv energy, L3
9837	float	_QH[3]	VArh	Reactiv energy, L4
9839	float	_QH[4]	VArh	Reactiv energy, L1+L2+L3
9841	float	_QH[5]	VArh	Reactiv energy, L1+L2+L3+L4
9843	float	_WH_V[0]	Wh	Real Energy, Supply, L1
9845	float	_WH_V[1]	Wh	Real Energy, Supply, L2
9847	float	_WH_V[2]	Wh	Real Energy, Supply, L3
9849	float	_WH_V[3]	Wh	Real Energy, Supply, L4
9851	float	_WH_V[4]	Wh	Real Energy, Supply, L1+L2+L3
9853	float	_WH_V[5]	Wh	Real Energy, Supply, L1+L2+L3+L4
9855	float	_WH_Z[0]	Wh	Real Energy, Consumption, L1
9857	float	_WH_Z[1]	Wh	Real Energy, Consumption, L2
9859	float	_WH_Z[2]	Wh	Real Energy, Consumption, L3
9861	float	_WH_Z[3]	Wh	Real Energy, Consumption, L4
9863	float	_WH_Z[4]	Wh	Real Energy, Consumption, L1+L2+L3
9865	float	_WH_Z[5]	Wh	Real Energy, Con., L1+L2+L3+L4
9867	float	_WH_V_HT[0]	Wh	Real Energy, Supply, HT-Tariff, L1
9869	float	_WH_V_HT[1]	Wh	Real Energy, Supply, HT-Tariff, L2
9871	float	_WH_V_HT[2]	Wh	Real Energy, Supply, HT-Tariff, L3
9873	float	_WH_V_HT[3]	Wh	Real Energy, Supply, HT-Tariff, L4
9875	float	_WH_V_HT[4]	Wh	Real Energy, Supply, HT-Tariff, L1+L2+L3
9877	float	_WH_V_HT[5]	Wh	Real Energy, Bez, HT-Tar., L1+L2+L3+L4
9879	float	_WH_V_NT[0]	Wh	Real Energy, Supply, NT-Tariff, L1
9881	float	_WH_V_NT[1]	Wh	Real Energy, Supply, NT-Tariff, L2
9883	float	_WH_V_NT[2]	Wh	Real Energy, Supply, NT-Tariff, L3
9885	float	_WH_V_NT[3]	Wh	Real Energy, Supply, NT-Tariff, L4
9887	float	_WH_V_NT[4]	Wh	Real Energy, Con., NT-Tariff, L1+L2+L3
9889	float	_WH_V_NT[5]	Wh	Real Energy, Con., NT-Tar., L1+L2+L3+L4
9891	float	_WH_Z_HT[0]	Wh	Real Energy, Consumption, HT-Tariff, L1
9893	float	_WH_Z_HT[1]	Wh	Real Energy, Consumption, HT-Tariff, L2
9895	float	_WH_Z_HT[2]	Wh	Real Energy, Consumption, HT-Tariff, L3

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9897	float	_WH_Z_HT[3]	Wh	Real Energy, Consumption, HT-Tariff, L4
9899	float	_WH_Z_HT[4]	Wh	Real Energy, Con., HT-Tariff, L1+L2+L3
9901	float	_WH_Z_HT[5]	Wh	Real Energy, Lief., HT-Tar., L1+L2+L3+L4
9903	float	_WH_Z_NT[0]	Wh	Real Energy, Consumption, NT-Tariff, L1
9905	float	_WH_Z_NT[1]	Wh	Real Energy, Consumption, NT-Tariff, L2
9907	float	_WH_Z_NT[2]	Wh	Real Energy, Consumption, NT-Tariff, L3
9909	float	_WH_Z_NT[3]	Wh	Real Energy, Consumption, NT-Tariff, L4
9911	float	_WH_Z_NT[4]	Wh	Real Energy, Lief., NT-Tariff, L1+L2+L3
9913	float	_WH_Z_NT[5]	Wh	Real Energy, Lief., NT-Tar., L1+L2+L3+L4
9915	float	_IQH[0]	VArh	Reactiv energy, inductive, L1
9917	float	_IQH[1]	VArh	Reactiv energy, inductive, L2
9919	float	_IQH[2]	VArh	Reactiv energy, inductive, L3
9921	float	_IQH[3]	VArh	Reactiv energy, inductive, L4
9923	float	_IQH[4]	VArh	Reactiv energy, inductive, L1+L2+L3
9925	float	_IQH[5]	VArh	Reactiv energy, inductive, L1+L2+L3+L4
9927	float	_CQH[0]	VArh	Reactiv energy, capacitative, L1
9929	float	_CQH[1]	VArh	Reactiv energy, capacitative, L2
9931	float	_CQH[2]	VArh	Reactiv energy, capacitative, L3
9933	float	_CQH[3]	VArh	Reactiv energy, capacitative, L4
9935	float	_CQH[4]	VArh	Reactiv energy, capacitative, L1+L2+L3
9937	float	_CQH[5]	VArh	Reactiv energy, cap., L1+L2+L3+L4
9939	float	_IQH_HT[0]	VArh	Reactiv energy, HT, inductive, L1
9941	float	_IQH_HT[1]	VArh	Reactiv energy, HT, inductive, L2
9943	float	_IQH_HT[2]	VArh	Reactiv energy, HT, inductive, L3
9945	float	_IQH_HT[3]	VArh	Reactiv energy, HT, inductive, L4
9947	float	_IQH_HT[4]	VArh	Reactiv energy, HT, inductive, L1+L2+L3
9949	float	_IQH_HT[5]	VArh	Reactiv energy, HT, ind., L1+L2+L3+L4
9951	float	_IQH_NT[0]	VArh	Reactiv energy, NT, inductive, L1
9953	float	_IQH_NT[1]	VArh	Reactiv energy, NT, inductive, L2
9955	float	_IQH_NT[2]	VArh	Reactiv energy, NT, inductive, L3
9957	float	_IQH_NT[3]	VArh	Reactiv energy, NT, inductive, L4
9959	float	_IQH_NT[4]	VArh	Reactiv energy, NT, inductive, L1+L2+L3
9961	float	_IQH_NT[5]	VArh	Reactiv energy, NT, ind., L1+L2+L3+L4
9963	float	_S0_CNT[0]	n	Counter, Pulse Input 1
9965	float	_S0_CNT[1]	n	Counter, Pulse Input 2
9967	float	_TIME_WH	s	Active and Apparent Energy Measurement, Elapsed Time
9969	float	_TIME_QH	s	Reactive Energy Measurement, Elapsed Time
9971	float	_SPU012	V	Neutral Point Displacement Voltage
9973	short	_DIGOUT_STAT[0]	n	Digital output 1, Status (r/w)
9974	short	_DIGOUT_STAT[1]	n	Digital output 2, Status (r/w)
9975	short	_DIGIN_STAT[0]	n	Digital input 1, Status (r)
9976	short	_DIGIN_STAT[1]	n	Digital input 2, Status (r)
9977	uint	_EVT_COUNT	n	Event-Counter
9979	uint	_FLAG_COUNT	n	Flag-Counter
9981	uint	_TRANS_COUNT	n	Transienten-Counter
9983	uint	_HWW_COUNT	n	Half Wave Effective Value Counter
9985	uint	_RX232_COUNT	n	Counter for Received Packets
9987	uint	_TX232_COUNT	n	Counter for Sent Packets
9989	uint	_ERR232_COUNT	n	Count. for Received Packets with Errors
9991	uint	_RX485_COUNT	n	Counter for Received Packets
9993	uint	_TX485_COUNT	n	Counter for Sent Packets
9995	uint	_ERR485_COUNT	n	Count. for Received Packets with Errors
9997	short	_DEL_WH		Delete all Effective Energy and Apparent Energy Counters (=1)
9998	short	_DEL_QH		Delete all Reactive Energy Counters (=1)
9999	short	_INIT_MAX		Delete all Minimum and Maximum Values
10032	float	_CTPRIM[0]	A	Current transformer, primary, L1



Address	Format	Designation	Unit	Remarks
10034	float	_CTPRIM[1]	A	Current transformer, primary, L2
10036	float	_CTPRIM[2]	A	Current transformer, primary, L3
10038	float	_CTPRIM[3]	A	Current transformer, primary, L4
10040	float	_CTSEC[0]	A	Current transformer, secondary, L1
10042	float	_CTSEC[1]	A	Current transformer, secondary, L2
10044	float	_CTSEC[2]	A	Current transformer, secondary, L3
10046	float	_CTSEC[3]	A	Current transformer, secondary, L4
10048	float	_VTPRIM[0]	V	Voltage transformer, primary, L1
10050	float	_VTPRIM[1]	V	Voltage transformer, primary, L2
10052	float	_VTPRIM[2]	V	Voltage transformer, primary, L3
10054	float	_VTPRIM[3]	V	Voltage transformer, primary, L4
10056	float	_VTSEC[0]	V	Voltage transformer, secondary, L1
10058	float	_VTSEC[1]	V	Voltage transformer, secondary, L2
10060	float	_VTSEC[2]	V	Voltage transformer, secondary, L3
10062	float	_VTSEC[3]	V	Voltage transformer, secondary, L4
10064	float	_IRATED[0]	A	Rated Current, K-Factor, L1
10066	float	_IRATED[1]	A	Rated Current, K-Factor, L1
10068	float	_IRATED[2]	A	Rated Current, K-Factor, L1
10070	float	_IRATED[3]	A	Rated Current, K-Factor, L1
10072	string	_DEV_NAME	64	Device Name
10104	string	_DEV_DESC	128	Device Description
10168	string	_LANGUAGE	16	Language Selection, Home Page
10176	uint	_SERNR		Serial-Number
10178	uint	_PRODNR		Production Number
10180	int	_MBUSADDR		Modbus Address
10182	int	_MODE485		Protokoll, Modbus-RTU Slave or Modbus-RTU Master
10184	int	_BAUD485		Baud Rate RS485
10186	int	_BAUD232		Baud Rate RS232
10188	int	_MODE232		Protokoll, Modbus-RTU Slave
10190	uint	_IP_ADDR		Geräte IP-Adresse
10192	uint	_IP_MASK		Geräte IP-Maske
10194	uint	_IP_GATE		Geräte Gateway-Adresse
10196	int	_DHCPMODE		IP-Mode
10198	int	_CONTRAST		LCD-Contrast
10200	int	_THERMOELEMENT		Temperature Sensor, type 0=PT100, 1=PT1000, 2=KTY-83, 3=KTY-84
10202	int	_KEY1		Button 1
10204	int	_KEY2		Button 2
10206	int	_KEY3		Service Button
10208	uint	_DEBUG_IP		IP Address for Debug Notifications
10210	int	_TIME_ZONE	s	Time Zones Offset
10212	int	_STIME	s	Daylight Savings Time Offset (3600)
		<i>Switch Daylight Savings Time-Standard Time</i>		
10214	short	_SDAY		Starting Date of Daylight Savings Time
10215	short	_SHOUR	h	Starting Hour of Daylight Savings Time
10216	short	_SMON		Starting Month of Daylight Savings Time
10217	short	_SMIN	min	Starting Minute of Daylight Savings Time
10218	short	_SDOW		Starting Minute of Daylight Savings Time
10219	short	_EDAY		Last Day of Daylight Savings Time
10220	short	_EHOUR	h	Last Hour of Daylight Savings Time
10221	short	_EMON		Last Month of Daylight Savings Time
10222	short	_EMIN	min	Last Minute of Daylight Savings Time
10223	short	_EDOW		Day of the Week, End of Daylight Savings Time
10232	float	_NOMINAL_U[0]	V	

Address	Format	Designation	Unit	Remarks
10234	float	_NOMINAL_U[1]	V	
10236	float	_NOMINAL_U[2]	V	
10238	float	_NOMINAL_U[3]	V	
10240	float	_NOMINAL_I[0]	A	
10242	float	_NOMINAL_I[1]	A	
10244	float	_NOMINAL_I[2]	A	
10246	float	_NOMINAL_I[3]	A	
10248	float	_NOMINAL_F	Hz	
10250	float	_TRNS_DELTA[0]	%	
10252	float	_TRNS_DELTA[1]	%	
10254	float	_TRNS_DELTA[2]	%	
10256	float	_TRNS_DELTA[3]	%	
10258	float	_TRNS_ABS[0]	%	
10260	float	_TRNS_ABS[1]	%	
10262	float	_TRNS_ABS[2]	%	
10264	float	_TRNS_ABS[3]	%	
10266	short	_TRNS_PRE	n	
10267	short	_TRNS_POST	n	
10268	float	_I_EVT_MAX[0]	%	
10270	float	_I_EVT_MAX[1]	%	
10272	float	_I_EVT_MAX[2]	%	
10274	float	_I_EVT_MAX[3]	%	
10276	float	_U_EVT_MAX[0]	%	
10278	float	_U_EVT_MAX[1]	%	
10280	float	_U_EVT_MAX[2]	%	
10282	float	_U_EVT_MAX[3]	%	
10284	float	_U_EVT_MIN[0]	%	
10286	float	_U_EVT_MIN[1]	%	
10288	float	_U_EVT_MIN[2]	%	
10290	float	_U_EVT_MIN[3]	%	
10292	short	_EVT_VAL_PRE	n	
10293	short	_EVT_VAL_POST	n	
10294	int	_EVT_CNT_POST	n	
10296	int	_EVT_CNT_PRE	n	
10298	int	_TRNS_MODE	n	
10300	int	_EVT_MODE	n	
10302	short	_ARON	n	
10303	short	_DREILEITER	n	
10408	float	_PULSWERT[0]	n/Wh	
10410	float	_PULSWERT[1]	n/Wh	
10412	float	_MAXSIZE_REC	%	
10414	float	_MAXSIZE_TRNS	%	
10416	float	_MAXSIZE_VWW	%	
10418	float	_MAXSIZE_EVT	%	
10420	float	_MAXSIZE_FLAGS	%	
10422	int	_TFTP_FILE_NR	n	
10424	int	_TFTP_NEWFILE	n	
10426	int	_DIGOUTEVT[0]	bin	
10428	int	_DIGOUTEVT[1]	bin	
10430	int	_DIGOUTEVT_TIME[0]	0.01s	
10432	int	_DIGOUTEVT_TIME[1]	0.01s	
10434	short	_INVERT_DIGOUT[0]	bool	
10435	short	_INVERT_DIGOUT[1]	bool	
10436	short	_EMAX_SPERRZEIT	s	
10437	short	_EMAX_PAUSENZEIT	s	
10438	float	_EMAX_LEISTUNG	W	
10440	float	_EMAX_MAX	W	
10442	float	_EMAX_TRENDWERT	W	

Address	Format	Designation	Unit	Remarks
10444	uint	_EMAX_MAX_T		
10446	short	_EMAX_D_STATUS[0]	n	
10447	short	_EMAX_D_STATUS[1]	n	
10448	short	_EMAX_D_STATUS[2]	n	
10449	short	_EMAX_D_STATUS[3]	n	
10450	short	_EMAX_D_STATUS[4]	n	
10451	short	_EMAX_D_STATUS[5]	n	
10452	short	_EMAX_D_STATUS[6]	n	
10453	short	_EMAX_D_STATUS[7]	n	
10454	short	_EMAX_D_STATUS[8]	n	
10455	short	_EMAX_D_STATUS[9]	n	
10456	short	_EMAX_D_STATUS[10]	n	
10457	short	_EMAX_D_STATUS[11]	n	
10458	short	_EMAX_D_STATUS[12]	n	
10459	short	_EMAX_D_STATUS[13]	n	
10460	short	_EMAX_D_STATUS[14]	n	
10461	short	_EMAX_D_STATUS[15]	n	
10462	short	_EMAX_D_STATUS[16]	n	
10463	short	_EMAX_D_STATUS[17]	n	
10464	short	_EMAX_D_STATUS[18]	n	
10465	short	_EMAX_D_STATUS[19]	n	
10466	short	_EMAX_D_STATUS[20]	n	
10467	short	_EMAX_D_STATUS[21]	n	
10468	short	_EMAX_D_STATUS[22]	n	
10469	short	_EMAX_D_STATUS[23]	n	
10470	short	_EMAX_D_STATUS[24]	n	
10471	short	_EMAX_D_STATUS[25]	n	
10472	short	_EMAX_D_STATUS[26]	n	
10473	short	_EMAX_D_STATUS[27]	n	
10474	short	_EMAX_D_STATUS[28]	n	
10475	short	_EMAX_D_STATUS[29]	n	
10476	short	_EMAX_D_STATUS[30]	n	
10477	short	_EMAX_D_STATUS[31]	n	
10478	short	_EMAX_D_STATUS[32]	n	
10479	short	_EMAX_D_STATUS[33]	n	
10480	short	_EMAX_D_STATUS[34]	n	
10481	short	_EMAX_D_STATUS[35]	n	
10482	short	_EMAX_D_STATUS[36]	n	
10483	short	_EMAX_D_STATUS[37]	n	
10484	short	_EMAX_D_STATUS[38]	n	
10485	short	_EMAX_D_STATUS[39]	n	
10486	short	_EMAX_D_STATUS[40]	n	
10487	short	_EMAX_D_STATUS[41]	n	
10488	short	_EMAX_D_STATUS[42]	n	
10489	short	_EMAX_D_STATUS[43]	n	
10490	short	_EMAX_D_STATUS[44]	n	
10491	short	_EMAX_D_STATUS[45]	n	
10492	short	_EMAX_D_STATUS[46]	n	
10493	short	_EMAX_D_STATUS[47]	n	
10494	short	_EMAX_D_STATUS[48]	n	
10495	short	_EMAX_D_STATUS[49]	n	
10496	short	_EMAX_D_STATUS[50]	n	
10497	short	_EMAX_D_STATUS[51]	n	
10498	short	_EMAX_D_STATUS[52]	n	
10499	short	_EMAX_D_STATUS[53]	n	
10500	short	_EMAX_D_STATUS[54]	n	
10501	short	_EMAX_D_STATUS[55]	n	
10502	short	_EMAX_D_STATUS[56]	n	
10503	short	_EMAX_D_STATUS[57]	n	

Address	Format	Designation	Unit	Remarks
10504	short	_EMAX_D_STATUS[58]	n	
10505	short	_EMAX_D_STATUS[59]	n	
10506	short	_EMAX_D_STATUS[60]	n	
10507	short	_EMAX_D_STATUS[61]	n	
10508	short	_EMAX_D_STATUS[62]	n	
10509	short	_EMAX_D_STATUS[63]	n	
10510	float	_EMAX_A_STATUS[0]	%	
10512	float	_EMAX_A_STATUS[1]	%	
10514	float	_EMAX_A_STATUS[2]	%	
10516	float	_EMAX_A_STATUS[3]	%	
10518	short	_EMAX_D_ACTIVE[0]		
10519	short	_EMAX_D_ACTIVE[1]		
10520	short	_EMAX_D_ACTIVE[2]		
10521	short	_EMAX_D_ACTIVE[3]		
10522	short	_EMAX_D_ACTIVE[4]		
10523	short	_EMAX_D_ACTIVE[5]		
10524	short	_EMAX_D_ACTIVE[6]		
10525	short	_EMAX_D_ACTIVE[7]		
10526	short	_EMAX_D_ACTIVE[8]		
10527	short	_EMAX_D_ACTIVE[9]		
10528	short	_EMAX_D_ACTIVE[10]		
10529	short	_EMAX_D_ACTIVE[11]		
10530	short	_EMAX_D_ACTIVE[12]		
10531	short	_EMAX_D_ACTIVE[13]		
10532	short	_EMAX_D_ACTIVE[14]		
10533	short	_EMAX_D_ACTIVE[15]		
10534	short	_EMAX_D_ACTIVE[16]		
10535	short	_EMAX_D_ACTIVE[17]		
10536	short	_EMAX_D_ACTIVE[18]		
10537	short	_EMAX_D_ACTIVE[19]		
10538	short	_EMAX_D_ACTIVE[20]		
10539	short	_EMAX_D_ACTIVE[21]		
10540	short	_EMAX_D_ACTIVE[22]		
10541	short	_EMAX_D_ACTIVE[23]		
10542	short	_EMAX_D_ACTIVE[24]		
10543	short	_EMAX_D_ACTIVE[25]		
10544	short	_EMAX_D_ACTIVE[26]		
10545	short	_EMAX_D_ACTIVE[27]		
10546	short	_EMAX_D_ACTIVE[28]		
10547	short	_EMAX_D_ACTIVE[29]		
10548	short	_EMAX_D_ACTIVE[30]		
10549	short	_EMAX_D_ACTIVE[31]		
10550	short	_EMAX_D_ACTIVE[32]		
10551	short	_EMAX_D_ACTIVE[33]		
10552	short	_EMAX_D_ACTIVE[34]		
10553	short	_EMAX_D_ACTIVE[35]		
10554	short	_EMAX_D_ACTIVE[36]		
10555	short	_EMAX_D_ACTIVE[37]		
10556	short	_EMAX_D_ACTIVE[38]		
10557	short	_EMAX_D_ACTIVE[39]		
10558	short	_EMAX_D_ACTIVE[40]		
10559	short	_EMAX_D_ACTIVE[41]		
10560	short	_EMAX_D_ACTIVE[42]		
10561	short	_EMAX_D_ACTIVE[43]		
10562	short	_EMAX_D_ACTIVE[44]		
10563	short	_EMAX_D_ACTIVE[45]		
10564	short	_EMAX_D_ACTIVE[46]		
10565	short	_EMAX_D_ACTIVE[47]		
10566	short	_EMAX_D_ACTIVE[48]		

Address	Format	Designation	Unit	Remarks
10567	short	_EMAX_D_ACTIVE[49]		
10568	short	_EMAX_D_ACTIVE[50]		
10569	short	_EMAX_D_ACTIVE[51]		
10570	short	_EMAX_D_ACTIVE[52]		
10571	short	_EMAX_D_ACTIVE[53]		
10572	short	_EMAX_D_ACTIVE[54]		
10573	short	_EMAX_D_ACTIVE[55]		
10574	short	_EMAX_D_ACTIVE[56]		
10575	short	_EMAX_D_ACTIVE[57]		
10576	short	_EMAX_D_ACTIVE[58]		
10577	short	_EMAX_D_ACTIVE[59]		
10578	short	_EMAX_D_ACTIVE[60]		
10579	short	_EMAX_D_ACTIVE[61]		
10580	short	_EMAX_D_ACTIVE[62]		
10581	short	_EMAX_D_ACTIVE[63]		
10582	short	_EMAX_A_ACTIVE[0]		
10583	short	_EMAX_A_ACTIVE[1]		
10584	short	_EMAX_A_ACTIVE[2]		
10585	short	_EMAX_A_ACTIVE[3]		
10586	string	_EMAX_D_DESC0	32	
10602	string	_EMAX_D_DESC1	32	
10618	string	_EMAX_D_DESC2	32	
10634	string	_EMAX_D_DESC3	32	
10650	string	_EMAX_D_DESC4	32	
10666	string	_EMAX_D_DESC5	32	
10682	string	_EMAX_D_DESC6	32	
10698	string	_EMAX_D_DESC7	32	
10714	string	_EMAX_D_DESC8	32	
10730	string	_EMAX_D_DESC9	32	
10746	string	_EMAX_D_DESC10	32	
10762	string	_EMAX_D_DESC11	32	
10778	string	_EMAX_D_DESC12	32	
10794	string	_EMAX_D_DESC13	32	
10810	string	_EMAX_D_DESC14	32	
10826	string	_EMAX_D_DESC15	32	
10842	string	_EMAX_D_DESC16	32	
10858	string	_EMAX_D_DESC17	32	
10874	string	_EMAX_D_DESC18	32	
10890	string	_EMAX_D_DESC19	32	
10906	string	_EMAX_D_DESC20	32	
10922	string	_EMAX_D_DESC21	32	
10938	string	_EMAX_D_DESC22	32	
10954	string	_EMAX_D_DESC23	32	
10970	string	_EMAX_D_DESC24	32	
10986	string	_EMAX_D_DESC25	32	
11002	string	_EMAX_D_DESC26	32	
11018	string	_EMAX_D_DESC27	32	
11034	string	_EMAX_D_DESC28	32	
11050	string	_EMAX_D_DESC29	32	
11066	string	_EMAX_D_DESC30	32	
11082	string	_EMAX_D_DESC31	32	
11098	string	_EMAX_D_DESC32	32	
11114	string	_EMAX_D_DESC33	32	
11130	string	_EMAX_D_DESC34	32	
11146	string	_EMAX_D_DESC35	32	
11162	string	_EMAX_D_DESC36	32	
11178	string	_EMAX_D_DESC37	32	
11194	string	_EMAX_D_DESC38	32	
11210	string	_EMAX_D_DESC39	32	

Address	Format	Designation	Unit	Remarks
11226	string	_EMAX_D_DESC40	32	
11242	string	_EMAX_D_DESC41	32	
11258	string	_EMAX_D_DESC42	32	
11274	string	_EMAX_D_DESC43	32	
11290	string	_EMAX_D_DESC44	32	
11306	string	_EMAX_D_DESC45	32	
11322	string	_EMAX_D_DESC46	32	
11338	string	_EMAX_D_DESC47	32	
11354	string	_EMAX_D_DESC48	32	
11370	string	_EMAX_D_DESC49	32	
11386	string	_EMAX_D_DESC50	32	
11402	string	_EMAX_D_DESC51	32	
11418	string	_EMAX_D_DESC52	32	
11434	string	_EMAX_D_DESC53	32	
11450	string	_EMAX_D_DESC54	32	
11466	string	_EMAX_D_DESC55	32	
11482	string	_EMAX_D_DESC56	32	
11498	string	_EMAX_D_DESC57	32	
11514	string	_EMAX_D_DESC58	32	
11530	string	_EMAX_D_DESC59	32	
11546	string	_EMAX_D_DESC60	32	
11562	string	_EMAX_D_DESC61	32	
11578	string	_EMAX_D_DESC62	32	
11594	string	_EMAX_D_DESC63	32	
11610	string	_EMAX_A_DESC0	32	
11626	string	_EMAX_A_DESC1	32	
11642	string	_EMAX_A_DESC2	32	
11658	string	_EMAX_A_DESC3	32	
11674	short	_EMAX_D_PRIORITY[0]		
11675	short	_EMAX_D_PRIORITY[1]		
11676	short	_EMAX_D_PRIORITY[2]		
11677	short	_EMAX_D_PRIORITY[3]		
11678	short	_EMAX_D_PRIORITY[4]		
11679	short	_EMAX_D_PRIORITY[5]		
11680	short	_EMAX_D_PRIORITY[6]		
11681	short	_EMAX_D_PRIORITY[7]		
11682	short	_EMAX_D_PRIORITY[8]		
11683	short	_EMAX_D_PRIORITY[9]		
11684	short	_EMAX_D_PRIORITY[10]		
11685	short	_EMAX_D_PRIORITY[11]		
11686	short	_EMAX_D_PRIORITY[12]		
11687	short	_EMAX_D_PRIORITY[13]		
11688	short	_EMAX_D_PRIORITY[14]		
11689	short	_EMAX_D_PRIORITY[15]		
11690	short	_EMAX_D_PRIORITY[16]		
11691	short	_EMAX_D_PRIORITY[17]		
11692	short	_EMAX_D_PRIORITY[18]		
11693	short	_EMAX_D_PRIORITY[19]		
11694	short	_EMAX_D_PRIORITY[20]		
11695	short	_EMAX_D_PRIORITY[21]		
11696	short	_EMAX_D_PRIORITY[22]		
11697	short	_EMAX_D_PRIORITY[23]		
11698	short	_EMAX_D_PRIORITY[24]		
11699	short	_EMAX_D_PRIORITY[25]		
11700	short	_EMAX_D_PRIORITY[26]		
11701	short	_EMAX_D_PRIORITY[27]		
11702	short	_EMAX_D_PRIORITY[28]		
11703	short	_EMAX_D_PRIORITY[29]		
11704	short	_EMAX_D_PRIORITY[30]		



Address	Format	Designation	Unit	Remarks
11705	short	_EMAX_D_PRIORITY[31]		
11706	short	_EMAX_D_PRIORITY[32]		
11707	short	_EMAX_D_PRIORITY[33]		
11708	short	_EMAX_D_PRIORITY[34]		
11709	short	_EMAX_D_PRIORITY[35]		
11710	short	_EMAX_D_PRIORITY[36]		
11711	short	_EMAX_D_PRIORITY[37]		
11712	short	_EMAX_D_PRIORITY[38]		
11713	short	_EMAX_D_PRIORITY[39]		
11714	short	_EMAX_D_PRIORITY[40]		
11715	short	_EMAX_D_PRIORITY[41]		
11716	short	_EMAX_D_PRIORITY[42]		
11717	short	_EMAX_D_PRIORITY[43]		
11718	short	_EMAX_D_PRIORITY[44]		
11719	short	_EMAX_D_PRIORITY[45]		
11720	short	_EMAX_D_PRIORITY[46]		
11721	short	_EMAX_D_PRIORITY[47]		
11722	short	_EMAX_D_PRIORITY[48]		
11723	short	_EMAX_D_PRIORITY[49]		
11724	short	_EMAX_D_PRIORITY[50]		
11725	short	_EMAX_D_PRIORITY[51]		
11726	short	_EMAX_D_PRIORITY[52]		
11727	short	_EMAX_D_PRIORITY[53]		
11728	short	_EMAX_D_PRIORITY[54]		
11729	short	_EMAX_D_PRIORITY[55]		
11730	short	_EMAX_D_PRIORITY[56]		
11731	short	_EMAX_D_PRIORITY[57]		
11732	short	_EMAX_D_PRIORITY[58]		
11733	short	_EMAX_D_PRIORITY[59]		
11734	short	_EMAX_D_PRIORITY[60]		
11735	short	_EMAX_D_PRIORITY[61]		
11736	short	_EMAX_D_PRIORITY[62]		
11737	short	_EMAX_D_PRIORITY[63]		
11738	short	_EMAX_A_PRIORITY[0]		
11739	short	_EMAX_A_PRIORITY[1]		
11740	short	_EMAX_A_PRIORITY[2]		
11741	short	_EMAX_A_PRIORITY[3]		
11742	float	_EMAX_D_POWER[0]	W	
11744	float	_EMAX_D_POWER[1]	W	
11746	float	_EMAX_D_POWER[2]	W	
11748	float	_EMAX_D_POWER[3]	W	
11750	float	_EMAX_D_POWER[4]	W	
11752	float	_EMAX_D_POWER[5]	W	
11754	float	_EMAX_D_POWER[6]	W	
11756	float	_EMAX_D_POWER[7]	W	
11758	float	_EMAX_D_POWER[8]	W	
11760	float	_EMAX_D_POWER[9]	W	
11762	float	_EMAX_D_POWER[10]	W	
11764	float	_EMAX_D_POWER[11]	W	
11766	float	_EMAX_D_POWER[12]	W	
11768	float	_EMAX_D_POWER[13]	W	
11770	float	_EMAX_D_POWER[14]	W	
11772	float	_EMAX_D_POWER[15]	W	
11774	float	_EMAX_D_POWER[16]	W	
11776	float	_EMAX_D_POWER[17]	W	
11778	float	_EMAX_D_POWER[18]	W	
11780	float	_EMAX_D_POWER[19]	W	
11782	float	_EMAX_D_POWER[20]	W	
11784	float	_EMAX_D_POWER[21]	W	

Address	Format	Designation	Unit	Remarks
11786	float	_EMAX_D_POWER[22]	W	
11788	float	_EMAX_D_POWER[23]	W	
11790	float	_EMAX_D_POWER[24]	W	
11792	float	_EMAX_D_POWER[25]	W	
11794	float	_EMAX_D_POWER[26]	W	
11796	float	_EMAX_D_POWER[27]	W	
11798	float	_EMAX_D_POWER[28]	W	
11800	float	_EMAX_D_POWER[29]	W	
11802	float	_EMAX_D_POWER[30]	W	
11804	float	_EMAX_D_POWER[31]	W	
11806	float	_EMAX_D_POWER[32]	W	
11808	float	_EMAX_D_POWER[33]	W	
11810	float	_EMAX_D_POWER[34]	W	
11812	float	_EMAX_D_POWER[35]	W	
11814	float	_EMAX_D_POWER[36]	W	
11816	float	_EMAX_D_POWER[37]	W	
11818	float	_EMAX_D_POWER[38]	W	
11820	float	_EMAX_D_POWER[39]	W	
11822	float	_EMAX_D_POWER[40]	W	
11824	float	_EMAX_D_POWER[41]	W	
11826	float	_EMAX_D_POWER[42]	W	
11828	float	_EMAX_D_POWER[43]	W	
11830	float	_EMAX_D_POWER[44]	W	
11832	float	_EMAX_D_POWER[45]	W	
11834	float	_EMAX_D_POWER[46]	W	
11836	float	_EMAX_D_POWER[47]	W	
11838	float	_EMAX_D_POWER[48]	W	
11840	float	_EMAX_D_POWER[49]	W	
11842	float	_EMAX_D_POWER[50]	W	
11844	float	_EMAX_D_POWER[51]	W	
11846	float	_EMAX_D_POWER[52]	W	
11848	float	_EMAX_D_POWER[53]	W	
11850	float	_EMAX_D_POWER[54]	W	
11852	float	_EMAX_D_POWER[55]	W	
11854	float	_EMAX_D_POWER[56]	W	
11856	float	_EMAX_D_POWER[57]	W	
11858	float	_EMAX_D_POWER[58]	W	
11860	float	_EMAX_D_POWER[59]	W	
11862	float	_EMAX_D_POWER[60]	W	
11864	float	_EMAX_D_POWER[61]	W	
11866	float	_EMAX_D_POWER[62]	W	
11868	float	_EMAX_D_POWER[63]	W	
11870	short	_EMAX_D_MIN_ONDURATION[0]	s	
11871	short	_EMAX_D_MIN_ONDURATION[1]	s	
11872	short	_EMAX_D_MIN_ONDURATION[2]	s	
11873	short	_EMAX_D_MIN_ONDURATION[3]	s	
11874	short	_EMAX_D_MIN_ONDURATION[4]	s	
11875	short	_EMAX_D_MIN_ONDURATION[5]	s	
11876	short	_EMAX_D_MIN_ONDURATION[6]	s	
11877	short	_EMAX_D_MIN_ONDURATION[7]	s	
11878	short	_EMAX_D_MIN_ONDURATION[8]	s	
11879	short	_EMAX_D_MIN_ONDURATION[9]	s	
11880	short	_EMAX_D_MIN_ONDURATION[10]	s	
11881	short	_EMAX_D_MIN_ONDURATION[11]	s	
11882	short	_EMAX_D_MIN_ONDURATION[12]	s	
11883	short	_EMAX_D_MIN_ONDURATION[13]	s	
11884	short	_EMAX_D_MIN_ONDURATION[14]	s	
11885	short	_EMAX_D_MIN_ONDURATION[15]	s	
11886	short	_EMAX_D_MIN_ONDURATION[16]	s	



Address	Format	Designation	Unit	Remarks
11887	short	_EMAX_D_MIN_ONDURATION[17]	s	
11888	short	_EMAX_D_MIN_ONDURATION[18]	s	
11889	short	_EMAX_D_MIN_ONDURATION[19]	s	
11890	short	_EMAX_D_MIN_ONDURATION[20]	s	
11891	short	_EMAX_D_MIN_ONDURATION[21]	s	
11892	short	_EMAX_D_MIN_ONDURATION[22]	s	
11893	short	_EMAX_D_MIN_ONDURATION[23]	s	
11894	short	_EMAX_D_MIN_ONDURATION[24]	s	
11895	short	_EMAX_D_MIN_ONDURATION[25]	s	
11896	short	_EMAX_D_MIN_ONDURATION[26]	s	
11897	short	_EMAX_D_MIN_ONDURATION[27]	s	
11898	short	_EMAX_D_MIN_ONDURATION[28]	s	
11899	short	_EMAX_D_MIN_ONDURATION[29]	s	
11900	short	_EMAX_D_MIN_ONDURATION[30]	s	
11901	short	_EMAX_D_MIN_ONDURATION[31]	s	
11902	short	_EMAX_D_MIN_ONDURATION[32]	s	
11903	short	_EMAX_D_MIN_ONDURATION[33]	s	
11904	short	_EMAX_D_MIN_ONDURATION[34]	s	
11905	short	_EMAX_D_MIN_ONDURATION[35]	s	
11906	short	_EMAX_D_MIN_ONDURATION[36]	s	
11907	short	_EMAX_D_MIN_ONDURATION[37]	s	
11908	short	_EMAX_D_MIN_ONDURATION[38]	s	
11909	short	_EMAX_D_MIN_ONDURATION[39]	s	
11910	short	_EMAX_D_MIN_ONDURATION[40]	s	
11911	short	_EMAX_D_MIN_ONDURATION[41]	s	
11912	short	_EMAX_D_MIN_ONDURATION[42]	s	
11913	short	_EMAX_D_MIN_ONDURATION[43]	s	
11914	short	_EMAX_D_MIN_ONDURATION[44]	s	
11915	short	_EMAX_D_MIN_ONDURATION[45]	s	
11916	short	_EMAX_D_MIN_ONDURATION[46]	s	
11917	short	_EMAX_D_MIN_ONDURATION[47]	s	
11918	short	_EMAX_D_MIN_ONDURATION[48]	s	
11919	short	_EMAX_D_MIN_ONDURATION[49]	s	
11920	short	_EMAX_D_MIN_ONDURATION[50]	s	
11921	short	_EMAX_D_MIN_ONDURATION[51]	s	
11922	short	_EMAX_D_MIN_ONDURATION[52]	s	
11923	short	_EMAX_D_MIN_ONDURATION[53]	s	
11924	short	_EMAX_D_MIN_ONDURATION[54]	s	
11925	short	_EMAX_D_MIN_ONDURATION[55]	s	
11926	short	_EMAX_D_MIN_ONDURATION[56]	s	
11927	short	_EMAX_D_MIN_ONDURATION[57]	s	
11928	short	_EMAX_D_MIN_ONDURATION[58]	s	
11929	short	_EMAX_D_MIN_ONDURATION[59]	s	
11930	short	_EMAX_D_MIN_ONDURATION[60]	s	
11931	short	_EMAX_D_MIN_ONDURATION[61]	s	
11932	short	_EMAX_D_MIN_ONDURATION[62]	s	
11933	short	_EMAX_D_MIN_ONDURATION[63]	s	
11934	short	_EMAX_D_MAX_OFFDURATION[0]	s	
11935	short	_EMAX_D_MAX_OFFDURATION[1]	s	
11936	short	_EMAX_D_MAX_OFFDURATION[2]	s	
11937	short	_EMAX_D_MAX_OFFDURATION[3]	s	
11938	short	_EMAX_D_MAX_OFFDURATION[4]	s	
11939	short	_EMAX_D_MAX_OFFDURATION[5]	s	
11940	short	_EMAX_D_MAX_OFFDURATION[6]	s	
11941	short	_EMAX_D_MAX_OFFDURATION[7]	s	
11942	short	_EMAX_D_MAX_OFFDURATION[8]	s	
11943	short	_EMAX_D_MAX_OFFDURATION[9]	s	
11944	short	_EMAX_D_MAX_OFFDURATION[10]	s	
11945	short	_EMAX_D_MAX_OFFDURATION[11]	s	

Address	Format	Designation	Unit	Remarks
11946	short	_EMAX_D_MAX_OFFDURATION[12]	s	
11947	short	_EMAX_D_MAX_OFFDURATION[13]	s	
11948	short	_EMAX_D_MAX_OFFDURATION[14]	s	
11949	short	_EMAX_D_MAX_OFFDURATION[15]	s	
11950	short	_EMAX_D_MAX_OFFDURATION[16]	s	
11951	short	_EMAX_D_MAX_OFFDURATION[17]	s	
11952	short	_EMAX_D_MAX_OFFDURATION[18]	s	
11953	short	_EMAX_D_MAX_OFFDURATION[19]	s	
11954	short	_EMAX_D_MAX_OFFDURATION[20]	s	
11955	short	_EMAX_D_MAX_OFFDURATION[21]	s	
11956	short	_EMAX_D_MAX_OFFDURATION[22]	s	
11957	short	_EMAX_D_MAX_OFFDURATION[23]	s	
11958	short	_EMAX_D_MAX_OFFDURATION[24]	s	
11959	short	_EMAX_D_MAX_OFFDURATION[25]	s	
11960	short	_EMAX_D_MAX_OFFDURATION[26]	s	
11961	short	_EMAX_D_MAX_OFFDURATION[27]	s	
11962	short	_EMAX_D_MAX_OFFDURATION[28]	s	
11963	short	_EMAX_D_MAX_OFFDURATION[29]	s	
11964	short	_EMAX_D_MAX_OFFDURATION[30]	s	
11965	short	_EMAX_D_MAX_OFFDURATION[31]	s	
11966	short	_EMAX_D_MAX_OFFDURATION[32]	s	
11967	short	_EMAX_D_MAX_OFFDURATION[33]	s	
11968	short	_EMAX_D_MAX_OFFDURATION[34]	s	
11969	short	_EMAX_D_MAX_OFFDURATION[35]	s	
11970	short	_EMAX_D_MAX_OFFDURATION[36]	s	
11971	short	_EMAX_D_MAX_OFFDURATION[37]	s	
11972	short	_EMAX_D_MAX_OFFDURATION[38]	s	
11973	short	_EMAX_D_MAX_OFFDURATION[39]	s	
11974	short	_EMAX_D_MAX_OFFDURATION[40]	s	
11975	short	_EMAX_D_MAX_OFFDURATION[41]	s	
11976	short	_EMAX_D_MAX_OFFDURATION[42]	s	
11977	short	_EMAX_D_MAX_OFFDURATION[43]	s	
11978	short	_EMAX_D_MAX_OFFDURATION[44]	s	
11979	short	_EMAX_D_MAX_OFFDURATION[45]	s	
11980	short	_EMAX_D_MAX_OFFDURATION[46]	s	
11981	short	_EMAX_D_MAX_OFFDURATION[47]	s	
11982	short	_EMAX_D_MAX_OFFDURATION[48]	s	
11983	short	_EMAX_D_MAX_OFFDURATION[49]	s	
11984	short	_EMAX_D_MAX_OFFDURATION[50]	s	
11985	short	_EMAX_D_MAX_OFFDURATION[51]	s	
11986	short	_EMAX_D_MAX_OFFDURATION[52]	s	
11987	short	_EMAX_D_MAX_OFFDURATION[53]	s	
11988	short	_EMAX_D_MAX_OFFDURATION[54]	s	
11989	short	_EMAX_D_MAX_OFFDURATION[55]	s	
11990	short	_EMAX_D_MAX_OFFDURATION[56]	s	
11991	short	_EMAX_D_MAX_OFFDURATION[57]	s	
11992	short	_EMAX_D_MAX_OFFDURATION[58]	s	
11993	short	_EMAX_D_MAX_OFFDURATION[59]	s	
11994	short	_EMAX_D_MAX_OFFDURATION[60]	s	
11995	short	_EMAX_D_MAX_OFFDURATION[61]	s	
11996	short	_EMAX_D_MAX_OFFDURATION[62]	s	
11997	short	_EMAX_D_MAX_OFFDURATION[63]	s	
11998	short	_EMAX_D_MIN_OFFDURATION[0]	s	
11999	short	_EMAX_D_MIN_OFFDURATION[1]	s	
12000	short	_EMAX_D_MIN_OFFDURATION[2]	s	
12001	short	_EMAX_D_MIN_OFFDURATION[3]	s	
12002	short	_EMAX_D_MIN_OFFDURATION[4]	s	
12003	short	_EMAX_D_MIN_OFFDURATION[5]	s	
12004	short	_EMAX_D_MIN_OFFDURATION[6]	s	

Address	Format	Designation	Unit	Remarks
12005	short	_EMAX_D_MIN_OFFDURATION[7]	s	
12006	short	_EMAX_D_MIN_OFFDURATION[8]	s	
12007	short	_EMAX_D_MIN_OFFDURATION[9]	s	
12008	short	_EMAX_D_MIN_OFFDURATION[10]	s	
12009	short	_EMAX_D_MIN_OFFDURATION[11]	s	
12010	short	_EMAX_D_MIN_OFFDURATION[12]	s	
12011	short	_EMAX_D_MIN_OFFDURATION[13]	s	
12012	short	_EMAX_D_MIN_OFFDURATION[14]	s	
12013	short	_EMAX_D_MIN_OFFDURATION[15]	s	
12014	short	_EMAX_D_MIN_OFFDURATION[16]	s	
12015	short	_EMAX_D_MIN_OFFDURATION[17]	s	
12016	short	_EMAX_D_MIN_OFFDURATION[18]	s	
12017	short	_EMAX_D_MIN_OFFDURATION[19]	s	
12018	short	_EMAX_D_MIN_OFFDURATION[20]	s	
12019	short	_EMAX_D_MIN_OFFDURATION[21]	s	
12020	short	_EMAX_D_MIN_OFFDURATION[22]	s	
12021	short	_EMAX_D_MIN_OFFDURATION[23]	s	
12022	short	_EMAX_D_MIN_OFFDURATION[24]	s	
12023	short	_EMAX_D_MIN_OFFDURATION[25]	s	
12024	short	_EMAX_D_MIN_OFFDURATION[26]	s	
12025	short	_EMAX_D_MIN_OFFDURATION[27]	s	
12026	short	_EMAX_D_MIN_OFFDURATION[28]	s	
12027	short	_EMAX_D_MIN_OFFDURATION[29]	s	
12028	short	_EMAX_D_MIN_OFFDURATION[30]	s	
12029	short	_EMAX_D_MIN_OFFDURATION[31]	s	
12030	short	_EMAX_D_MIN_OFFDURATION[32]	s	
12031	short	_EMAX_D_MIN_OFFDURATION[33]	s	
12032	short	_EMAX_D_MIN_OFFDURATION[34]	s	
12033	short	_EMAX_D_MIN_OFFDURATION[35]	s	
12034	short	_EMAX_D_MIN_OFFDURATION[36]	s	
12035	short	_EMAX_D_MIN_OFFDURATION[37]	s	
12036	short	_EMAX_D_MIN_OFFDURATION[38]	s	
12037	short	_EMAX_D_MIN_OFFDURATION[39]	s	
12038	short	_EMAX_D_MIN_OFFDURATION[40]	s	
12039	short	_EMAX_D_MIN_OFFDURATION[41]	s	
12040	short	_EMAX_D_MIN_OFFDURATION[42]	s	
12041	short	_EMAX_D_MIN_OFFDURATION[43]	s	
12042	short	_EMAX_D_MIN_OFFDURATION[44]	s	
12043	short	_EMAX_D_MIN_OFFDURATION[45]	s	
12044	short	_EMAX_D_MIN_OFFDURATION[46]	s	
12045	short	_EMAX_D_MIN_OFFDURATION[47]	s	
12046	short	_EMAX_D_MIN_OFFDURATION[48]	s	
12047	short	_EMAX_D_MIN_OFFDURATION[49]	s	
12048	short	_EMAX_D_MIN_OFFDURATION[50]	s	
12049	short	_EMAX_D_MIN_OFFDURATION[51]	s	
12050	short	_EMAX_D_MIN_OFFDURATION[52]	s	
12051	short	_EMAX_D_MIN_OFFDURATION[53]	s	
12052	short	_EMAX_D_MIN_OFFDURATION[54]	s	
12053	short	_EMAX_D_MIN_OFFDURATION[55]	s	
12054	short	_EMAX_D_MIN_OFFDURATION[56]	s	
12055	short	_EMAX_D_MIN_OFFDURATION[57]	s	
12056	short	_EMAX_D_MIN_OFFDURATION[58]	s	
12057	short	_EMAX_D_MIN_OFFDURATION[59]	s	
12058	short	_EMAX_D_MIN_OFFDURATION[60]	s	
12059	short	_EMAX_D_MIN_OFFDURATION[61]	s	
12060	short	_EMAX_D_MIN_OFFDURATION[62]	s	
12061	short	_EMAX_D_MIN_OFFDURATION[63]	s	
12062	float	_EMAX_D_PROBABILITY[0]	%	
12064	float	_EMAX_D_PROBABILITY[1]	%	

Address	Format	Designation	Unit	Remarks
12066	float	_EMAX_D_PROBABILITY[2]	%	
12068	float	_EMAX_D_PROBABILITY[3]	%	
12070	float	_EMAX_D_PROBABILITY[4]	%	
12072	float	_EMAX_D_PROBABILITY[5]	%	
12074	float	_EMAX_D_PROBABILITY[6]	%	
12076	float	_EMAX_D_PROBABILITY[7]	%	
12078	float	_EMAX_D_PROBABILITY[8]	%	
12080	float	_EMAX_D_PROBABILITY[9]	%	
12082	float	_EMAX_D_PROBABILITY[10]	%	
12084	float	_EMAX_D_PROBABILITY[11]	%	
12086	float	_EMAX_D_PROBABILITY[12]	%	
12088	float	_EMAX_D_PROBABILITY[13]	%	
12090	float	_EMAX_D_PROBABILITY[14]	%	
12092	float	_EMAX_D_PROBABILITY[15]	%	
12094	float	_EMAX_D_PROBABILITY[16]	%	
12096	float	_EMAX_D_PROBABILITY[17]	%	
12098	float	_EMAX_D_PROBABILITY[18]	%	
12100	float	_EMAX_D_PROBABILITY[19]	%	
12102	float	_EMAX_D_PROBABILITY[20]	%	
12104	float	_EMAX_D_PROBABILITY[21]	%	
12106	float	_EMAX_D_PROBABILITY[22]	%	
12108	float	_EMAX_D_PROBABILITY[23]	%	
12110	float	_EMAX_D_PROBABILITY[24]	%	
12112	float	_EMAX_D_PROBABILITY[25]	%	
12114	float	_EMAX_D_PROBABILITY[26]	%	
12116	float	_EMAX_D_PROBABILITY[27]	%	
12118	float	_EMAX_D_PROBABILITY[28]	%	
12120	float	_EMAX_D_PROBABILITY[29]	%	
12122	float	_EMAX_D_PROBABILITY[30]	%	
12124	float	_EMAX_D_PROBABILITY[31]	%	
12126	float	_EMAX_D_PROBABILITY[32]	%	
12128	float	_EMAX_D_PROBABILITY[33]	%	
12130	float	_EMAX_D_PROBABILITY[34]	%	
12132	float	_EMAX_D_PROBABILITY[35]	%	
12134	float	_EMAX_D_PROBABILITY[36]	%	
12136	float	_EMAX_D_PROBABILITY[37]	%	
12138	float	_EMAX_D_PROBABILITY[38]	%	
12140	float	_EMAX_D_PROBABILITY[39]	%	
12142	float	_EMAX_D_PROBABILITY[40]	%	
12144	float	_EMAX_D_PROBABILITY[41]	%	
12146	float	_EMAX_D_PROBABILITY[42]	%	
12148	float	_EMAX_D_PROBABILITY[43]	%	
12150	float	_EMAX_D_PROBABILITY[44]	%	
12152	float	_EMAX_D_PROBABILITY[45]	%	
12154	float	_EMAX_D_PROBABILITY[46]	%	
12156	float	_EMAX_D_PROBABILITY[47]	%	
12158	float	_EMAX_D_PROBABILITY[48]	%	
12160	float	_EMAX_D_PROBABILITY[49]	%	
12162	float	_EMAX_D_PROBABILITY[50]	%	
12164	float	_EMAX_D_PROBABILITY[51]	%	
12166	float	_EMAX_D_PROBABILITY[52]	%	
12168	float	_EMAX_D_PROBABILITY[53]	%	
12170	float	_EMAX_D_PROBABILITY[54]	%	
12172	float	_EMAX_D_PROBABILITY[55]	%	
12174	float	_EMAX_D_PROBABILITY[56]	%	
12176	float	_EMAX_D_PROBABILITY[57]	%	
12178	float	_EMAX_D_PROBABILITY[58]	%	
12180	float	_EMAX_D_PROBABILITY[59]	%	
12182	float	_EMAX_D_PROBABILITY[60]	%	

Address	Format	Designation	Unit	Remarks
12184	float	_EMAX_D_PROBABILITY[61]	%	
12186	float	_EMAX_D_PROBABILITY[62]	%	
12188	float	_EMAX_D_PROBABILITY[63]	%	
12190	short	_EMAX_A_GENERATOR[0]		
12191	short	_EMAX_A_GENERATOR[1]		
12192	short	_EMAX_A_GENERATOR[2]		
12193	short	_EMAX_A_GENERATOR[3]		
12194	float	_EMAX_A_MAXPOWER[0]	W	
12196	float	_EMAX_A_MAXPOWER[1]	W	
12198	float	_EMAX_A_MAXPOWER[2]	W	
12200	float	_EMAX_A_MAXPOWER[3]	W	
12202	float	_EMAX_A_MINPOWER[0]	W	
12204	float	_EMAX_A_MINPOWER[1]	W	
12206	float	_EMAX_A_MINPOWER[2]	W	
12208	float	_EMAX_A_MINPOWER[3]	W	
12210	float	_EMAX_A_MAXSAVE[0]	W	
12212	float	_EMAX_A_MAXSAVE[1]	W	
12214	float	_EMAX_A_MAXSAVE[2]	W	
12216	float	_EMAX_A_MAXSAVE[3]	W	
12218	short	_EMAX_A_STARTUP[0]	s	
12219	short	_EMAX_A_STARTUP[1]	s	
12220	short	_EMAX_A_STARTUP[2]	s	
12221	short	_EMAX_A_STARTUP[3]	s	
12222	int	_EMAX_A_MINACTIVE[0]	s	
12224	int	_EMAX_A_MINACTIVE[1]	s	
12226	int	_EMAX_A_MINACTIVE[2]	s	
12228	int	_EMAX_A_MINACTIVE[3]	s	
12230	int	_EMAX_PERIOD	s	
12232	int	_EMAX_T_REMAIN	s	
12234	float	_EMAX_TREND	W	
12236	float	_EMAX_C_TREND	W	
12242	int	_QUARZ_KORR_NTP	ppm	NTP Correction (read only)
12409	int	_QUARZ_KORR	ppm	The factory default correction value for the internal clock. The correction value will be used if the device is not in NTP mode. The correction value can be overwritten by the user.
12411	float	_TEMPERATUR_OFFSET	°C	Default=0. The temperature offset can be overwritten by the user.
13437	string	_RELEASE	16	Firmware-Release.

Address	Format	Designation	Unit	Remarks
Full Wave Effective Values				
14133	float	_ULN_QUICK[L1]	V	
14135	float	_ULN_QUICK[L2]	V	
14137	float	_ULN_QUICK[L3]	V	
14139	float	_ULN_QUICK[L4]	V	
14141	float	_ILN_QUICK[L1]	A	
14143	float	_ILN_QUICK[L2]	A	
14145	float	_ILN_QUICK[L3]	A	
14147	float	_ILN_QUICK[L4]	A	
14149	float	_PLN_QUICK[L1]	W	
14151	float	_PLN_QUICK[L2]	W	
14153	float	_PLN_QUICK[L3]	W	
14155	float	_PLN_QUICK[L4]	W	
Full Wave Effective Values for the Fundamental Oscillation Offset Reactive Power				
14157	float	_QLN_QUICK[L1]	VAr	
14159	float	_QLN_QUICK[L2]	VAr	
14161	float	_QLN_QUICK[L3]	VAr	
14163	float	_QLN_QUICK[L4]	VAr	
Access Configuration of Log 0 = Not Blocked (default), 1 = Blocked				
14179	short	_FORBID_HTML		HTML
14180	short	_FORBID_CFG_HTML		Configuration with HTML
14181	short	_FORBID_FTP		FTP
14182	short	_FORBID_CFG_FTP		Configuration with FTP
14183	short	_FORBID_MODETH		Modbus over Ethernet
14184	short	_FORBID_CFG_MODETH		Configuration with Modbus over Ethernet
14185	short	_FORBID_BACNET		BACnet
14186	short	_IP_UP		
14187	short	_SYSVAR_CNT		
14188	string	_SEQ_IP0 32		
14204	string	_SEQ_IP1 32		
14220	string	_SEQ_IP2 32		
14236	string	_SEQ_IP3 32		
14252	string	_SEQ_IP4 32		
14268	string	_SEQ_IP5 32		
14284	string	_SEQ_IP6 32		
14300	string	_SEQ_IP7 32		
14316	short	_CH_MAP[0]		
14317	short	_CH_MAP[1]		
14318	short	_CH_MAP[2]		
14319	short	_CH_MAP[3]		
14320	short	_CH_MAP[4]		
14321	short	_CH_MAP[5]		
14322	short	_CH_MAP[6]		
14323	short	_CH_MAP[7]		
14324	float	_NTP_DIV	s	
14326	float	_NTP_TURNAROUND	s	
14328	float	_NTP_KORR	ppm	
14330	long64	_RX_ETH_COUNT		
14334	long64	_TX_ETH_COUNT		
14338	long64	_ERR_ETH_COUNT		
14342	long64	_RX_NTP_COUNT		
14346	long64	_TX_NTP_COUNT		
14350	long64	_ERR_NTP_COUNT		
14354	long64	_RX_DNS_COUNT		
14358	long64	_TX_DNS_COUNT		



Address	Format	Designation	Unit	Remarks
14362	long64	_ERR_DNS_COUNT		
14366	long64	_RX_DHCP_COUNT		
14370	long64	_TX_DHCP_COUNT		
14374	long64	_ERR_DHCP_COUNT		
14378	long64	_TX_EMAIL_COUNT		
14382	long64	_ERR_EMAIL_COUNT		
14398	int	_MTU_SIZE		
14400	long64	_SYSTIMEUP	ms	Resolution=10ms
Energy tariff 3/4				
14404	float	_WH_V_T3[0]	Wh	Real energy, consumption, tariff 3, L1
14406	float	_WH_V_T3[1]	Wh	Real energy, consumption, tariff 3, L2
14408	float	_WH_V_T3[2]	Wh	Real energy, consumption, tariff 3, L3
14410	float	_WH_V_T3[3]	Wh	Real energy, consumption, tariff 3, L4
14412	float	_WH_V_T3[4]	Wh	Real energy, consump., tariff 3, L1..L3
14414	float	_WH_V_T3[5]	Wh	Real energy, consump., tariff 3, L1..L4
14416	float	_WH_V_T4[0]	Wh	Real energy, consumption, tariff 4, L1
14418	float	_WH_V_T4[1]	Wh	Real energy, consumption, tariff 4, L2
14420	float	_WH_V_T4[2]	Wh	Real energy, consumption, tariff 4, L3
14422	float	_WH_V_T4[3]	Wh	Real energy, consumption, tariff 4, L4
14424	float	_WH_V_T4[4]	Wh	Real energy, consump., tariff 4, L1..L3
14426	float	_WH_V_T4[5]	Wh	Real energy, consump., tariff 4, L1..L4
14428	float	_WH_Z_T3[0]	Wh	Real energy, supply, tariff 3, L1
14430	float	_WH_Z_T3[1]	Wh	Real energy, supply, tariff 3, L2
14432	float	_WH_Z_T3[2]	Wh	Real energy, supply, tariff 3, L3
14434	float	_WH_Z_T3[3]	Wh	Real energy, supply, tariff 3, L4
14436	float	_WH_Z_T3[4]	Wh	Real energy, supply, tariff 3, L1..L3
14438	float	_WH_Z_T3[5]	Wh	Real energy, supply, tariff 3, L1..L4
14440	float	_WH_Z_T4[0]	Wh	Real energy, supply, tariff 4, L1
14442	float	_WH_Z_T4[1]	Wh	Real energy, supply, tariff 4, L2
14444	float	_WH_Z_T4[2]	Wh	Real energy, supply, tariff 4, L3
14446	float	_WH_Z_T4[3]	Wh	Real energy, supply, tariff 4, L4
14448	float	_WH_Z_T4[4]	Wh	Real energy, supply, tariff 4, L1..L3
14450	float	_WH_Z_T4[5]	Wh	Real energy, supply, tariff 4, L1..L4
14452	float	_IQH_T3[0]	varh	Reactive energy, induktiv, tariff 3, L1
14454	float	_IQH_T3[1]	varh	Reactive energy, induktiv, tariff 3, L2
14456	float	_IQH_T3[2]	varh	Reactive energy, induktiv, tariff 3, L3
14458	float	_IQH_T3[3]	varh	Reactive energy, induktiv, tariff 3, L4
14460	float	_IQH_T3[4]	varh	Reactive energy, induktiv, tariff 3, L1..L3
14462	float	_IQH_T3[5]	varh	Reactive energy, induktiv, tariff 3, L1..L4
14464	float	_IQH_T4[0]	varh	Reactive energy, induktiv, tariff 4, L1
14466	float	_IQH_T4[1]	varh	Reactive energy, induktiv, tariff 4, L2
14468	float	_IQH_T4[2]	varh	Reactive energy, induktiv, tariff 4, L3
14470	float	_IQH_T4[3]	varh	Reactive energy, induktiv, tariff 4, L4
14472	float	_IQH_T4[4]	varh	Reactive energy, induktiv, tariff 4, L1..L3
14474	float	_IQH_T4[5]	varh	Reactive energy, induktiv, tariff 4, L1..L4
14936	short	_PULS_WIDTH	ms	Pulse width (1=10ms, 2=20ms...32000)
14937	int	_LCD_LED_MAX		Backlight, max. brightness
14939	int	_LCD_LED_MIN		Backlight, min. brightness
14941	int	_LCD_LED_ONTIME		Max/Min-brightness switchover time
14943	uint	_MB_STATUS	n	Metering range monitoring
14945	int	_SET_SYSTIME	sec	Time (UTC)
14963	ushort	_SMTP_PORT	n	SMTP-Port
14964	float	_IND_CAP_SUM3		Sign, Q1+Q2+Q3
14966	float	_IND_CAP_SUM		Sign, Q1+Q2+Q3+Q4